

FREE SUPPLEMENT CHRISTMAS PROJECTS

THE No.1 MAGAZINE FOR ELECTRONICS TECHNOLOGY & COMPUTER PROJECTS

EVERYDAY

DECEMBER 2000

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12v 18Ah SEALED LEAD ACID BATTERIES, new and boxed, unused pack of 4 £39.95 ref CYC7 or £15 each ref CYC6

AUTOMATIC CHARGER For the above batteries charges 2 at or re-charge level indicator circuitry 6 hour charge £10 ref CYC8

A new range of 12v to 240v INVERTERS

IV400S (400 watt) £89
IV800S (800 watt) £159
IV1200S (1200 watt) £219

ECG MACHINES 76v 10AH BATT/24V 8A TX EX govt. ECG machines! Measures 390x320x120mm, on the front are controls for scan speed, scan delay, scan mode, loads of connections on the rear including video out etc. On the front panel are two DIN sockets for connecting the body sensors to. Sensors not included. Inside 2 x 6V 10AH lead acid cells (not in good condition), PCB's and a 8A/24V toroidal transformer (mains in, sold as seen, may have one or two broken knobs etc due to poor storage. £15.99 ref VP2

SODIUM LAMP SYSTEMS £75.70 Complete system with 250w or 400 watt SON-T Agro bulb, reflector with bulb holder and remote ballast and starter (uncased) all you need is wire. 250W system ref SLS1, 400W system SLS2.

PC SUPPORT HANDBOOK The ultimate technical guide to building and maintaining PCs. Over 460 AA pages packed with technical data and diagrams just £10 ref PCBK. If you want 4 copies for £33 ref PCBK2. Also available is a CD packed with diagnostic programmes to use with the book £5 ref PCBK1

D SIZE NICADS Tagged, 1200mA, 1.2v pack of 4 for £6 ref CYC9 or as a pack of 24 for £22 ref CYC10

D SIZE SEALED LEAD ACID BATTERIES

2v 2.5Ah rechargeable sealed lead acid battery made by Cyclon 60x45mm (standard D size) supplied as a pack of 12 or 20 giving you options for battery configurations eg 12v at 5Ah 24v at 2.5Ah 6v at 10Ah. These batteries are particularly useful in that you can arrange them in your project to optimise space etc (eg boat ballast etc) Pack of 12 £10 ref CYC4, pack of 20 £16 ref CYC5

HYDROPONICS DO YOU GROW YOUR OWN?

We have a full colour hydroponics catalogue available containing nutrients, pumps, fittings, environmental control light fittings plants, test equipment etc Ring for your free copy.

PC COMBINED UPS AND PSU The unit has a total power of 292 watts, standard mother board connectors and 12 peripheral power leads for drives etc. Inside is 3 12v 7Ah sealed lead acid batteries. Backup time is 8 mins at full load or 30 mins at half load. Made in the UK by Magnum, 110 or 240vac input, +5v at 35A, -5v at 5A, +12v at 9A, -12v at 5A outputs. 170x260x220mm, new and boxed £29.95 ref PCUPS2

ALTERNATIVE ENERGY CD, PACKED WITH HUNDREDS OF ALTERNATIVE ENERGY RELATED ARTICLES, PLANS AND INFORMATION ETC £14.50 REF CD56

AERIAL PHOTOGRAPHY KIT This rocket comes with a built in camera! It flies up to 500 feet (150 m) turns over, and takes an aerial photograph of the ground below. The rocket then returns with its film via its parachute. Takes 110 film. Supplied complete with everything including a launch pad and 3 motors (no film) £29.98 ref astro

PROJECT BOXES Another bargain for you are these smart ABS project boxes, smart two piece screw together case measuring approx 6"x5"x2" complete with panel mounted LED. Inside you will find loads of free bits, tape heads, motors, chips resistors, transistors etc. Pack of 20 £19.95 ref MD2

TELEPHONES Just in this week is a huge delivery of telephones, all brand new and boxed. Two piece construction - illuminated keypad tone or pulse (switchable) recall redial and pause high/low and off ringer switch and quality construction. Off white colour and is supplied with a standard international lead (same as US or moderns!) if you wish to have a BT lead supplied to convert the phones these are £1.55 each ref BTLX. **Phones £4.99 each ref PH210 off £30 ref SS2**

3HP MAINS MOTORS Single phase 240v, brand new 2 pole 340x180mm 2850 rpm, built in automatic reset overload protector, keyed shaft (40x16mm) Made by Leeson £99 each ref LEE1

BUILD YOUR OWN WINDFARM FROM SCRAP New publication gives step by step guide to building wind generators and propellers. Armed with this publication and a good local scrap yard could make you self sufficient in electricity! £12 ref LOT81

CHIEFTAN TANK DOUBLE LASERS 9 WATT + 3 WATT + LASER OPTICS Could be adapted for laser listener long range comms etc. Double beam units designed to fit in the barrel of a tank, each unit has 2 semiconductor lasers and motor drive units for alignment 7 mile range, no circuit diagrams due to MOD, new price £50 000? us? £199. Each unit has two gallium Arsenide injector lasers, 1 x 9 watt 1 x 3 watt 900nm wavelength, 28vdc, 600hz pulse freq. The units also contain a receiver to detect reflected signals from targets. £99 ref LOT4

MAGNETIC CREDIT CARD READERS AND ENCODING MANUAL £9.95 Cased with flyleads designed to read standard credit cards! complete with control electronics PCB and manual covering everything you could want to know about what's hidden in that magnetic strip on your card! just £9.95 ref BAR31

SOLAR POWER LAB SPECIAL 2x 6"x6" 6v 130mA cells 4LED's wire buzzer switch + relay motor £7.99 REF SA27

SOLAR NICAD CHARGERS 4x AA size £9.99 ref 6P476

2 x C size £9.99 ref 6P477

BRAND NEW MILITARY ISSUE DOSE METERS

Current NATO issue Standard emergency services unit Used by most of the worlds Military personnel New and boxed Normal retail price £400. **BULLS** bargain price just £99. The PDRM 82 M is a portable lightweight water resistant gamma radiation survey meter to measure radiological dose rate in the range 0.1 to 300 centigrays per hour in air. The Geiger Muller (G M) tube detecting unit is energy and polar response corrected. The radiation level is displayed on a Liquid Crystal Display. The microcomputer corrects for the non-linearity of the G M tube response. The instrument is powered by three international C size batteries giving typically 400 hours operation in normal conditions. The dose rate meter PDRM 82M designed and selected for the United Kingdom Government, has been fully evaluated to satisfy a wide range of environmental conditions and is nuclear hard. The construction enables the instrument to be easily decontaminated. The instrument is designed for radiation surveys for post incident monitoring. Used in a mobile role, either carried by troops or in military vehicles for rapid deployment enabling radiation hot spots to be quickly located. Range 0 - 300 cGy/h in 0.1 cGy/h increments. Over range to 1500 cGy/h - indicates flashing 300. Accuracy 20% of true dose rate +0.1 cGy/h, 0 - 100 cGy/h 130% of true dose rate, 100 - 300 cGy/h Energy Response 0.3 MeV to 3 MeV - within 120% (Ra 226) 80 KeV to 300 KeV - within 140% (Ra 226) Detector Energy compensated Halogen quenched Geiger Muller Tube. Controls Combined battery access and ON/OFF switch. Batteries 3 International standard C cells. Weight 560 grams Operating Temperature Range -30deg C to +60 deg C indicators High contrast 4 digit LCD. Battery low indication Dose rate Rising/Falling £99 ref PDRM

Hydrogen fuel cells Our new Hydrogen fuel cells are 1v at up to 1A output, Hydrogen input, easily driven from a small electrocatalysis assembly or from a hydrogen source, our demo model uses a solar panel with the output leads in a glass of salt water to produce the hydrogen! Each cell is designed to be completely taken apart, put back together and expanded to what ever capacity you like, (up to 10watts and 12v per assembly. Cells cost £49 ref HFC11

PHILIPS VP406 LASER DISC PLAYERS, SCART OUTPUT, JUST PUT YOUR VIDEO DISK IN AND PRESS PLAY, STANDARD AUDIO AND VIDEO OUTPUTS, £14.95 REF VP406

SMOKE ALARMS Mains powered made by the famous Gent company, easy fit next to light fittings, power point. Pack of 5 £15 ref SS23 pack of 12 £24 ref SS24

4AH D SIZE NICADS pack of 4 £10 ref 4AHPK SENDER KIT Contains all components to build a A/V transmitter complete with case £35 ref VSXX2

10 WATT SOLAR PANEL Amorphous silicon panel fitted in a anodized aluminium frame. Panel measures 3' by 1' with screw terminals for easy connection. 3' x 1' solar panel £56 ref MAG46

12V SOLAR POWERED WATER PUMP Perfect for many 12v DC uses, from solar fountains to hydroponics! Small and compact yet powerful works direct from our 10 watt solar panel in bright sun. Max hd 17 ft Max flow = 8 l.p.m 1.5A Ref AC8 £18.99

SOLAR ENERGY BANK KIT 50x 6"x12" 6v solar panels (amorphous) +50 diodes £99 ref EF112 PINHOLE CAMERA MODULE WITH AUDIO!

Superb board camera with on board sound! extra small just 28mm square (including microphone) ideal for covert surveillance. Can be hidden inside anything, even a matchbox! Complete with 15 metre cable, psu and tv/r connectors. £49.95 ref CC6J

SOLAR MOTORS Tiny motors which run quite happily on voltages from 3-12vdc. Works on our 6v amorphous 6" panels and you can run them from the sun! 32mm dia 20mm thick £1.50 each **WALKIE TALKIES 1 MILE RANGE £37/PAIR REF MAG30**

LIQUID CRYSTAL DISPLAY Bargain prices, 40 character 1 line 154x16mm £6.00 ref SMC4011A **YOUR HOME COULD BE SELF SUFFICIENT IN ELECTRICITY** Comprehensive plans with loads of info on designing systems, panels, control electronics, etc £7 ref PV1

AUTO SUNCHARGER 155x300mm solar panel with diode and 3 metre lead and cigar plug 12v 2w £12.99 REF AUG10P3

SOLAR POWER LAB SPECIAL 2x 6"x6" 6v 130mA cells 4LED's wire buzzer switch + relay motor £7.99 REF SA27

SOLAR NICAD CHARGERS 4 x AA size £9.99 ref 6P476 2 x C size £9.99 ref 6P477

MINATURE TOGGLE SWITCHES These top quality Japanese panel mount toggle switches measure 35x13x12mm, are 2 pole changeover and will switch 1A at 250vac, or 3 A at 125vac. Complete with mounting washers and nuts. Supplied as a box of 100 switches for £29.95 ref SWT35 or a bag of 15 for £4.99 ref SWT34

VOICE CHANGERS Hold one of these units over your phone mouth piece and you can adjust your voice using the controls on the unit! Battery operated £15 ref CC3

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30 WATTS OF SOLAR POWER for just £69, 4 panels each one 3'x1' and producing 8w, 13v. PACK OF FOUR £69 ref SOLX

200 WATT INVERTERS plugs straight into your car cigarette lighter socket and is fitted with a 13A socket so you can run your mains operated devices from your car battery. £49.95 ref SS66

THE TRUTH MACHINE Tells if someone is lying by micro tremors in their voice, battery operated, works in general conversation and on the phone and TV as well! £42.49 ref TD3

INFRA RED FILM 6" square piece of flexible infra red film that will only allow IR light through. Perfect for converting ordinary torches, lights, headlights etc to infra red output only using standard light bulbs. Easily cut to shape. 6" square £15 ref IRF2

33 KILO LIFT MAGNET Neodymium 32mm diameter with a fixing bolt on the back for easy mounting. Each magnet will lift 33 kilos. 4 magnets bolted to a plate will lift an incredible 132 kilos! £15 ref MAG33 Pack of 4 just £39 ref MAG33AA

HYDROGEN FUEL CELL PLANS Loads of information on hydrogen storage and production. Practical plans to build a Hydrogen fuel cell (good workshop facilities required) £8 set ref FCP1

STIRLING ENGINE PLANS Interesting information pack covering all aspects of Stirling engines, pictures of home made engines made from an aerosol can running on a candle! £12 ref STIR2

ENERGY SAVER PLUGS Saves up to 15% electricity when used with fridges motors up to 2A, light bulbs, soldering irons etc. £9 ea ref LOT71, 10 pack £69 ref LOT72

12V OPERATED SMOKE BOMBS Type 3 is a 12v trigger and 3 smoke canisters. Each canister will fill a room in a very short space of time! £14.99 ref SB3. Type 2 is 20 smaller canisters (suitable for mock equipment fires etc) and 1 trigger module for £29 ref SB2. Type 1 is a 12v trigger and 20 large canisters. £49 ref SB1

HIPOWER ZENON VARIABLE STROBES Useful 12v PCB fitted with hi power strobe tube and control electronics and speed control potentiometer. Perfect for interesting projects. etc 70x55mm 12vdc operation. £6 ea ref FLS1, pack of 10 £49 ref FLS2

NEW LASER POINTERS 4.5mw 75 metre range hand held unit runs on two AA batteries (supplied) 670nm £29 ref DEC49J

HOW TO PRODUCE 35 BOTTLES OF WHISKY FROM A SACK OF POTATOES Comprehensive 270 page book covers all aspects of spirit production from everyday materials. Includes construction details of simple stills. £12 ref MS3

NEW HIGH POWER MINI BUG With a range of up to 800 metres and a 3 days use from a PP3 this is our top selling bug! less than 1" square and a 10m voice pickup range. £28 ref LOT102

IR LAMP KIT Suitable for CCTV cameras, enables the camera to be used in total darkness! £6 ref EF138

INFRA RED POWER BEAM Hand held battery powered lamp, 4 inch reflector, gives out powerful pure infrared light! perfect for CCTV use, night sights etc. £29 ref PB1

SUPER WIDEBAND RADAR DETECTOR Detects both radar and laser. X K and KA bands, speed cameras, and all known speed detection systems. 360 degree coverage, front & rear waveguides, 1.1"x2.7"x4.6" fits on visor or dash. £149

LOPTX Made by Samsung for colour TV. £3 each ref SS52

LAPTOP LCD SCREENS 240x175mm £12 ref SS51

WANT TO MAKE SOME MONEY? STUCK FOR AN IDEA? We have collated 140 business manuals that give you information on setting up different businesses, you peruse these at your leisure using the text editor on your PC. Also included is the certificate enabling you to reproduce (and sell!) the manuals as much as you like! £14 ref EP74

ELECTRONIC SPEED CONTROLLER KIT For the above motor is £19 ref MAG17. Save £5 if you buy them both together. 1 motor plus speed controller rrp is £41, offer price £36 ref MOT5A

INFRA RED REMOTE CONTROLS made for TVs but may have other uses pack of 100 £39 ref IREM

RCB UNITS In line IEC lead with fitted RC breaker. Installed in seconds. Pack of 3 £9.98 ref LOT5A

On our web sites you can

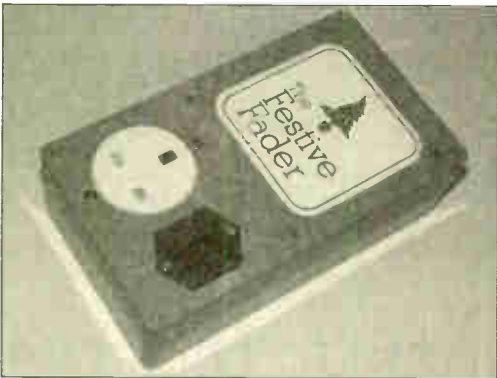
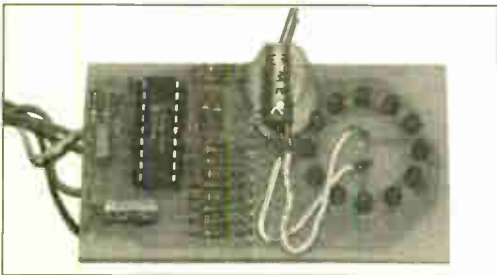
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AUTO SUNCHARGER 155x300mm solar panel with diode and 3 metre lead and cigar plug 12v 2w £12.99 REF AUG10P3

STEPPER MOTORS Brand new stepper motors, 4mm fixing holes with 47.14mm fixing centres, 20mm shaft, 6.35mm diameter, 5v/phase, 0.7A/phase, 1.8 deg step (200 step). Body 56x36mm £14.99 ea ref STEP6, pack of 4 for £49.95. PIC based variable speed controller kit £15 ref STEP7



NOTE NEW PUBLISHING DATE
 January issue on sale
 Thursday December 14

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Our January 2001 issue will be published on Thursday, 14 December 2000. See page 875 for details

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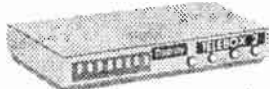
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TELEBOX ST for composite video input type monitors £36.95
TELEBOX STL as ST but fitted with integral speaker £39.50
TELEBOX MB Multiband VHF/UHF/Cable/Hyperband tuner £69.95
For overseas PAL versions state 5.5 or 6 MHz sound specification.
*For cable / hyperband signal reception Telebox MB should be connected to a cable type service. Shipping on all Telebox's code (B)

NEW State of the art PAL (UK spec) UHF TV tuner module with composite 1V pp video and NICAM hi fi stereo sound outputs. Micro electronics all on one small PCB only 73 x 160 x 52 mm enable full tuning control via a simple 3 wire link to an IBM pc type computer. Supplied complete with simple working program and documentation. Requires +12V & +5V DC to operate.
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See www.distel.co.uk/data_my00.htm for picture + full details

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- 3 1/2" Mitsubishi MF355C-D 1.4 Meg. Non laptop £18.95(B)
- 5 1/4" Teac FD-55GR 1.2 Meg (for IBM pc's) RFE £18.95(B)
- 5 1/4" Teac FD-55F-03U 720K 40/80 (for BBC's etc) RFE £29.95(B)
- 5 1/4" BRAND NEW Mitsubishi MF501B 360K £22.95(B)
- Table top case with integral PSU for HH 5 1/4" Floppy / HD £29.95(B)
- 8" Shugart 800/801 8" SS refurbished & tested £210.00(E)
- 8" Shugart 810 8" SS HH Brand New £195.00(E)
- 8" Shugart 851 8" double sided refurbished & tested £260.00(E)
- 8" Mitsubishi M2894-63 double sided NEW £295.00(E)
- 8" Mitsubishi M2894-63-02U DS slimline NEW £295.00(E)
- Dual 8" cased drives with integral power supply 2 Mb £499.00(E)

HARD DISK DRIVES 2 1/2" - 14"

- 2 1/2" TOSHIBA MK1002MAV 1.1Gb laptop (12.5 mm H) New £79.95
 - 2 1/2" TOSHIBA MK2101MAN 2.16 Gb laptop (19 mm H) New £89.50
 - 2 1/2" TOSHIBA MK4309MAT 4.3Gb laptop (8.2 mm H) New £105.00
 - 2 1/2" TOSHIBA MK409MAV 6.1Gb laptop (12.7 mm H) New £190.00
 - 2 1/2" to 3 1/2" conversion kit for PCs, complete with connectors £14.95
 - 3 1/2" FUJII FK-309-26 20Mb MF/ RFE £59.95
 - 3 1/2" CONNER CP3024 20 Mb IDE /RFE (or equiv) RFE £59.95
 - 3 1/2" CONNER CP3044 40 Mb IDE /RFE (or equiv) RFE £69.00
 - 3 1/2" QUANTUM 40S Prodr/ve 42Mb SCSI /RFE, New RFE £49.00
 - 5 1/4" MINISCRIBE 3425 20Mb MF/ RFE (or equiv) RFE £49.95
 - 5 1/4" SEAGATE ST-238R 30 Mb RLL /RFE Refurb £69.95
 - 5 1/4" CDC 94205-51 40Mb HH MF/ RFE RFE tested £69.95
 - 5 1/4" HP 97548 850 Mb SCSI RFE tested £99.00
 - 5 1/4" HP C3010 2 Gbyte SCSI differential RFE tested £195.00
 - 8" NEC D2246 85 Mb SMD interface. New £199.00
 - 8" FUJITSU M2322K 160Mb SMD /RFE RFE tested £195.00
 - 8" FUJITSU M2329K 2 Gb SMD /RFE RFE tested £345.00
- Many other drives in stock - Shipping on all drives is code (C1)

TEST EQUIPMENT & SPECIAL INTEREST ITEMS

- 3 1/2" MIT. FA3445ETKL 14" Industrial spec SVGA monitors £245
- FARNELL 0-60V DC @ 50 Amps, bench Power Supply £995
- FARNELL AP3080 0-30V DC @ 80 Amps, bench Supply £1850
- 1KW to 400 kW - 400 Hz 3 phase power sources - ex stock EPOA
- IBM 8230 Type 1, Token ring base unit driver £760
- Wayne Kerr RA200 Audio frequency response analyser £2500
- IBM 53F5501 Token Ring ICS 20 port lobe modules £750
- IBM MAU Token ring distribution panel 8228-23-5050N £95
- AIM 501 Low distortion Oscillator 9Hz to 30KHz, IEE £250
- ALLGON 8360.1 1805-1880 MHz hybrid power combiners £250
- Trend DSA 274 Data Analyser with G703(2M) 64 v/c EPOA
- Marconi 6313 Programmable 2 to 22 GHz sweep generator £6500
- Marconi 2022C 10KHz-1GHz RF signal generator £1550
- Marconi 2030 opt 03 10KHz-1.3 GHz signal generator, New £4995
- HP1650B Logic Analyser £3750
- HP3781A Pattern generator & HP3782A Error Detector EPOA
- HP6621A Dual Programmable GPIB PSU 0-7 V 160 watts £1800
- HP6264 Rack mount variable 0-20V @ 20A metered PSU £675
- HP54121A DC to 22 GHz four channel test set EPOA
- HP8130A opt 020 300 MHz pulse generator, GPIB etc £7900
- HP A1, A0 8 pen HPGL high speed drum plotters from HP DRAFTMASTER 1 8 pen high speed plotter £550
- EG+G Brookdeal 95035C Precision lock in amp £1800
- View Eng. Mod 1200 computerised inspection system EPOA
- Sony DXC-3000A High quality CCD colour TV camera £995
- Keithley 590 CV capacitor / voltage analyser £3750
- Racal ICR40 dual 40 channel video recorder system £3750
- Fiskers 45KVA 3 ph On Line UPS - Now batteries £9500
- Emerson AP130 2.5KVA industrial spec UPS £2100
- Mann Tally MT645 High speed line printer £2200
- Intel SBC 486/133SE Multibus 486 system 8Mb Ram £945
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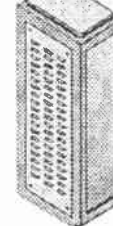
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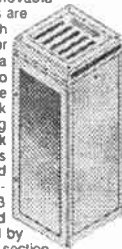
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NEXT MONTH

UFO DETECTOR AND EVENT RECORDER

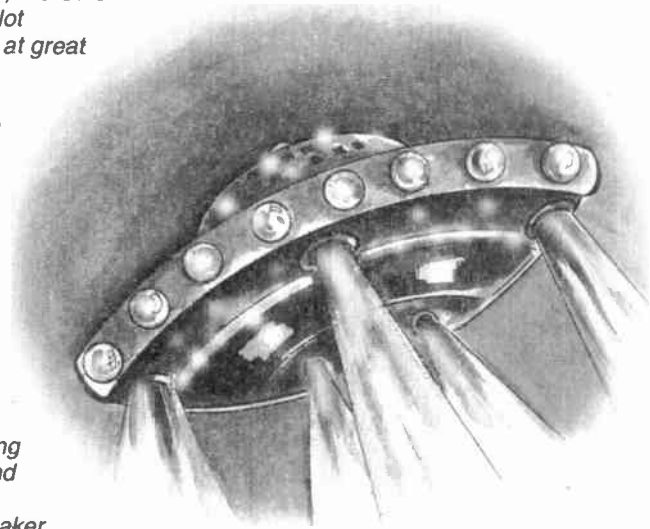
Although some ancient texts are said to contain references to spacecraft, the UFO enigma really began on the afternoon of 24 June, 1947, when aircraft pilot Kenneth Arnold reported nine crescent-shaped objects crossing the sky at great speed near Mount Rainier in the State of Washington, USA. Since then there have been countless sightings, world-wide, and private and government organisations have been set up to investigate and report on the phenomena.

And there's been no shortage of encounters to fill the researchers' files. Whilst many incidents have been shown to have a terrestrial origin, there remains a solid core of cases where inexplicable phenomena and reliable witnesses combine to challenge our disbelief.

One thing running like a thread through many of the reports is the powerful magnetic disturbance which accompanies the craft. Car and aircraft ignition systems falter or fail (presumably the ignition coil core becomes saturated), and dashboard and navigation instruments behave erratically.

As recently as 30 March this year, a family travelling along the Klondike Highway in Canada claim to have observed a saucer-shaped UFO closing in on their car. Headlights dimmed, the tape recorder stopped playing and battery operated watches malfunctioned.

The equipment to be described next month will detect and record far weaker magnetic perturbations than these. Stand-by current is extremely low, and the battery powered units can be operated economically in remote locations. Go out and find your alien!



A TWO-WAY INTERCOM

Intercom projects used to be part of the staple diet of electronic construction enthusiasts with at least one appearing somewhere every year. Over time they seem to have become less common, perhaps because they can be bought quite cheaply nowadays, so when a reader asked if EPE had recently published one, editorial eyebrows rose at the discovery that some eight years had passed since the last appeared. It seemed timely, therefore, to present a new intercom design.

It might be asked why anyone would build an intercom when they can be bought quite cheaply. In fact there are several reasons. A home-built design can be customised, built into other projects, modified and used in ways its original design never intended. Parts of the circuit might be adapted for use in other projects. The constructor can easily repair it if it goes wrong and an intercom is a good starter project for those seeking electronic experience. Last, but by no means least, constructors with children will probably find that an intercom's entertainment potential will earn them lots of brownie points with the kids! Given all this, a new design seems well worthwhile.

VERSATILE OPTICAL TRIGGER

This is a circuit that is flexible enough to cater for many different applications. In its basic form, the Versatile Optical Trigger switches a load on or off, depending on the amount of light falling on its sensor. It can be set to respond in reasonably bright conditions or in dim light. It can be adapted to work either way round, switching on when the light gets brighter, or when it becomes dimmer. Applications for the basic circuit include switching on a porch lamp at dusk, briefly sounding a buzzer when someone's shadow falls on the sensor (or when the cat leaves the house by the cat door), or to switch on a lamp in a cupboard when the door is opened. We leave it to the imagination of the reader to find other interesting things to do with this circuit.

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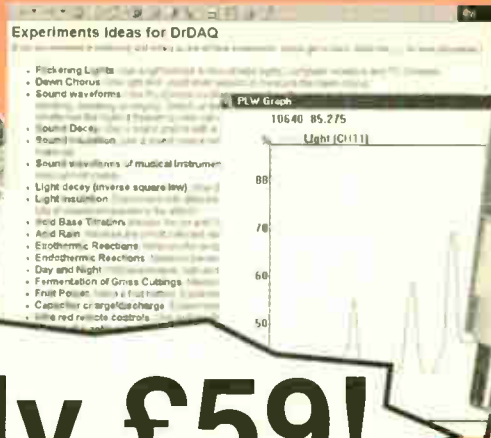


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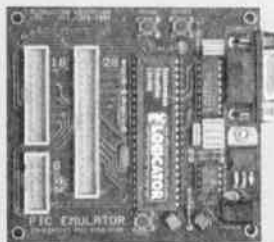
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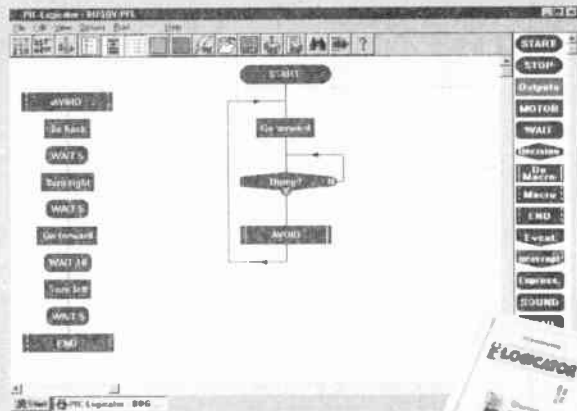
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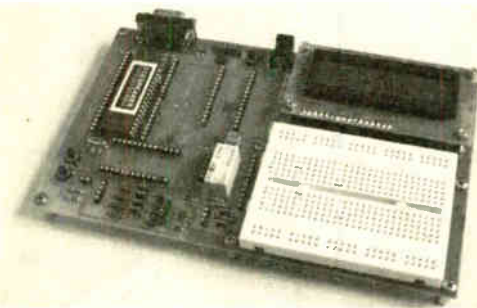
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VOL. 29 No. 12 DECEMBER 2000

BUMPER

A bumper issue this month with the extra 16 pages devoted to Christmas projects. We have a regular problem with trying to fit everything into each issue, and I try to balance the content to appeal to a wide range of readers, with regular series like *Teach-In 2000* (Nov 1999 to Oct 2000) and now *The Schmitt Trigger*, plus a range of constructional projects and the regular features it's always a bit of a tight squeeze and sometimes it's difficult to know what to leave out. Only occasionally can we afford to go "over the top" with the number of pages to bring you extra content, we are, however, planning a couple more bumper issues for the Spring.

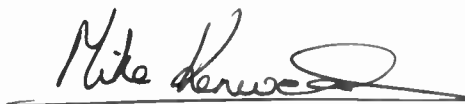
Incidentally, our educational series are always very popular and we are presently working at putting *Teach-In 2000* on a mini CD-ROM. The complete course, together with all the software, should soon be available in this form, hopefully there will be more news on this in the next issue.

DESIGN

We are often asked by readers how to design circuits and sometimes more specifically to tell them how we arrived at the values of each component in a particular circuit design – not something we can offer to provide, I'm afraid.

As an insight to the variations and complexity of circuit design the present *Schmitt Trigger* series should be an eye-opener for many readers. One *EPE* contributor has already commented that he did not realise there was so much to say about Schmitt Triggers – and that was after Part 1! This series is a little above the general level of theory we normally carry in *EPE*, but should interest those of you who are above the beginner level and who want to understand more about circuit design.

We can't promise to tell you everything there is to know about Schmitt Triggers, but you will certainly learn a lot.



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PIC-MONITORED DUAL PSU



JOHN BECKER Part One

Ever keen to add tools to the workshop, the author designs yet another, and finds more uses for a PIC16F877!

THE dual power supply unit (PSU) described here can be built in several forms.

At the simplest level it can be built with a single d.c. output switched for 5V or variable between about 6V and 9V. This shortened version is probably an ideal starter power supply for those who have been following the recent *Teach-In 2000* series and now wish to start adding workshop equipment.

This version will be described in Part 2, as will other constructional options. Some aspects of the main PSU have also been described in such a way as to reinforce the understanding of power supplies by *Teach-In 2000* readers.

It is emphasised that mains a.c. electrical power is dangerous and that construction of any of the versions of this power supply should only be undertaken (or supervised) by those who are suitably qualified or experienced.

FULL VERSION

The full version of the dual power supply provides PIC microcontroller monitoring of voltage and current, displaying the data on a liquid crystal display (l.c.d.). It has the specifications shown opposite.

Specifications

● **Dual channel**, switchable for series or parallel operation:

Two outputs per channel (four outputs total).

Output 1 switchable for fixed voltages of 5V, 6V, 9V, 12V, 15V or 18V.

Output 2 fully variable from about 0V up to 1V less than the switch-selected fixed voltage.

In series connection mode, the common rail of Channel B is connected to the selected fixed voltage of Channel A, providing a maximum output of +18V from Channel A and +36V from Channel B, or -18V from Channel A and +18V from Channel B.

All outputs are "floating" with respect to mains earth (ground) and any output can be regarded as the 0V (common) level.

● **Output monitoring:**

PIC16F877 microcontroller simultaneously monitors voltage and current for both outputs of both channels (four outputs).

Monitored data is output to a 2-line 16-character (per line) alphanumeric l.c.d.

The PIC controls l.e.d.s and buzzer in response to preset current limits being exceeded.

● **Display modes:**

1. Each channel's data shown individually, stating output voltage, output current, preset alarm-trip current. Channels switch-selectable on a cycle of four.

2. All four monitored voltages shown simultaneously.

3. All four monitored currents shown simultaneously.

● **Maximum output currents:**

Output 1 (switched voltage), 1A but see text and Table 6 later.

Output 2 (variable voltage), 350mA but see text in Part 2.

● **Current limiting:**

Output 2 can be set to limit the power supplied to the load circuit, using a panel control.



Each of the four outputs can have a maximum current limit set via pushbutton switches. If the preset current is exceeded, a light emitting diode (l.e.d.) indicates which channel is overloaded. A buzzer sounds if the total current drawn from either channel exceeds 1A. Physical limitation of the current supplied is *not* controlled by this option.

The PIC's EEPROM data memory retains the limit value set even when the power supply unit is switched off.

The basic block diagram for the power supply is shown in Fig.1. All controls are omitted except for the Series/Parallel switch.

TRANSFORMER

Illustrated in Fig.2 is the circuit diagram for the mains a.c. input and transformer. For use in the UK, transformer T1 should have the primary winding rated for 230V a.c. For the USA, the primary winding should be rated for 110V a.c. Readers from other countries should select the primary

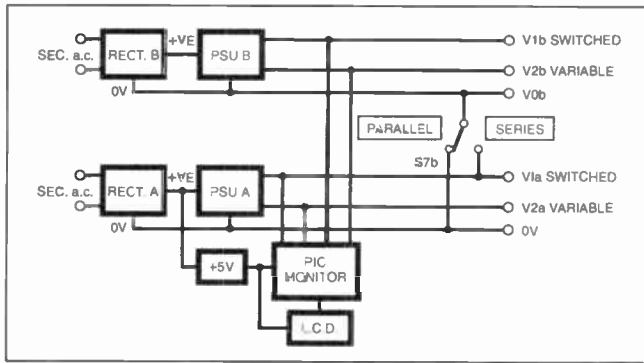


Fig.1. Block diagram for the full dual power supply.

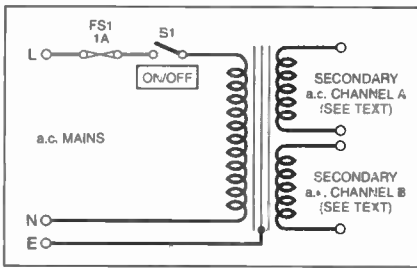


Fig.2. Mains transformer circuit diagram.

winding voltage to suit their domestic mains supply.

The choice of secondary winding voltage and current ratings is somewhat up to the user. In the author's prototype described here, each is rated at 15V a.c. 1.67A (25VA per winding). Lower voltage and current types may be selected instead, but with an accompanying reduction in the power supply's capabilities.

It should be remembered that the bridge-rectified d.c. output voltage is approximately 1.414 times the a.c. voltage supplied by the transformer secondary, minus 1.4V for the bridge rectifier voltage drop, and that the voltage regulator requires a minimum voltage drop across it of about 2V d.c.

Thus, for example, the 15V a.c. secondary of the prototype is rectified to produce approximately 20V d.c. at the input to the regulator. In practice, the rectified voltage is likely to be somewhat higher than this when the power supply is not

connected to an external load circuit (approx 22.3V on the prototype).

BASIC REGULATION

The circuit diagram of the power supply as a basic concept for a single channel is shown in Fig.3. It is this circuit which will be returned to when construction of the simple power supply is described in Part 2. For the dual supply, two modified versions of this circuit are used.

The bridge rectifier is shown as REC1, with capacitors C1 and C2 providing the initial smoothing to eliminate the ripple voltage from the rectifier. As said earlier, the voltage at this node is likely to be about 20V minimum.

In *Teach-In 2000* it was stated that the working voltage for the smoothing capacitor (C1) should ideally be twice that of the rectified output voltage, to provide a reasonable safety margin in the event of power line spikes or surges.

For the sake of expediency, however, in the prototype a capacitor rated at 35V d.c. is used (the author had them in stock). However, the printed circuit board has been designed to accept the physical size of capacitors rated at greater than 35V should you prefer to use them.

The bridge rectifier used is rated at 50V 1A and is a commonly available device, type W005. It should not be called upon to supply a current greater than the rated 1A.

This factor limits the actual current which can be drawn from each power supply channel, a value which must take into account the current drawn by the

controlling and monitoring circuit. For Channel A, allow for an "internal" current of about 50mA maximum should all the monitoring i.e.d.s and the buzzer be on together.

The voltage regulation circuit comprises regulator IC1, op.amp IC2a, potentiometer VR1, resistor R1 and switch S2.

WELL ESTABLISHED

This circuit is based on the "industry standard" that has been around for several decades, as shown in National Semiconductor's linear device data book, for example.

The regulator, a +5V device, tries to maintain a difference of 5V between its output and common pins. If the voltage to which the common pin is connected is 0V, then the output will, of course, be 5V. However, if the voltage at the common pin is raised, then the output voltage will rise similarly, to maintain the 5V differential.

To achieve a practical variable output voltage an op.amp is used as a unity gain buffer whose non-inverting input is fed with a variable voltage, as supplied by potentiometer VR1. The buffer's output thus sets the voltage supplied to IC1's common pin. Resistor R1 provides a minimum current flow from the common pin to 0V.

Because VR1 is supplied by the output of IC1, the circuit configuration maintains the desired 5V output/common differential, and the final output voltage of the circuit is held regulated at the value set by VR1.

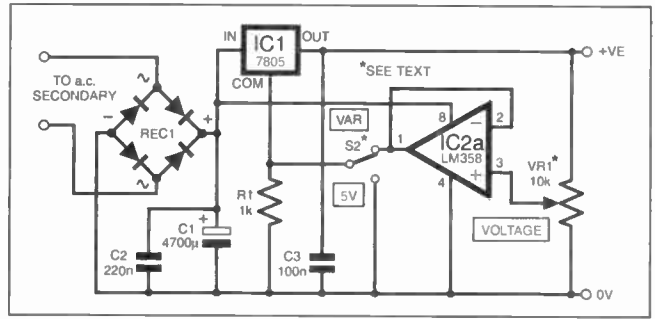


Fig.3. Circuit diagram for one channel of the power supply as a simple concept.

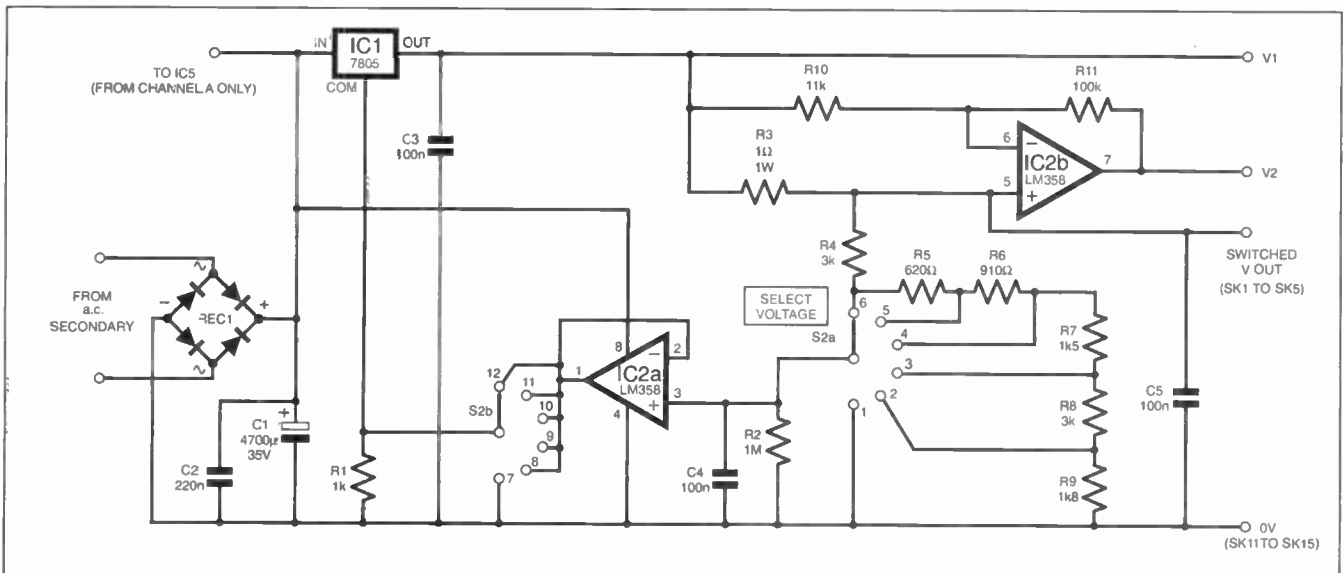


Fig.4. Circuit diagram of a single channel as used in the full power supply discussed. It is an expanded variant of that in Fig.3.

It should be noted, though, that the output of the op.amp (a type LM358) can never fall to 0V. More typically the minimum output voltage available will be about 0.5V. Consequently, the minimum regulated voltage that can be set by VR1 will be about 5.5V. The maximum voltage will about 2V below the rectified voltage fed into the input of regulator IC1.

In Fig.3, switch S2 selects whether IC1's common pin is connected to 0V (for fixed 5V output) or to the op.amp's output (for variable voltage control).

SWITCHED SUPPLY

For the full power supply design, the circuit of Fig.3 is expanded to become that in Fig.4.

Here the single potentiometer of Fig.4 has been replaced by a chain of six resistors, providing a tapped potential divider whose nodes are selected by rotary switch S2a. The reason for the inclusion of resistor R3 will be stated presently.

Capacitor C4 and resistor R2 are included to minimise voltage surges when the voltage range is switched. A smoothing capacitor is NOT connected between the op.amp's output and the 0V line since it was found that this could cause oscillation in the regulated supply.

Switch S2b replaces S2 of Fig.3, selecting between 5V and the preset output voltage from the buffering op.amp.

POTENTIAL CALCULATIONS

When considering the design of this power supply, the author originally believed that the tapped controlling voltages fed to the op.amp would need to be provided via individual preset potentiometers, each set for a different bias voltage, 5V below the required output from the regulator. The first constructed model actually used presets.

Initial calculations for a fixed multi-node potential divider had showed that the required resistors would have unusual values. The calculations were based on a total resistance across the divider of 10kΩ (as used for the basic potentiometer control).

As an example, and referring to Fig.5, consider the situation for $V_{out} = 6V$:

$$R_{total} = R_x + R_y = 10k\Omega$$

$$V_{out} = 6V$$

$$V_{bias} = V_{out} - 5V = 1V$$

(5V is the voltage differential between IC1's output and common pins)

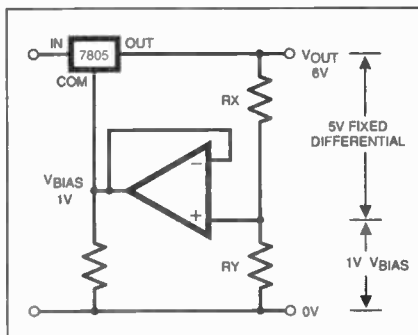


Fig.5. Potential divider use to control the voltage output from regulator IC1 at 6V.

Using the potential divider formula of:

$$V_{bias} = V_{out} \times (R_y / R_{total})$$

the equation can be stated as:

$$1V = 6V \times (R_y / 10k)$$

Making R_y the subject produces the equation:

$$R_y = (1V / 6V) \times 10k = 1.666667k$$

Thus R_x becomes:

$$R_{total} - R_y = 10k - 1.666667k = 8.333333k$$

Table 1 shows the individual resistor values in the divider chain of Fig.6 calculated for all five required output voltages. The calculations were produced by the QBasic program in Listing 1, in which R_{total} is the total resistance set at 10 (the "k" factor being omitted).

Table 1

IDEAL VALUES for $R_{total} = 10k$

Vout	Vbias	Resistor
6V	1V	R1 1.666667k
9V	4V	R2 2.777778k
12V	7V	R3 1.388889k
15V	10V	R4 .8333332k
18V	13V	R5 .5555557k
		$R_6 = R_{total} - (R_1 + R_2 + R_3 + R_4 + R_5)$ $= 2.777778k$

The calculations were originally done by hand without the computer program. However, in an idle moment some weeks after building the power supply, the author gave the problem to the computer, using Listing 1. Calculations were made for several different values of R_{total} . A value for R_{total} of 11(k) produced the results shown in Table 2.

Table 2

IDEAL VALUES for $R_{total} = 11k$

Vout	Vbias	Resistor
6V	1V	R1 1.833333k
9V	4V	R2 3.055556k
12V	7V	R3 1.527778k
15V	10V	R4 .9166668k
18V	13V	R5 .6111109k
		$R_6 = R_{total} - (R_1 + R_2 + R_3 + R_4 + R_5)$ $= 3.055555k$

Since these values appeared to be close to the available E24 series values, a second program was written (Listing 2). In the program, the calculated output voltages were derived for a divider chain comprised of E24 values nearest to those in Table 2, i.e. 1k8, 3k, 1k5, 910Ω, 620Ω, 3k. The results are shown in Table 3.

Table 3

E24	Values	Vout
R1	1k8	5.996678V
R2	3k	8.9801V
R3	1k5	11.95364V
R4	910Ω	14.95856V
R5	620Ω	18.05V
R6	3k	
		$R_{total} = 10.83k$

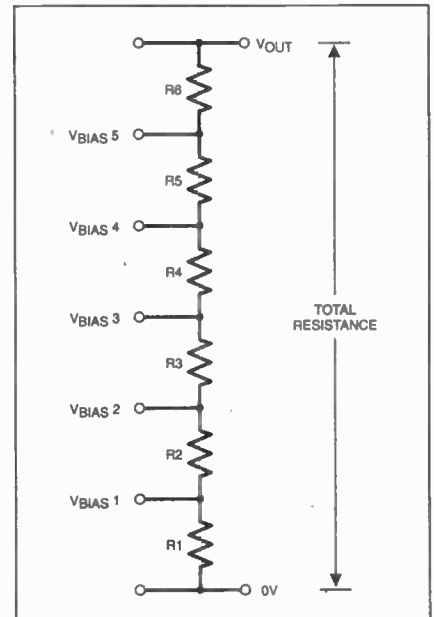


Fig.6. Basic potential divider chain used for voltage control selection.

LISTING 1

```
DATA 6,9,12,15,18: ' required Vout
rtotal = 10: m = 0
PRINT "IDEAL VALUES for Rtotal";
PRINT " = "; rtotal; "k"
PRINT "Vout"; TAB(10); "Vbias";
PRINT TAB(20); "Resistor"
FOR a = 1 TO 5: READ vout
vbias = vout - 5
r(a) = ((vbias / vout) * rtotal) - m.
rn = rn + r(a)
PRINT vout; "V"; TAB(10); vbias;
PRINT "V"; TAB(20); "R";
PRINT LTRIM$(STR$(a));
PRINT r(a); "k": NEXT
PRINT "R6 =
Rtotal-(R1+R2+R3+R4+R5)";
PRINT " = "; rtotal - rn; "k"
```

LISTING 2

```
DATA 1.8,3.0,1.5,910,620,3.0
PRINT : PRINT "E24 VALUES";
PRINT TAB(16); "CURRENT";
PRINT TAB(31); "Vout"
FOR a = 1 TO 6: READ r(a)
t = t + r(a): NEXT
ry = 0: rx = t
FOR a = 1 TO 5
rx = rx - r(a)
ry = ry + r(a)
I = 5000 / rx
v = (I * ry) / 1000 + 5
PRINT "R"; a; " = "; r(a); "k";
PRINT TAB(15); I; "mA";
PRINT TAB(30); v; "V": NEXT
PRINT "R"; a; " = "; r(a); "k";
PRINT TAB(20); "Rtotal = "; t; "k"
```

Table 4

Ideal	Chan 1	Chan 2
5V	4.97V	4.98V
6V	5.97V	5.98V
9V	8.93V	8.94V
12V	11.86V	11.87V
15V	14.84V	14.74V
18V	17.91V	17.82V

The values in Table 3 were considered to be close enough to the ideal for them to be acceptable. In practice, they will differ slightly because of resistor tolerance factors. Those obtained with the prototype are given in Table 4 (note that even the "fixed" 5V output of the two regulator i.c.s is not exactly 5V).

The basic formulae used in the programs are those for potential dividers, as given earlier (Listing 1), and Ohm's Law (Listing 2), $V = I \times R$.

Referring to Fig.5, the voltage (call it V_b) across R_x is automatically specified as 5V, and thus the current (I) flowing through R_x is calculated as:

$$I = V_b / R_x$$

The current flowing through a potential divider chain is constant at whatever point in the chain it is measured. Consequently, the same current flows through R_y as flows through R_x .

Therefore the voltage drop across R_y (V_{bias}) simply equals $I \times R_y$, and so the regulated output voltage (V_{out}) for the specified values of R_x and R_y is V_{bias} plus 5V.

As an example, and referring to Fig.6 and the resistor values in Table 3, to find V_{out} when V_{bias} at the junction of R_5 and R_6 is selected (V_{bias5}), the following reasoning is used:

Since a voltage of 5V exists across R_6 (3k), then a current of $5V/3k = 1.666667mA$ flows through R_6 (Ohm's Law derivative $I = V/R$). Consequently the voltage drop (V_{bias}) across the total of $R_1 + R_2 + R_3 + R_4 + R_5$ (7.83k) is calculated as $1.666667mA \times 7.83k = 13.05V$. Thus the regulated output voltage $V_{out} = V_{bias} + 5V = 18.05V$, as listed in Table 3.

As a result of these calculations, the pre-sets were dropped from the prototype and a resistor chain substituted instead, as shown in Fig.4 earlier. (All of which confirms the author's belief that a computer is one of his most important workshop tools!)

The two programs listed can be modified to calculate other tapped potential divider characteristics, for as many nodes as required.

CURRENT MONITORING

The switched voltage from IC1 (Fig.4) is taken to the output sockets (SK1 to SK5) via a 1Ω resistor, R_3 . This allows current flow to be monitored, according to the voltage drop across R_3 caused by the amount of current flowing (Ohm's Law again). The resistor is included in the bias setting (potential divider) chain to maintain the correct output voltage irrespective of load currents.

Op.amp IC2b is configured as a differential amplifier. The voltage to either side of R_3 is fed to the op.amp's inputs (pins 5 and 6) and the amplified difference is routed to the PIC microcontroller (discussed later) from point V2. The gain as seen by changes in voltage on the non-inverting input is about ten, as set by R_{10} and R_{11} ($R_{11} / R_{10} + 1$).

If current monitoring is not required, the circuit around IC2b may be omitted and the switched voltage fed to the output sockets, SK1 to SK5, from point V1. R_3 must be replaced by a wire link.

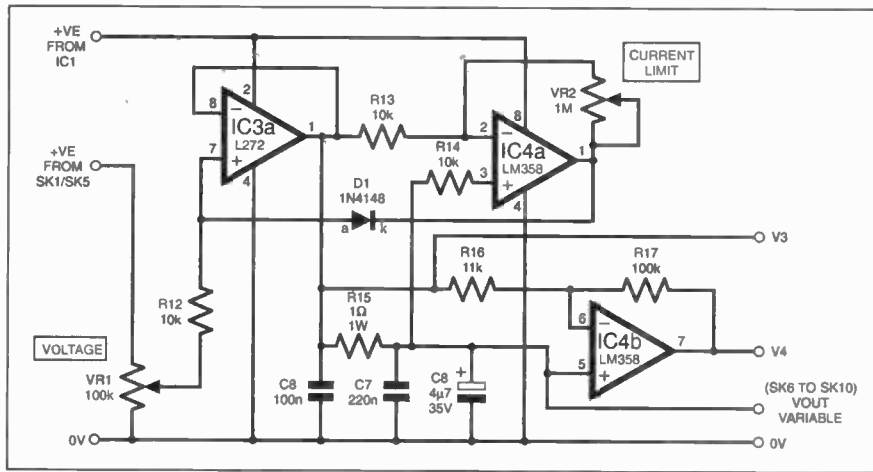


Fig.7. Variable voltage and current limiting circuit.

VARIABLE SUPPLY

As it stands, the circuit in Fig.4 is perfectly usable on its own, with or without current monitoring. However, the author frequently has the need for a control voltage that can be varied from 0V upwards. Whilst a potentiometer across a fixed supply can provide such a voltage via its wiper, the current available is limited by the resistance at the wiper.

Consequently, this power supply has had a buffer circuit added, the circuit diagram for which is shown in Fig.7.

The buffer is formed around op.amp IC3a, one half of an L272 dual power op.amp (the other half is unused). The L272 is capable of supplying a current of 1A, but there are limitations imposed by the circuit, as discussed presently.

Power for the op.amp is taken from the switch-selected voltage output from regulator IC1. Potentiometer VR1 is also connected across the same supply, but following resistor R_3 , and its wiper voltage is fed via resistor R_{12} to the non-inverting input of IC3a, pin 7.

The op.amp's output from pin 1 could be used directly as a variable voltage supply via the connection marked V3. However, as with the switched supply, it has a 1Ω resistor (R_{15}) in series with it, through which the voltage is fed to the output sockets (SK6 to SK10). This allows current flow to be monitored via differential amplifier IC4b, which has the same function as IC2b, outputting an amplified current-dependent voltage to the PIC.

CURRENT LIMITING

The variable supply has been given a simple current limiting facility, via the circuit around IC4a.

This circuit is also configured as a differential amplifier, monitoring the voltage across R_{15} and amplifying it according to the ratio of R_{13} and the resistance across potentiometer VR2.

The output of IC4a is connected back to the non-inverting input of IC3a via diode D1. If the current through R_{15} causes the amplified output voltage from IC4a to fall 0.7V (the "diode drop" voltage for a silicon diode) below the voltage on IC3a pin 7, the latter will be pulled down across resistor R_{12} . The result is that the output voltage from IC3a will fall by the same amount, so limiting the power fed to the load circuit.

Potentiometer VR2 permits the threshold gain to be varied from roughly unity ($VR_2 = 0\Omega$) to about 100 (VR_2 at maximum resistance). By test on the prototype, with VR2 at zero resistance, a current flow of about 350mA through R_{15} causes the threshold to be reached, beyond which the input to IC3a is progressively reduced. With VR2 at maximum resistance, 5mA across R_{15} has the same effect.

Any circuit powered from the variable supply must have a smoothing capacitor across its input power lines in order to prevent the current limiting circuit from oscillating when the threshold is reached. Without the capacitor, when the threshold is reached the output voltage falls, and so the current flow decreases, causing the voltage to rise again, etc.

Note that the presence of R_{15} on its own will also cause a voltage drop at the output sockets in response to increasing current, simply according to Ohm's Law (100mA causes a 0.1V drop).

PIC MONITOR

The PIC microcontroller, IC6 in Fig.8, monitors the voltages input to it from the four power supply circuits, outputting data to the l.e.d.s. and the l.c.d. It interprets and displays the data according to factors input to it from switches S3 to S6. It does not actually control the power supply in any way.

As discussed in previous published PIC16F87x designs, this family of devices has several inputs which can be used for analogue-to-digital conversion. The PIC16F877 used here has eight A/D inputs, allowing the twin voltage levels from all channels to be monitored.

For each channel, the twin voltages are tapped prior to the 1Ω current sensing resistor and at the output of the respective differential amplifier.

The voltages can be several times greater than the PIC can safely handle and are attenuated by eight 20k/2k2 potential dividers, formed around R_8 to R_{33} . The attenuation ratio is 1:10, which at first sight may seem high.

The reason is that Channel B can be connected in series with Channel A (refer back to Fig.1). In this situation, Channel B can produce a possible maximum voltage of 36V with reference to the PIC's 0V line. The 1:10 attenuation thus results in 3.6V at the input to PIC. Whilst a ratio of 5:36 (1:7.2) would allow slightly greater

precision of the digital conversion, a ratio of 1:10 makes the software processing somewhat easier.

The software repeatedly samples the eight inputs, and produces 10-bit conversion values. From these it calculates the source voltages prior to the attenuators. The value of the voltage prior to each 1Ω resistor is stored for output to the l.c.d.

This value is also compared with the voltage from the respective differential amplifier and a value for the current being drawn by the load circuit is calculated. This too is stored for subsequent output to the l.c.d.

The current values are additionally compared with the current limit values preset via switches S3 to S5. If the limit is being exceeded, the appropriate l.e.d. (D3 to D6) is turned on. Resistors R39 to R42 are the l.e.d. ballast resistors.

If the total current being supplied by a channel, via either or both of its outputs, exceeds 1A then both l.e.d.s for that channel are turned on, as is buzzer WD1.

The current being drawn must be reduced below the limits before the l.e.d.s and buzzer are turned off.

Remember that the PIC does not control the power supply in response to these limits being reached.

L.C.D. MODULE

Data is sent to the l.c.d. module (X2) in 4-bit mode, with the same physical pin connection order as used with all the author's l.c.d. controlling designs over the last couple of years.

Readers who already have l.c.d.s with connectors that match those designs can simply plug them straight in to this Power Supply's monitor p.c.b. via the matching terminal pins (notated as TB1).

Preset potentiometer VR3 adjusts the l.c.d. screen contrast.

A point worth considering is whether or not to use a back-lit l.c.d. The author's workshop is well lit and the screen of the normal reflective type of l.c.d. used can be clearly seen.

In a less well-lit situation, however, the use of a (slightly more expensive) back-lit version could be beneficial, because the screen is on the front panel and faces forwards, rather than upwards as with the majority of published designs using l.c.d.s.

Typically, back-lit l.c.d.s have illumination provided by internal l.e.d.s. It is possible that the l.e.d.s can be powered from the monitor board's 5V supply (check the l.c.d. data sheet for the backlighting power requirements and connections). If this is the case, it would be prudent to use a 7805 1A regulator for IC5, instead of the 78L05 100mA device listed.

CONTROL SWITCHES

Pushbutton switches S3 to S5 allow the PIC's current limiting data to be changed as required. S6 selects which of three display modes is shown: full data for one output, voltage data for all four outputs, or current data for all four outputs. Each push of S6 steps the display through the modes, on a repeating cycle.

When in the mode for single-output full data display, switch S5 steps the display through each output, on a repeating cycle of four. Typical displays are shown in Part 2.

Top left of the screen shows the output identity. This is notated in the form Ch1 to Ch4, where:

- Ch1 = Channel A switched output
- Ch2 = Channel A variable output
- Ch3 = Channel B switched output
- Ch4 = Channel B variable output

Top right of the screen shows the preset current limit for that output. It can be increased by S4 or decreased by S3. The limit is changed in steps of 10mA, with a minimum of 10mA and a maximum of 1A.

When S3 or S4 are released, the value displayed is stored in the PIC's EEPROM data memory. It remains there even after power has been switched off. It is recalled when the unit is again switched on.

Bottom left of the screen shows the voltage presently supplying the selected output (before the 1Ω resistor), in steps of 0.05V.

Bottom right of the screen shows the current being drawn from the output, in steps of 5mA.

Be mindful of the fact that the monitored voltage and current details on the l.c.d. screen are not as precise as those which a multimeter will display. They should be treated only as an approximate guide to prevailing conditions.

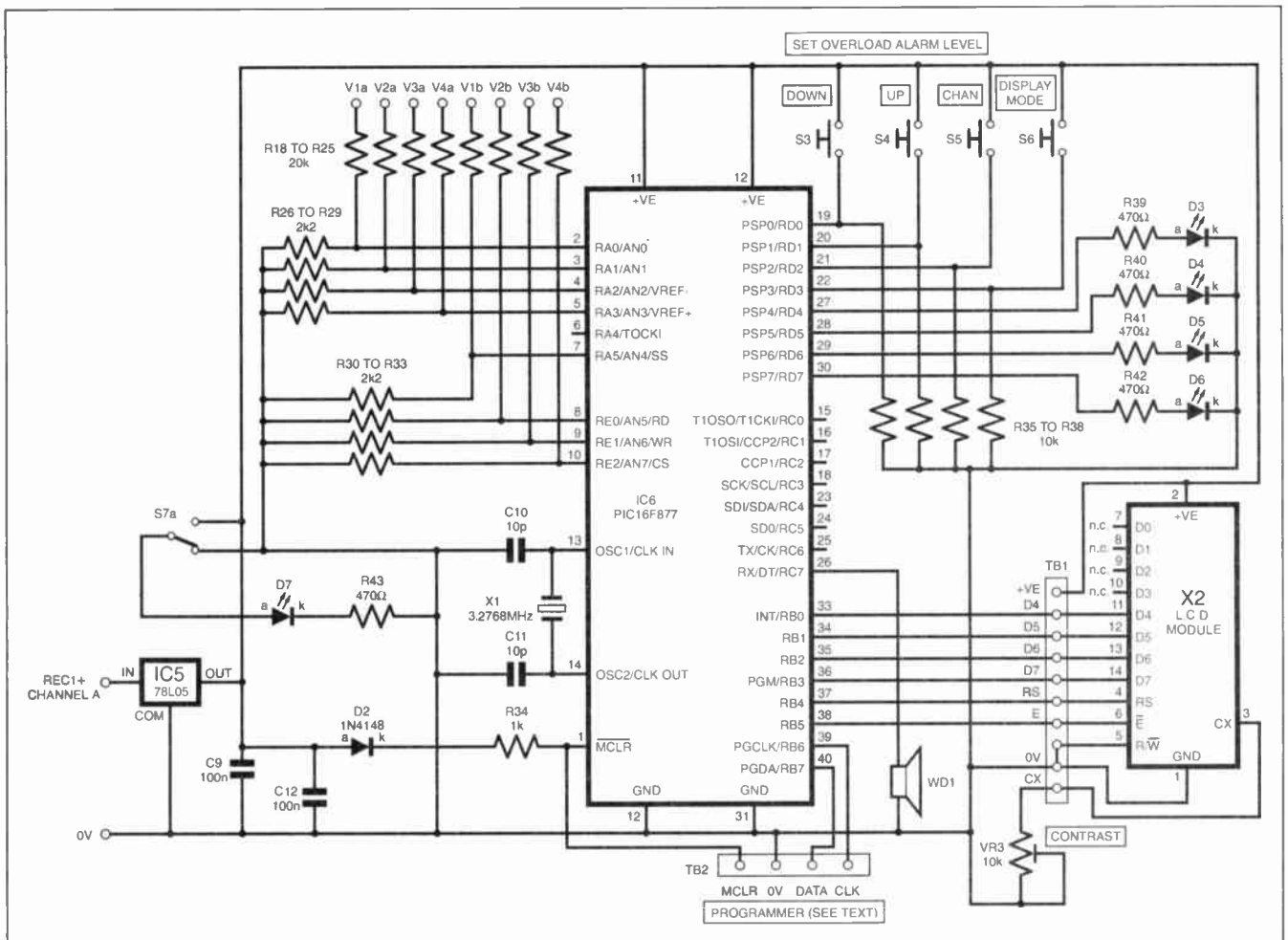


Fig.8. Circuit diagram for the PIC-monitoring option of the full power supply.

COMPONENTS

SINGLE FULL PSU CHANNEL Excluding the PIC monitoring circuit

Resistors

R1	1k 0.25W 5%
R2	1M 0.25W 5%
R3, R15	1Ω 1W 5% (or better) (2 off)
R4, R8	3k 0.25W 1% (2 off)
R5	620Ω 0.25W 1%
R6	910Ω 0.25W 1%
R7	1k5 0.25W 1%
R9	1k8 0.25W 1%
R10, R16	11k 0.25W 1% (2 off)
R11, R17	100k 0.25W 1% (2 off)
R12 to R14	10k 0.25W 5% (3 off)

Potentiometers

VR1	100k lin rotary
VR2	1M lin rotary

Capacitors

C1	4700μF radial elect. (see text)
C2, C7	220n ceramic disc, 5mm pitch (2 off)
C3 to C6	100n ceramic disc, 5mm pitch (4 off)
C8	4μ7 radial elect. 35V

Semiconductors

D1	1N4148 silicon signal diode
IC1	7805 +5V 1A voltage regulator
IC2, IC4	LM358 dual op.amp (2 off)
IC3	L272 dual power op.amp

Miscellaneous

REC1	W005 50V 1A bridge rectifier, or similar
S1	s.p.d.t. switch, mains rated
S2	2-pole 6-way rotary switch
SK1 to SK15	2mm socket, 3 colours, 5 off each (see text)

Printed circuit board (power supply), available from the *EPE PCB Service* code 280; knob (3 off); TO220 insulating washer kit for IC1; 8-pin d.i.l. socket (3 off)

All above parts repeated for second channel.

Also required

FS1	20mm fuseholder, panel mounting, with 1A 20mm fuse, slow blow
T1	mains transformer, 0-15V, 0-15V secondaries, 50VA (25VA per winding)

Metal case, 255mm × 160mm × 196mm (see text); heatsink compound (see text); eyelet tag; mains cable clamping grommet; nuts and bolts for mounting transformer (2 off each); cable ties; 1mm terminal pins; 3-core mains cable, 5A; connecting wire; solder, etc

Approx. Cost
Guidance Only

£30
excluding case

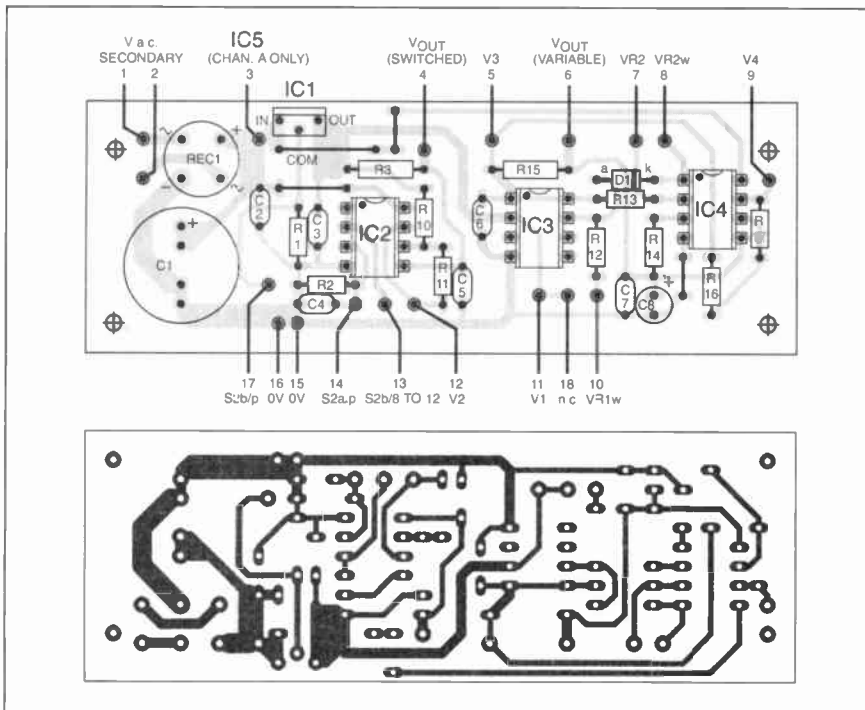
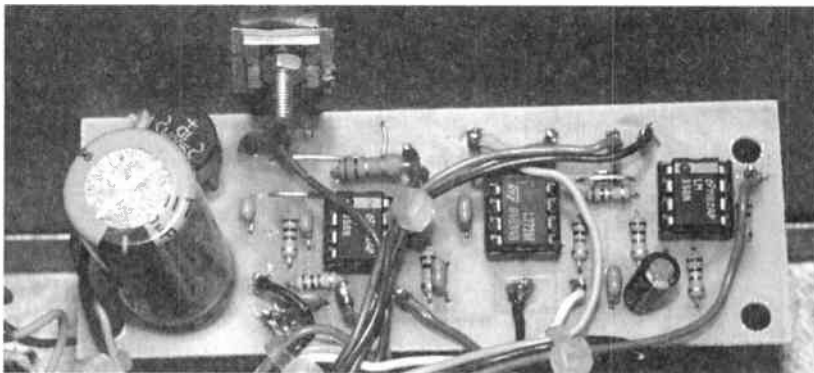


Fig.9. Printed circuit board component layout and full size copper foil master track pattern for the power supply in Fig.4.



MISCELLANY

The PIC and l.c.d. are powered at 5V. This is provided by regulator IC5, whose input is connected directly to the rectified voltage at capacitor C1 (approximately 20V) of Channel A. The current drawn, with the l.e.d.s inactive, is a little under 6mA.

Crystal X1 sets the PIC's clock frequency at 3.2768MHz.

Selection of parallel or serial connection of Channels A and B is made by switch S7b in Fig.1 earlier. In Fig.8, S7a is the second half of the same switch and turns on l.e.d. D7 when the channels are connected in series.

CONSTRUCTION

There are two printed circuit boards, one for the PIC monitoring circuit, the other for the power supply components of a single channel (two are needed if both channels are required).

Their constructional and track layout details are shown in Fig.9 and Fig.10. The boards are available from the *EPE PCB Service*, code 281 for the monitor and 280 for the power supply.

Preferably assemble the components in ascending order of size, commencing with the on-board link wires. Use sockets for IC2, IC3, IC4 and IC6. Do not insert IC6 (the PIC) into its socket until a few circuit

tests have been made later. Ensure the correct orientation of all other semiconductors and the electrolytic capacitors.

Mount the rectifier (REC1) and 1Ω resistors (R3 and R15) so that their bodies stand a bit above the p.c.b., allowing air to circulate around them. Also mount regulator IC1 somewhat above the p.c.b. to allow it to be easily bolted to the side of the case during the final stages of connecting up.

For terminal pin blocks TB1 and TB2 use 1mm pin-header strips. For the other off-board connection points insert 1mm terminal pins.

CASE PREPARATION

The case used in the prototype and shown in the photographs is one which the author has had for some years. Regrettably it has been discontinued by the supplier, but an alternative case of a similar size is quoted in *Shoptalk*. The size of the original is 255mm × 160mm × 196mm (l × h × d). The detachable front and rear panels measure 245mm × 135mm. They are made from aluminium, whilst the rest of the case is mild steel.

Referring to the photographs, plan and drill your chosen case with care. Allow ample clearance between all mains powered connections and other items. Use a clamping cable grommet for the mains input lead.

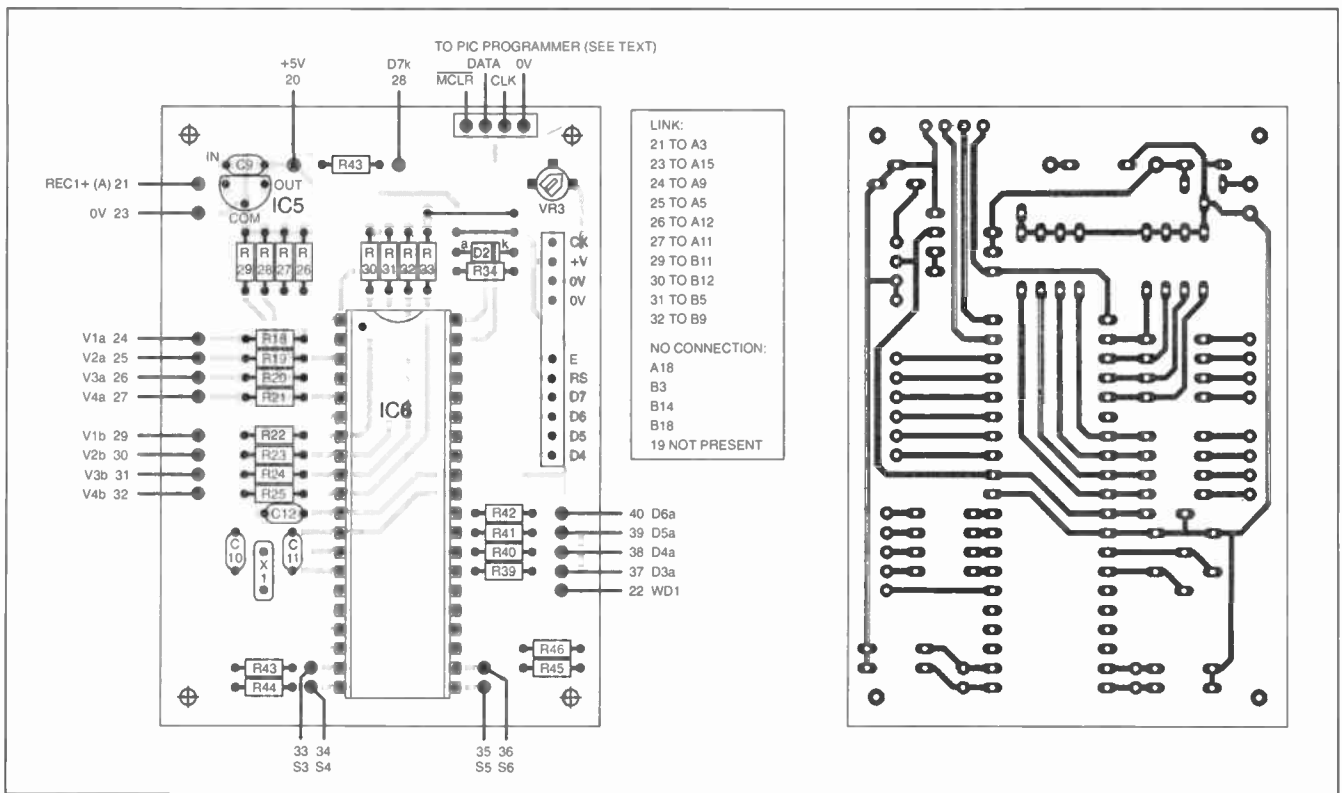
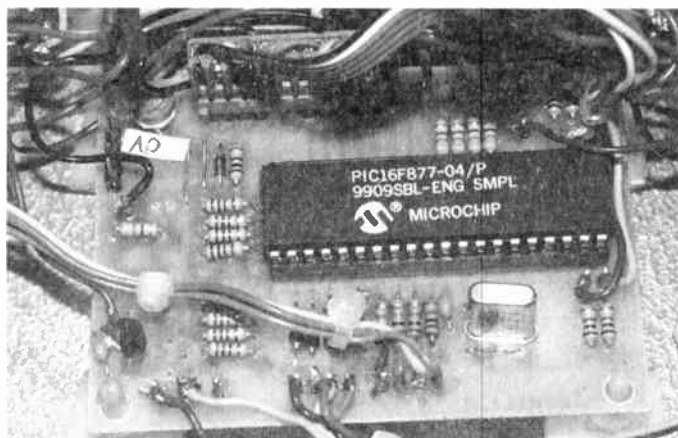


Fig. 10. Component layout and full size copper foil master track pattern for the PIC-monitoring circuit in Fig. 8.



If you prefer to use output sockets of a larger size to the 2mm type used in the prototype, you may not have room for the same quantity. The author prefers having several sockets connected to a single power supply output, allowing several circuits to be powered simultaneously from the same source.

Allow reasonable space for the control knobs to be rotated.

Mark the l.c.d. position carefully, then drill a succession of holes inside the perimeter of its screen position to ease the sawed removal of the oblong cut-out. Finish off with a file.

The l.e.d.s in the prototype were purchased as panel mounting components complete with pre-connected leads. Conventional l.e.d.s and mounting clips may be used instead. The wiring diagram in Part 2 shows the connections for the latter type.

Drill a hole in each side panel through which the IC1 regulators have to be bolted, attached to their p.c.b. Insulating washers and bushes should be used with the regulators, together with heatsink compound (some types of washer do not require the compound – consult your supplier when ordering the washers).

It is essential to check that there is no electrical connection between the case and the tabs of the regulators.

NEXT MONTH

In the concluding part next month the wiring up of the full power supply is detailed, heat sinking is discussed, and operation of the software is described. Details of constructing simpler versions will also be given.

See this month's *Shoptalk* page for information on obtaining the software, and general information on buying the components.

COMPONENTS

Approx. Cost
Guidance Only

£30
excluding case

MONITOR UNIT

Resistors

R18 to	20k 0.25W
R25	1% (8 off)
R26 to	2k2 0.25W
R33	1% (8 off)
R34	1k 0.25W
	5%
R35 to	10k 0.25W 5% (4 off)
R38	
R39 to	470Ω 0.25W 5% (5 off)
R43	

Capacitors

C9, C12	100n ceramic disc, 5mm pitch (2 off)
C10, C11	10p ceramic disc, 5mm pitch (2 off)

Potentiometer

VR3	10k min. preset, round
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Semiconductors

D2	1N4148 silicon signal diode
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See
SHOP
TALK
page

D3 to D7	red l.e.d. (5 off) (see text)
IC5	78L05 +5V 100mA voltage regulator (see text)
IC6	PIC16F877-4 microcontroller, pre-programmed (see text)

Miscellaneous

S3 to S6	s.p. min. push-to-make switch (4 off)
S7	d.p.d.t. min. toggle switch
TB1, TB2	1mm pin header strips (see text)
WD1	5V to 9V active buzzer
X1	3.2768MHz crystal
X2	2-line, 16-character (per line) liquid crystal display (see text)

Printed circuit board, available from the *EPE PCB Service*, code 281 (monitor); 40-pin d.i.l. socket; nuts and bolts for l.c.d. (4 off each); cable ties; connecting wire; solder, etc.

Prices fully inclusive

The FED PIC C Compiler - Rapid, Efficient, High level development

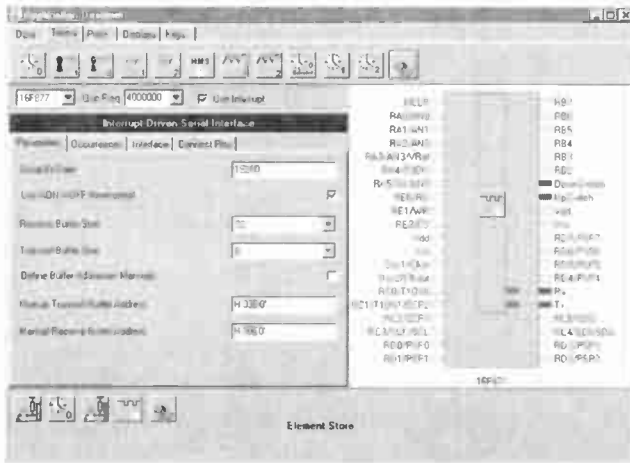
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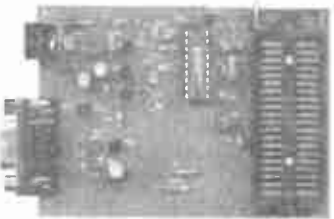
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MOBILE PROMISES . . . PROMISES . . .

Can Packet Radio make marketplace headway if it does not fulfil promised expectations? Barry Fox reports.

THE cellphone industry risks crippling the fledgling market for GPRS by making the same kind of undeliverable promises that turned users off WAP.

GPRS, the new General Packet Radio Service due for consumer launch this Christmas, is already being wildly overhyped. It will deliver much slower data speeds than promised and looks sure to disappoint – like WAP before it.

"Who needs 3G (the third generation cellphone system due in a few years), when GPRS is here now," asks Motorola, the first company to deliver GPRS handsets. BT, now trialling the first commercial GPRS service, says "accessing video and multimedia applications on your mobile phone is now a reality."

"GPRS opens the mobile market to wireless multimedia," promises Motorola's web site, "with streaming and live video content."

Technical and medical issues make even the theoretical speeds unattainable.

TIME SLOTS

The European GSM digital cellphone system is now used in most countries. Channels 25kHz wide are sliced into eight time slots. Each slot carries a separate conversation, or data, at 9.6Kbps, one sixth the speed of a fixed phone line and modem. Users are charged for the time they use a slot.

GPRS lets users share time slots, with charges levied for data moved, not time on line. Several coding systems are used to protect against transmission errors. CS-1 has the most powerful error correction, but delivers only 9.05Kbps per slot. Where the radio signal is strong, CS-2 coding delivers 13.4Kbps.

Some of this data is wasted on "headers" needed to label Internet data. The data rate also varies depending on how many people are sharing slots. Most important, four or five slots can eventually be used for reception, a handset or PC card can transmit only one or two slots before the chips get too hot and burn out. Above two slots there is a health risk from excessive radiation (Specific Absorption Rate). Battery drain doubles for two slot working, so life halves.

Motorola's first GPRS phones, such as the Timeport, will handle only one time slot out of the phone and two into the phone. New phones next year will work with one slot out and four down. The first models are not upgradeable. Ericsson will wait and launch with one out and four down.

EXPERT OPINIONS

Rainer Lischetzki of Motorola says, "The realistic maximum rates we can get from GPRS are 64Kbps into the handset and 30Kbps out. We have known for ages about these limitations. We regret the sales talk, and data rate exaggeration."

A BT Cellnet engineer was privately even more conservative, promising only between 7Kbps and 10Kbps per slot, or a best case scenario next year of 10Kbps transmit and 40Kbps receive.

But Motorola's own web site and technical briefing documents promise speeds up to 171.2Kbps with "streaming and live video content", while BT's

publicity literature promises the chance to "send and receive data up to five times faster than is currently possible . . . and speeds will increase up to ten times faster in the coming months."

Because GPRS is an always-on system, with charges for the quantity or quality of data handled, rather than time on line, it becomes the ideal tool for receiving E-mail on the move. But people who believe the publicity and buy GPRS as a mobile multimedia tool will be sorely disappointed. Even low quality mono sound needs two time slots; MPEG-4 videophone links can manage only one or two coarse video pictures a second.

IT SUCKS – IN STYLE!



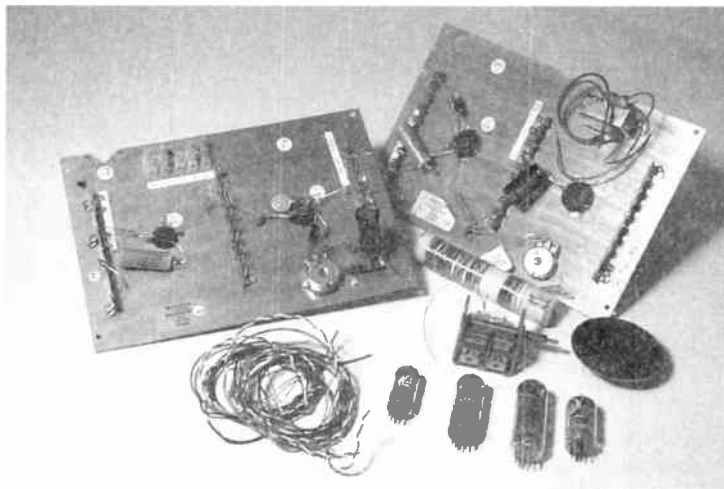
THE new Shesto Pal range of suction pick-up tools is invaluable for model making, craft and hobby uses, and they work without batteries.

Held like a pen, a gentle press of the button or bulb creates the correct amount of pressure to pick up, rotate and easily place small objects. To release them simply press the button again. The tools easily handle electronic components and a variety of other materials without risk of damage or blemish.

There are two models: the Model Pal at £7.95, comprising suction bulb and three cups, and the Hobby Pal at £14.95, which includes suction generator, four cups, two straight holders and two angled holders. The prices are quoted as *post free*.

For more information contact Shesto Ltd., Dept EPE, Unit 2, Sapcote Trading Centre, 374 High Road, Willesden, London NW10 2DH. Tel: 020 8451 6188. Fax: 020 8451 5450. E-mail: sales@shesto.co.uk. Web: www.shesto.com.

GREENWELD AND KITMASTER



TWO well known companies, Greenweld and Kitmaster, have announced that they are to combine.

Greenweld, having been successfully resurrected after the closure of the old company in 1999, have seen high levels of demand for their new and surplus range of electronic bargain buys. During the same period, the arrival of Kitmaster by David Johns has brought back the era of valve radios.

Recently, Greenweld have been featuring Kitmaster products for sale through their own mail order and online shopping services. Realising the potential offered by these popular designs, Greenweld are combining their established mail order infrastructure with David John's expertise in electronics and valve radio design.

Geoffrey Carter of Greenweld tells us that Kitmaster's novel approach to electronic kit building has revived interest in valve technology assembly for both novices and experienced users alike. Models such as the popular Four Valve Regeneration Unit are selling at a high rate. Recent introductions include a range of battery-operated valve radios, which are becoming even more sought after. Each kit contains all the necessary parts, together with a detailed and comprehensive manual.

Greenweld will continue their commitment to offering a huge range of electronic components, together with frequent purchases of surplus electronic equipment of every type which is, as usual, offered at bargain prices. David Johns will continue to develop new products.

For a free catalogue contact Greenweld Ltd, Dept EPE, Unit 24, Horndon Industrial Park, West Horndon, Brentwood, Essex CM13 3XD. Tel: 01277 811042. Fax: 01277 812419. E-mail: service@greenweld.co.uk. Web: www.greenweld.co.uk.

EWB WITH PCB CAD

ELECTRONICS Workbench devotees will be pleased to learn that this superb circuit design and simulation software package has now had printed circuit design facilities added to its pedigree. The sense of making such an addition will be obvious to anyone who is familiar with EWB.

EWB multiSIM is a complete system design tool which offers schematic entry, comprehensive component database, SPICE simulation, VHDL/Verilog entry and simulation, waveform analysis, r.f. capabilities and "seamless" transfer to p.c.b. layout. It is said to offer a unique combination of advanced functionality and exceptional ease of use.

Many of you will recall that we featured the basic EWB software in Mike Tooley's excellent *Electronics from the Ground Up* series of Oct '94 to Jun '95.

For more information contact Adept Scientific plc, Dept EPE, Amor Way, Letchworth, Herts SG6 1ZA. Tel: 01462 480055. Fax: 01462 480213.

E-mail: ewb@adeptscience.co.uk.
Web: www.adeptscience.co.uk.

MAPLIN 2000/2001 CAT

MAPLIN Electronics have launched their new 2000/2001 catalogue with a huge range of products, over £100 worth of money-off vouchers and many brand new lines.

Maplin comment that their catalogue is "widely regarded as *the* electronics product bible." Now in its 28th year, it contains products ranging from individual components to state-of-the-art electronic equipment. It is available in traditional format (cost £3.99) or on a CD-ROM (£1.99).

The products can also be found at 57 Maplin stores nationwide, where specialist staff are available to help with technical and product enquiries. The Maplin website also features full product range details and a secure on-line ordering service with stock checking facilities.

For more catalogue information contact Maplin Electronics, Dept EPE, Valley Road, Wombwell, Barnsley S73 0BS. Tel: 0870 264 6002.

Web: www.maplin.co.uk.

Sparing DVD Egg-spense?

By Barry Fox

TECHNICS launched DVD-Audio at the Hammersmith HiFi show. Consumers now have the chance to spend £900 on a new format player with no new format software to play on it. The only discs at the show were DVD-R dubs from Universal. None exploited the full DVD-Audio specification of 192kHz.

"It's chicken and egg," says Technics.

Most people may prefer to wait until there are eggs to go with their £900 chickens.

Talking Signs

ON a number of occasions we have mentioned NXT, the inventors of Surface Sound flat panel loudspeaker technology. They tell us that they have unveiled a multilingual talking sign incorporating this revolutionary technology.

Using the latest digital audio techniques (MP3), the sign speaks in nine languages and is installed at the Whittington Hospital in Highgate, London. Research had shown that many public areas encounter a growing number of ethnic issues, including the variety of languages spoken and the need for simple spoken information.

Simply touching the panel gives the user instant access to customised information in a selection of languages. The combination of colourful graphics and clear high quality sound allows a wide range of messages and information to be imparted in a concise and friendly manner to both English and non-English speakers.

A spokesman for the Whittington Hospital said "This is a very exciting development for us, and we are pleased to be the first hospital in the UK with this particular initiative. We serve a culturally mixed community and we are always striving to improve our standards of health and ethnic issues."

For further information contact New Transducer Ltd., Dept EPE, 37 Ixworth Place, London SW3 3QH. Tel: 020 7343 5050. Fax: 020 7343 5055.

E-mail: marketing@nxtsound.com.
Web: www.nxtsound.com.

Patents Rising

APPLICATIONS for patents have risen by six per cent to over 30,000 for 1999, according to figures released by the UK Patent Office. Most patents were granted in the telecomms sector, 865 patents, but electric circuitry also came high, at 429 patents.

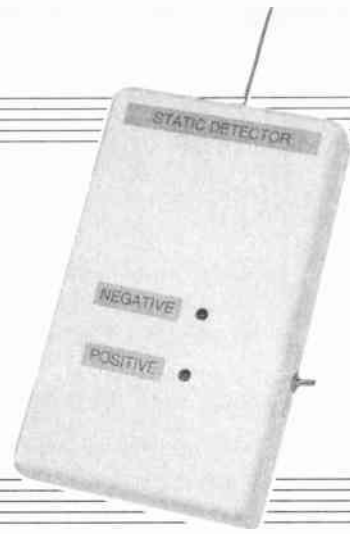
The Patent Office web site (www.patent.gov.uk) is receiving 50,000 hits daily (up from 20,000 a day last year), signifying that more people are wanting to find out how to protect their ideas and inventions.

The DTI (Department of Trade and Industry) also tells us that 27 per cent of UK businesses are now trading on-line. This puts the UK on a par with the USA and Canada, and ahead of Germany and Sweden (see www.ukonlineforbusiness.gov.uk).

Starter Project

STATIC FIELD DETECTOR

ROBERT PENFOLD



Amuse your friends and family with this novel "electroscope" starter project. – See if they are highly charged characters!

THIS ultra-simple device was designed as a low cost project for complete beginners, but it should also be of interest to those who like to experiment with unusual gadgets. It is a form of electroscope, which is a device that detects static electricity.

No doubt most readers have seen demonstration of purely mechanical devices that use electrostatic forces to show the presence of high static voltages. This device uses some simple electronics to detect much smaller potentials, with a twin l.e.d. display showing any increase or decrease in the detected voltage.

It has to be emphasised that this very simple unit is only intended to be a "fun" project, and it is not suitable for serious scientific purposes. Those with a serious interest in the subject of atmospheric electricity should refer to the recent *EPE* articles (*Atmospheric Electricity Detector* – June/July 2000) on this subject by Keith Garwell.

BASICS

What is the difference between static electricity and the regular variety, and why is it not possible to measure static electricity using ordinary test equipment?

In normal electronics we are concerned with a flow of electricity, with electrons moving along wires or into and out of components. Static electricity is not fundamentally different to the electrical signals we normally deal with in that it is still comprised of electrons. The difference is that the electrons are not going anywhere.

Although normal matter contains electrons, it does not necessarily have a static charge. Matter has a positive charge when it has fewer electrons than normal, or a negative charge if it has an excess of electrons.

As most readers will be aware, static charges can be generated by friction, and rubbing many plastics will generate quite high voltages. The fact that static charges are present in most environments is probably less well known. Where you are right now there could well be a potential of 50V to 100V between the air near the floor and the air about two metres higher up.

On the face of it, measuring voltages of this order should be easy enough and any multimeter should be able to handle the task. In practice matters are more complicated due to the nature of the signals involved. The voltages may be quite high, but the available current is quite low. To be more precise, an appreciable current is available, but only very briefly.

Although a digital multimeter has a high input resistance of typically over 10 megohms, this will still rapidly leak away the charge being measured. In fact, it will leak it away before a meaningful measurement can be made.

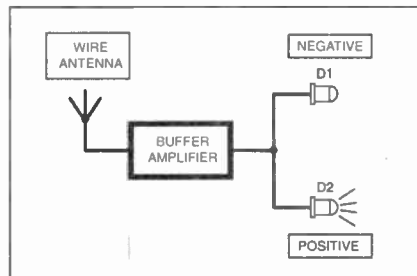


Fig.1. The static detector is basically just a buffer amplifier and two l.e.d.s.

A voltmeter having an extremely high input resistance is needed in order to measure static charges. The amount of current drawn by the test instrument is then so low that it does not significantly reduce the charge voltage during the measurement process.

Obtaining a suitably high input resistance is not difficult, since this is a natural characteristic of field effect transistors (f.e.t.s). It is also an attribute of many operational amplifiers (op.amps) which use field effect devices in their input stages. Op.amps having input resistances of one million megohms or more are commonplace, and this is more than adequate for the present application.

SYSTEM OPERATION

This Static Field Detector uses the simple arrangement shown in Fig.1. An antenna consisting of a short piece of wire is connected to the input of a buffer amplifier that

has an ultra-high input resistance. This amplifier has no voltage gain, and its sole purpose is to provide the circuit with an ultra-high input resistance. There are no bias resistors or other components at the input of the amplifier, which is therefore free to float to whatever potential the antenna assumes.

The output of the amplifier drives two l.e.d. indicators. With the output of the amplifier at about half the supply potential both l.e.d.s are switched on fairly brightly.

If the output potential rises, the brightness of l.e.d. D2 increases but l.e.d. D1 becomes dimmer and will switch off if the output potential becomes high enough. A decrease in the output voltage has the opposite effect, with D1 becoming brighter and D2 going dimmer or even switching off altogether. This method is very simple and inexpensive, but it clearly shows any variations in the detected voltage.

MEASURING WHAT?

When measuring voltages in a circuit you do not simply place one test prod on a test point and read its voltage. Most equipment is of the negative earth variety, and voltages are therefore measured relative to the negative supply rail. One test prod is connected to the earth rail (0V), and the other is placed on the test points.

Here we are effectively using a single test prod in the form of the antenna, with voltage measurements being made relative to nothing. Although it might seem as though the same middle reading will always be obtained, this is not actually the case.

When the unit is first switched on the two l.e.d.s will switch on to indicate a middle voltage. If the unit is moved around the l.e.d.s should soon start to indicate changes in potential. The unit is registering changes in voltage relative to the antenna's starting potential. It would be possible to connect the negative supply rail of the unit to an earth and then make measurements relative to the earth's potential.

However, a simple circuit such as this can only handle an input voltage range of about 0V to 9V, whereas signals of either polarity and up to a few hundred volts in magnitude might be encountered. Also, using an earth is relatively awkward and restrictive. The method used here is freer, easier, and works quite well.

CIRCUIT OPERATION

The full circuit diagram for the Static Field Detector appears in Fig.2. The

operational amplifier, IC1, is the buffer amplifier, and is a bi-f.e.t. device that uses junction gate field effect transistors in its input stage. A device having a MOSFET input stage should work equally well on the input side of things, as should any other bi-f.e.t. op.amp.

The specified TL061CP op.amp has an output stage that will drive both l.e.d.s from fully switched off to fully switched on, whereas most other op.amps will fail to do this. Consequently, the use of alternative devices is not recommended.

No voltage gain is required in this application, so 100 per cent negative feedback is provided by coupling the output of the amplifier (pin 6) to the inverting input (pin 2) via resistor R1. The output adopts the same voltage as the non-inverting input at pin 3, but there is a massive current gain through IC1.

The input current is probably a few nanoamps or even picoamps, but the output can provide a few milliamps to drive the l.e.d.s at good brightness. Resistors R2 and R3 limit the current fed to l.e.d.s D1 and D2 to a safe level.

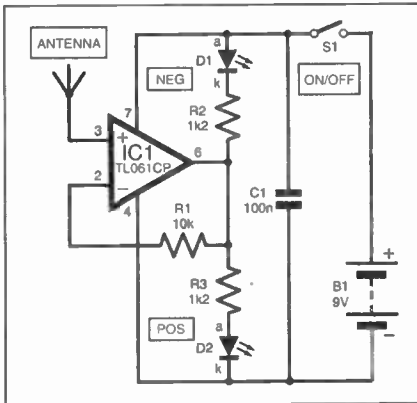


Fig.2. Complete circuit diagram for the Static Field Detector.

The maximum drive current is about 5mA. The TL061CP used for IC1 is a low current device, and the current consumption of the circuit as a whole is never much more than about 5mA.

CONSTRUCTION

The construction of the Static Field Detector is based on the *EPE* multi-project printed circuit board. This board is available from the *EPE PCB Service*, code 932. The component layout, wiring and the actual size foil master pattern are shown in Fig.3.

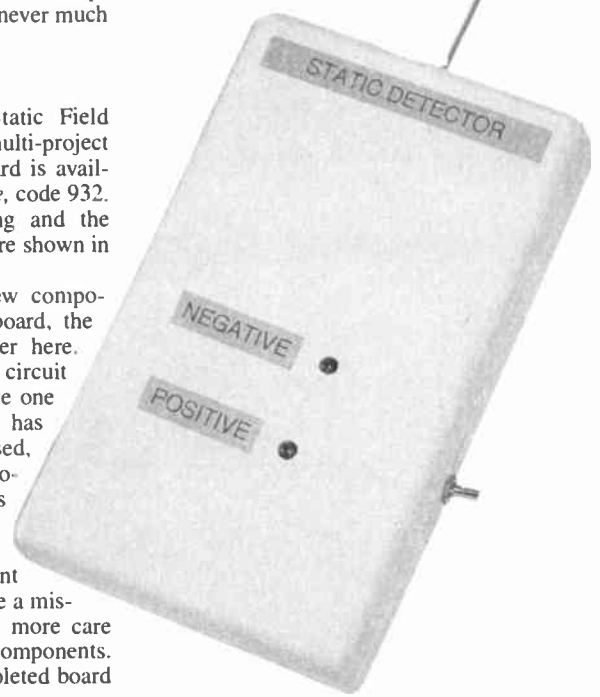
Although there are very few components to fit onto the circuit board, the usual warning is still in order here. Unlike a normal custom printed circuit board, this board does not have one hole per component lead. It has many holes that are left unused, and the small number of components used in this circuit means that the vast majority of them are not used.

The low component count actually makes it easier to make a mistake, so it is essential to take more care than normal when fitting the components. Also, carefully check the completed board for errors.

In all other respects construction of the board offers nothing out of the ordinary. The TL061CP used for IC1 is not a device that is vulnerable to damage from static charges, but it is still advisable to mount it on the board via an i.c. socket.

There are two ways of dealing with the l.e.d.s. One is to mount them in panel holders and then hard wire them to the circuit board. The board should be fitted with single-sided solder pins at the points where the connections to the two l.e.d.s will be made. Incidentally, it should also be fitted with pins at the points where connections will be made to on/off switch S1, the battery, and the antenna.

Finished handheld detector showing labelling of the two "static" l.e.d.s.



The alternative method is to mount the l.e.d.s D1 and D2 on the printed circuit board, and to leave the leadout wires quite long. With the printed circuit board mounted on the base panel of the case, the l.e.d.s will then fit into two 5mm dia. holes drilled at the appropriate positions in the top panel.

Note that l.e.d.s, unlike filament bulbs, will only operate if they are connected with the correct polarity. The cathode (k) lead-out wire is normally shorter than the anode (a) lead. Also, most l.e.d.s have a "flat" on the component's body, next to the cathode lead.

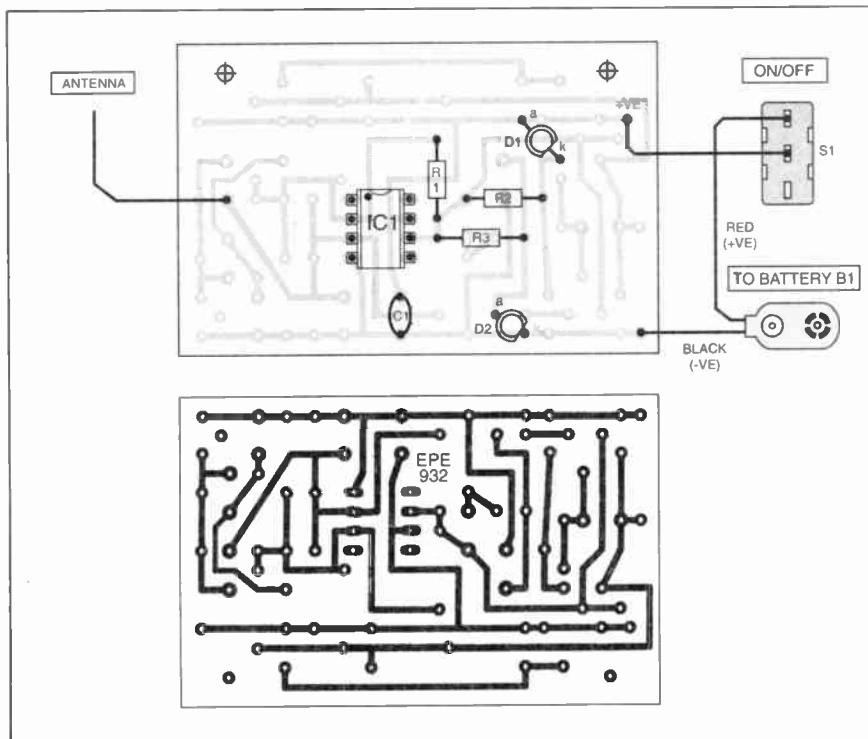


Fig.3. Component layout on the multi-project printed circuit board and full-size copper foil master. Double-check layout as not all holes are used.

COMPONENTS

Resistors
R1 10k
R2, R3 1k2 (2 off)
All 0.25W 5% carbon film

Capacitor
C1 100n ceramic

Semiconductors
D1, D2 5mm panel l.e.d.s, red
IC1 TL061CP (see text)

Miscellaneous
S1 s.p.s.t. min toggle switch
B1 9V battery (PP3 size)

Small plastic case, size to choice; printed circuit board available from the *EPE PCB Service*, code 932; battery connector; stout tinned copper wire for antenna; plastic stand-off pillars or M3 nuts and bolts (see text); single-sided solder pins (3 off); solder, etc.

Approx. Cost
Guidance Only

£10

CASING-UP

Any small to medium size plastic case is suitable for this project. It is best not to use a metal box as it could interfere with the correct operation of the device, and would complicate fitting the antenna.

The completed printed circuit board is mounted inside the case using either plastic stand-offs or metric M3 bolts and fixing nuts. If bolts are used, spacers a few millimetres long must be fitted between the case and the board.

On/off switch S1 is mounted at any convenient point on the case, and a hole about 2mm dia. is drilled in the top side panel of the case, see photographs. This hole is for the antenna, which is merely a piece of tinned copper wire that protrudes about 75mm to 100mm beyond the front of the case. This wire should be fairly thick, but anything from about 0.7mm to 1.6mm (22 to 16s.w.g.) is suitable.

To complete the unit add the battery connector, fit the antenna, and add the wire from S1 to the circuit board.

TESTING

Start with the lid of the case removed so that you have access to the circuit board.

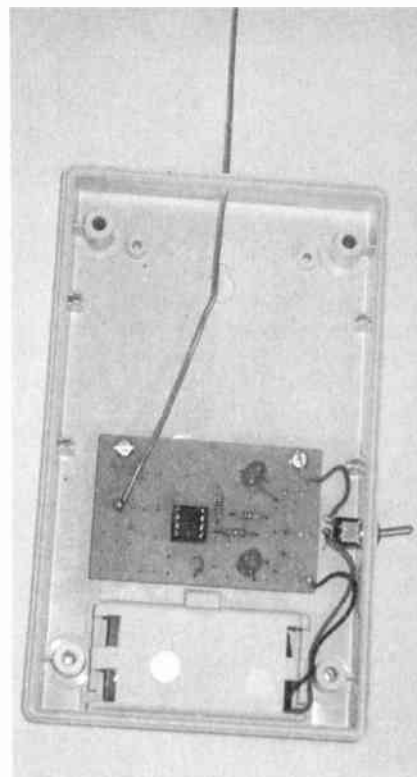
Both l.e.d.s should light up quite brightly when the unit is switched on. Try touching the antenna and the solder pin on the circuit board that takes the connection from the negative (black) battery lead. This should result in l.e.d. D2 switching off and D1 increasing in brightness.

Next touch the antenna and the solder pin that takes the lead from S1. This should have the opposite effect, with l.e.d. D1 switching off and D2 lighting more brightly. If there is any sign of a malfunction switch off at once and recheck the circuit board, etc.

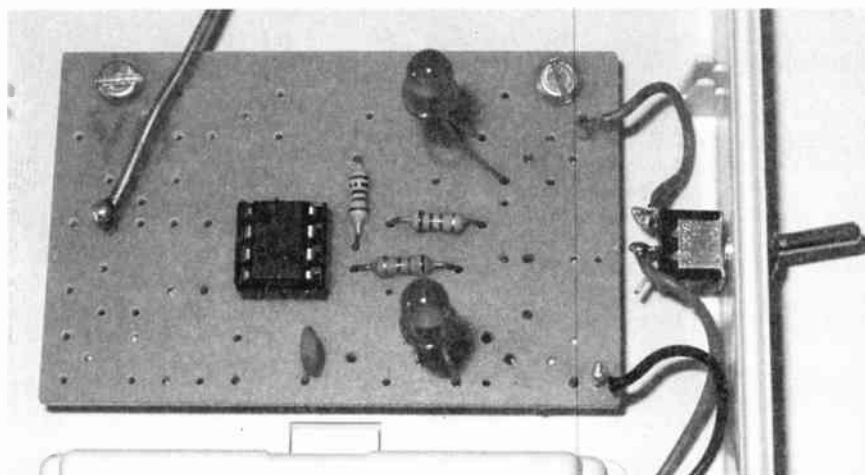
If all is well, refit the lid of the case and make some initial tests with the detector. In general, there is more to detect in a dry atmosphere than in a humid one where charges tend to leak away. Up and down movement will usually produce some change in the display.

Placing the unit near the ground invariably produces a strong positive indication, as will placing the antenna near anything that is earthed. This includes things like the metal case of a computer, a radiator, or the walls of a house.

You can amuse your friends and family by checking to see if they are highly charge



The simple layout of components inside the handheld case.



Completed circuit board. Note the unused holes.

characters, and whether they emit positive or negative energy. Get them to rub their clothes and then try again to see if different results are obtained.

The device used for ICI has built-in protection circuitry that should prevent the input voltage from going outside the range that the unit can handle. If the l.e.d.s seem to get stuck showing a fully positive or negative indication try switching off, waiting a second or two, and then switching on again.

Attempts to deliberately "zap" ICI by placing the antenna near known sources of high static voltages such as television screens proved fruitless. This suggests that the unit is reasonably "zap" resistant, but large static charges can destroy most modern semiconductors, so you try this sort of thing at your own risk.

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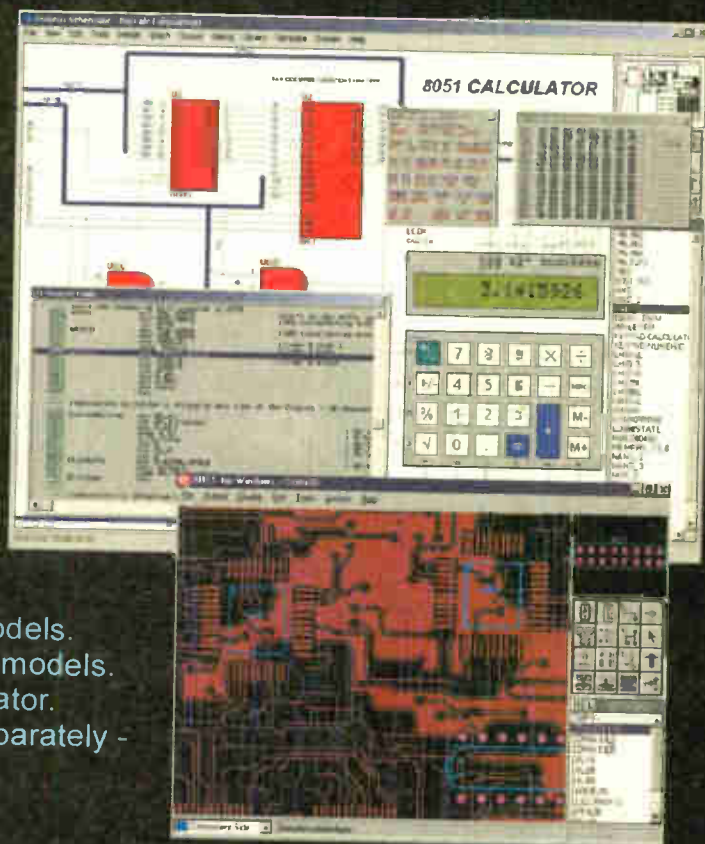
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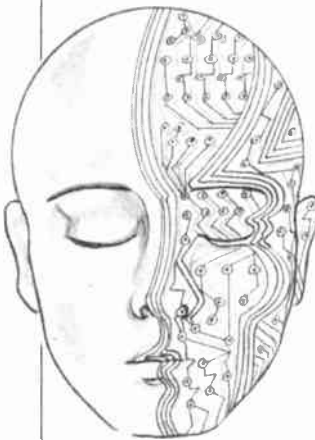
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Car Wash-Wipe Latch – More Delays

FOR cars which have only a simple rear wash-wipe control giving a single sweep of the wiper each time the switch is operated, the latching circuit of Fig. 1 will additionally provide a sweep automatically every few seconds, for use in continuous spray conditions. No extra switches are needed and the normal single-sweep operation can still be used at any time.

In the circuit diagram of Fig.1, IC1a is one half of a 556 dual timer, with the reset terminal (pin 4) connected unusually to the output (pin 5) via resistor R6 to form a latch. This can be set or reset depending on the duration of the wiper switch closure. When power is first applied (probably by switching on the car ignition), capacitor C2 briefly pulls the reset terminal high which enables the timer.

The trigger terminal (pin 6) voltage is low, so the output goes high and maintains the

reset terminal high. The timing capacitor C1 charges from the output via R4, R5 and R6, but because of resistor R7 it does not reach the timer's threshold voltage (two-thirds of the supply voltage).

When the wiper switch is closed, capacitor C1 charges further via resistor R2, and will reach the threshold voltage in about 0.4 seconds, at which point the timer output will go low. (If the switch is opened before this, C1 simply discharges again.) The output then holds the reset terminal low after the wiper switch is opened, and C1 then discharges through R7. The latch remains in this state until the wiper switch is closed again, which takes the reset high and allows the output to go high again.

If the wiper switch is then held closed for more than 0.4 seconds capacitor C1 will have charged above the threshold voltage and so when the switch is opened the

output will go low, resetting the latch. Releasing the switch in less than 0.4 seconds sets the latch.

While the output of the latch is low it enables IC1b, which is an astable multivibrator with a duty cycle of 5 per cent. The inclusion of diode D3 enables the on and off times to be set independently by resistors R9 and R10 respectively. The output at IC1b pin 9 drives the existing transistorised wiper relay to give one sweep every ten seconds.

The circuit is powered from the car battery via resistor R1 and Zener diode D1 which provide a regulated 6.2V. To prevent damage to the i.c. from any voltage spikes from the wiper switch, the signal from R2 is clamped to the regulated supply rail by diode D2.

N. Jewell,
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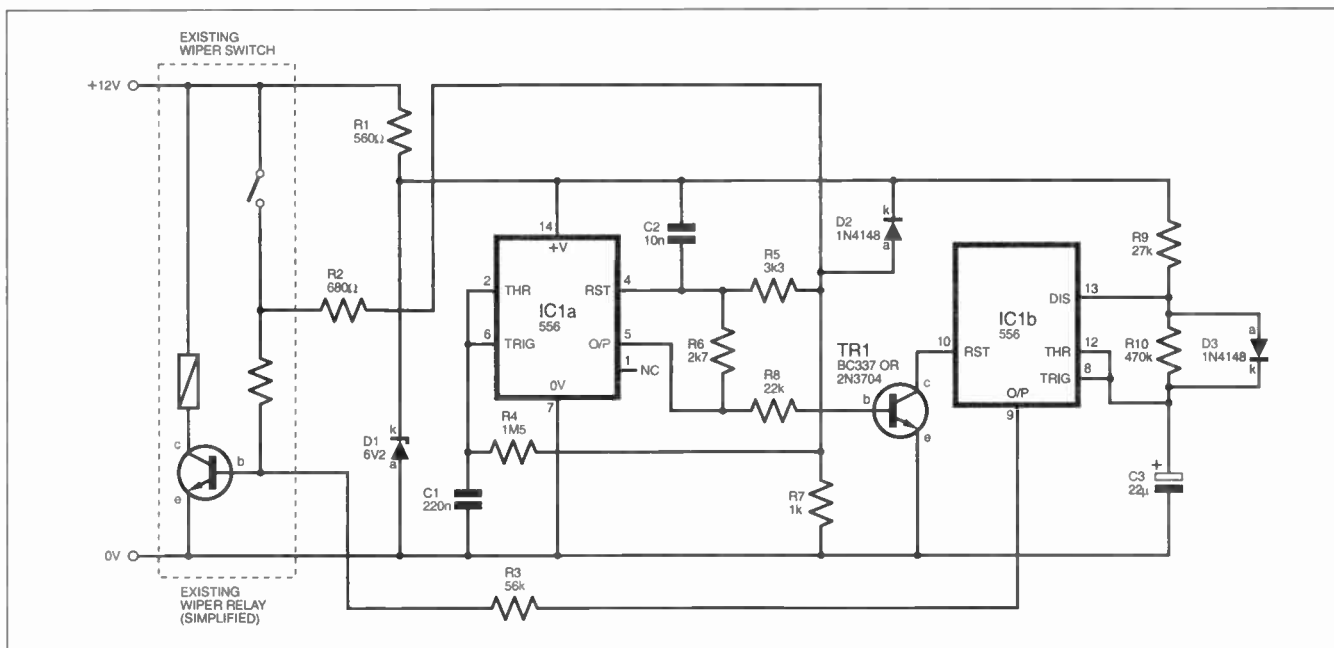


Fig.1. Circuit diagram for the Car Wash-Wipe Latch.

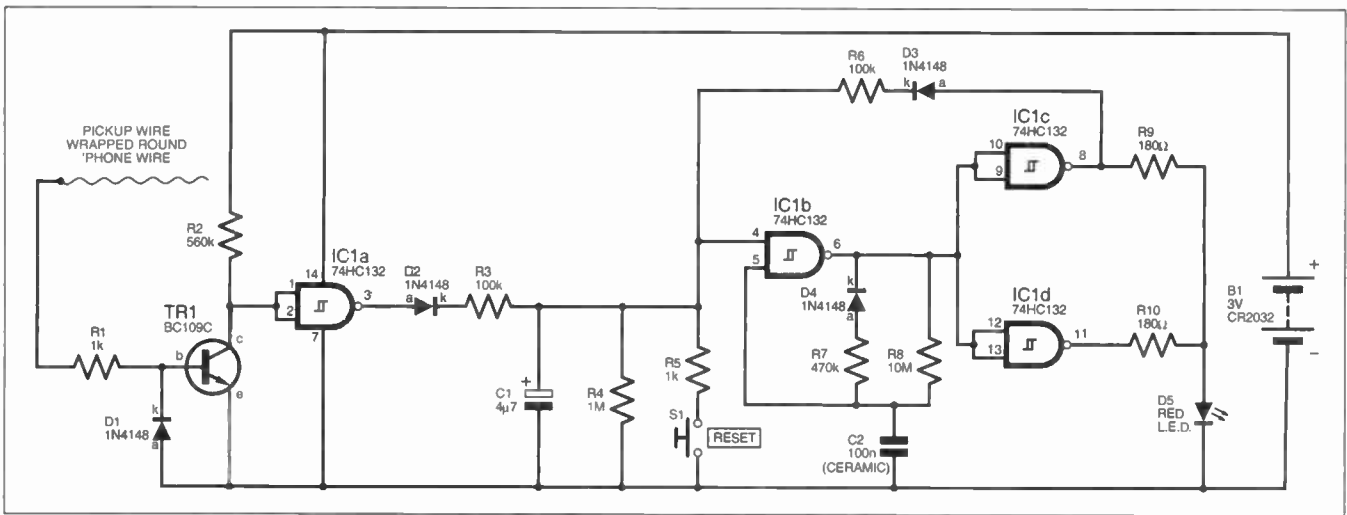


Fig.2. Circuit diagram for a Missed Call Indicator.

Missed Call Indicator – Call Back In A Flash

THIS project can provide an immediate visual indication that a telephone call has been missed. This is particularly useful when services such as the 1471 last call or automated call answer are used, since there is no way of telling if a new call has been missed without actually using the phone to check.

The circuit diagram shown in Fig.2 is designed to avoid the need for any direct connection to the phone line and to be battery operated. When not triggered, the quiescent current is near zero to ensure long battery life.

To avoid the need for a direct connection to the phone line, in accordance with UK regulations, a pick-up wire is instead, wrapped around the wire to the phone. This is connected to a high input impedance amplifier to detect the ringing voltage on the line.

A transistor TR1 with no bias, followed by a Schmitt trigger IC1a, provides sufficient amplification to trigger the circuit from the

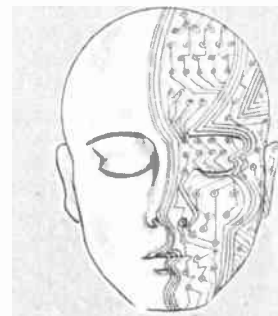
ringing voltage. Unlike more traditional methods of detecting the phone ring with a microphone and amplifier, this method draws negligible quiescent current.

A second Schmitt trigger gate IC1b is used to implement a gated oscillator to generate the l.e.d. flash rate. An RC network formed by R3 and C1 at the input to this gated oscillator helps to prevent false triggering by requiring the equivalent of around three rings before the oscillator triggers. The two remaining gates are used as buffers to drive the l.e.d. and also to provide a feedback signal, via diode D3 and resistor R6, to latch the circuit once triggered.

The project can be built on stripboard and housed in a small plastic case. A 3V lithium cell or two 1.5V cells provides the power supply (B1) and the use of a 74HC series Schmitt trigger ensures that the circuit operates at 3V.

David Corder,
Loughborough, Leics.

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Scissors, Paper, Stone – The Game's Up

AN electronic variation of the Scissors, Paper, Stone game, designed for one player versus a machine, is shown in circuit diagram Fig.3. With pushswitches S1, S2 and S3 open, timer IC2 operates as an astable at approximately 30kHz with its output (pin 3) driving the clock (pin 14) of decade counter IC1. This counts to "3" and then resets so giving three viable outputs "1," "0" and "2".

On closing any one of the switches, one of the l.e.d.s D4, D5 or D6 illuminates and IC2 output is reset by lowering the voltage of pin 4. This stops the astable and the clock of the 4017 (IC1). Now one of the output pins 2, 3 or 4 of IC1 will be held high and the corresponding l.e.d. D1, D2 or D3 connected to it lights up. The machine's "response" to the player's selected diode D4, D5, D6 can therefore be observed.

Diode D7 has a dual purpose. Its primary function is to raise the voltage of IC2 so that when one of the switches S1 to S3 is selected, the input at the Reset pin (4) is low enough to operate the reset function and drive the IC2 output low. It also provides a power-on indicator.

George A. Vicary,
Swayfield,
Grantham.

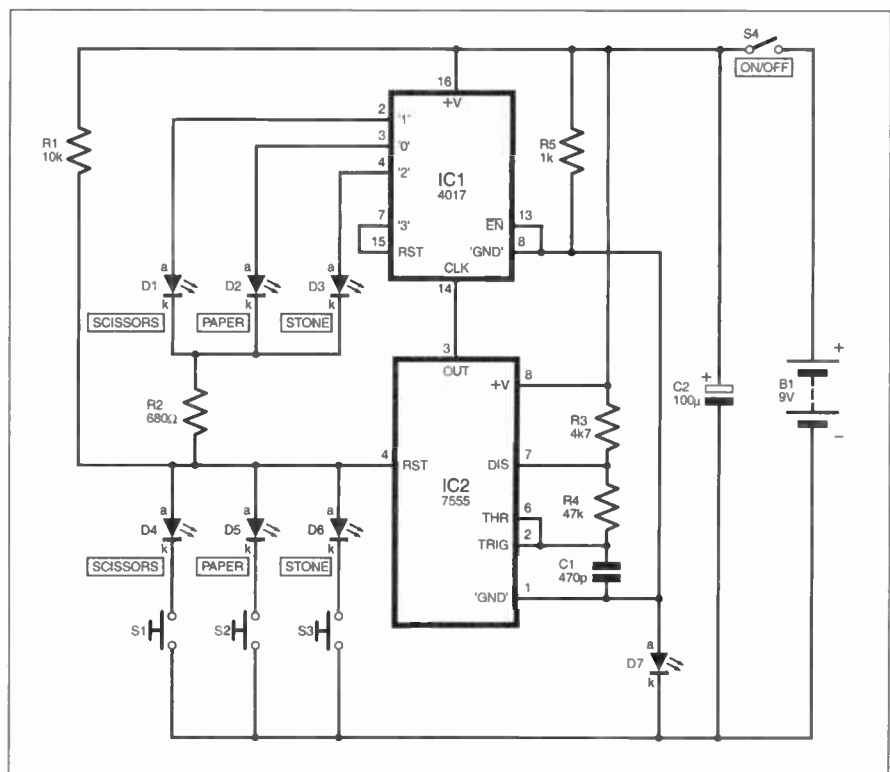


Fig.3. Scissors, Paper, Stone game circuit diagram.

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READOUT

E-mail: editorial@epemag.wimborne.co.uk

John Becker addresses some of the general points readers have raised. Have you anything interesting to say? Drop us a line!

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★ LETTER OF THE MONTH ★

BASE-32 CODE

Dear EPE,

Your extension of base-16 (so-called *hexadecimal*) code to provide a compact date/time code for file names in the *PIC Dual-Channel Virtual Scope* project (Oct '00), is easily extended further to provide a full base-32 code and allow even more compactness.

As you point out, the month requires only one digit in base-16 code, and by extending the code as you have done, hours from 0 to 24 can also be represented by only one digit. With a further extension of the code to base 32, the day of the month could also be represented by only one digit, and three digits would suffice to represent any year up to 32,767 AD.

I have been using such a base-32 code for some time, with some modifications (dare I emulate Microsoft and call them *enhancements*?) that I find useful. These are:

1. Leaving out I (eye) and O (oh), which can be confused with 1 (one) and 0 (zero)

2. For those, like me, who prefer to use lower-case letters, also leaving out l (el), easily confused with 1 (one).

So with the above, the code sequence is: 0 to 9, a to f (as in the commonly used base-16 code), g h j k m n, p to y. This leaves z for use as a dummy symbol.

3. I considered devising a base-64 code to represent minutes and seconds, but decided that it would be more bother than it was worth, so like you I have retained base-10 for these.

4. The most significant digit is placed first, i.e. the date and time are expressed as year + month + day + hour + minute + second. This simplifies sorting and arithmetical operations.

5. The code may have uses beyond incorporating date and time information in file names. So for, say, astronomical events, dates BC are expressed by prefixing a minus sign, and the BC/AD discontinuity smoothed out by assigning 000 to 1 BC. Examples:

1. My E-mail user name (pk1V7) comprises my initials and my birth year (yes, 1927 in base-10 code) 1x0 means 1984; this year (2000) is 1xg = 1984 + 16

2. The inventor of the Julian calendar made his first attempt to invade Britain in -01p

To conform with the DOS file-name and extension format of up-to-eight + up-to-three characters, the code for the seconds is placed in the extension. This leaves room to include the Admiralty time-zone designation as well: z for GMT, a for BST, k for my part of Oz. Your example of 7 Sep 2000 at 1:37:13 a.m., which you code as 07913713.Y00, then becomes 1xg97137.13a

The advantages of this coding scheme, it seems to me, are its generality and its versatility. There can be a trade-off between time span and precision, with the code truncated at one or both ends to suit the application, thereby leaving room within file-naming conventions for other information.

**Peter Kelly,
Woombye, Queensland, Australia**

Many thanks Peter for raising this discussion. Your comments are interesting for several reasons.

First, you highlight the problem of differentiating between several characters. It has long bugged me that some programmers insist on using the letter l (or i) as a variable name. For example: FOR A = I TO K, which if seen in print could be taken as FOR A = (ONE) TO K when in fact it means FOR A = (the value stored in the variable represented by letter I) TO K, a misinterpretation that could have a profound effect on the successful running of the program.

To me, programming use of characters i, l and l (eye, EYE, el) should be prohibited by cosmic edict (or at least common sense)! I'm currently getting to grips with VisualBASIC and even in its demo software there are frequent instances where I am not immediately sure which of the three characters is meant. (Oh, alright, I've been known to use them myself in my own software!)

Incidentally, my OCR scanner can also be confused by these characters, plus ' / \ and ! (lefthand single quote, apostrophe, forward slash, backslash, exclamation) and 5 and S (five, ESS).

You are quite right about using base-32 for coding. For characters that have to be read by a human eye, as in a file name within a computer directory (folder) for example, base-32 seems a reasonable limit. However, if it is only the computer that has to read coded data, almost the full extent of base-256 can be used.

Some years ago, I wrote a fixtures allocation program for a local Sunday football league. This was written for a very low powered machine (Commodore PET) and to economise on memory space (32K bytes) I succeeded in coding each data item within single bytes, one each for date, venue, teams, home/away, score points etc. These bytes only needed to be read by the computer from a single string of characters within a data file, it then translated them according to calculation and lookup tables.

Whilst a few ASCII values within the possible 0 to 255 range could not be used (comma, semicolon, ASCII 0 and 13, for instance) because the computer had its own ideas of their use in a string of characters, most could be used, and were.

*On using the file name extension for seconds coding, I avoided this option in order to simplify file name searching (and possible interpretation by the computer as having a different significance). Using the dot-suffix of .Y allows a more ready search for file types. For example keying in DIR *.Y0? immediately calls up all PSCOPE (and VSCOPE) files for the years 2000 to 2009 (I don't think I actually coded the year number).*

Lastly, I was interested to read that time zones have officially (Admiralty) allocated letter designations.

BASIC AND DELPHI

Dear EPE,

I would like to make some comments on replacements for GWBasic, QBASIC interpreters and the QuickBASIC compiler for use in simple interfacing projects, and to comment on Delphi.

FirstBASIC, which is shareware and for DOS, can be downloaded from www.powerbasic.com and registered within the UK for £30.55, see www.greymatter.co.uk for this. It is a very good BASIC compiler with a simple Integrated Development Environment (IDE) and, in the registered version, on-screen help.

Like QBASIC and QuickBASIC it has all the constructs for structured programming and the syntax is easy to learn, but the IDE does not support the use of the mouse. See PowerBASIC website for comparisons and some help with translating between Basics.

Having written a number of large programs in QuickBASIC for student use, some of which need to write to or poll the printer port to examine the hardware connected to it, I'm now trying to move them to Delphi. I think this is essential if I'm going to be able to run them under future operating systems.

Until recently all the books and articles I have come across have concentrated almost exclusively on the "components" used to build the various types of windows. This approach quickly allows you to build a "gee whiz" user interface, but to use Delphi seriously it is necessary to learn to use Object Pascal, which in turn requires an understanding of standard Pascal program structure.

Computer Programming in Pascal by David Lightfoot, *Teach Yourself Series*, is old, 1983, but adequate for understanding Pascal program structure. *Delphi in a Nutshell* by Ray Lischner is a new, 2000, desktop quick reference to Object Pascal, and is very comprehensive, 560pp, but it assumes some knowledge of Pascal. It also mentions that Delphi is being ported to Linux.

Delphi 1 is still available from Greymatter for £57.68p (see above) as *Learn to Program with Delphi 1*. This is a thick, 900pp, self-study manual and CD-ROM containing Delphi 1. It covers both the components and the Object Pascal language, though you have to dig a little to find what you want to know about the latter and there are some mistakes and ambiguities in the text. The "hidden gem" tucked away on the CD-ROM is the 300 page Object Pascal manual which can be printed from Adobe Acrobat.

Having no previous knowledge of Pascal, I almost gave up on Delphi because I could not figure out how to store, retrieve and manipulate data. Now I'm hooked. I've concluded that a rule of thumb is to ignore any book with less than 300 pages as it will be too superficial. It's a steep learning curve and I'm still in the foothills, but I'm still climbing!

**Dr Les May,
Rochdale, Lancs**

Interesting. Thank you Dr Les. It's an aspect that some readers may find it worthwhile looking into. Personally, I'm now just about coming to grips with VisualBASIC 6 and, despite finding the documentation inadequate, believe that this, with its Windows base, is the route to pursue.

BCD CHALLENGE ACCEPTED

Dear EPE,

I was very intrigued by the Binary to BCD conversion routine given in September 2000 *EPE*, as I had always seen this done by some method involving division by ten.

After a lot of thought, I managed to come up with what I think is an improved version. The procedure used to do the conversion is: "Start with a Partial Result (PR) of zero. For each bit in the binary, starting at the left hand end, multiply the PR by two and add the bit."

By doing this arithmetic in decimal, the PR at the end has the converted value which holds the digits as binary coded decimal (BCD) in the lower four bits of each of a succession of bytes, bits 4 to 7 are zeroes (Unpacked BCD). You could also, with a different program, use Packed BCD with two digits in a byte, one in each nibble.

Throughout the process, decimal adjustment (DecAdj) of the PR is necessary to maintain its BCD nature, so that 0 to 9 are unchanged but a result in the range 10 to 15, which is stored as hex 0A to 0F, is converted to 0 to 5 with a 1 carry ready to go in the next BCD, i.e. 0A to 0F become 10 to 15 hex.

The actual process is to add six to the unadjusted result. If this causes a 1 in the fifth bit (bit 4) then the changed pattern is used, other-

wise the original unadjusted pattern is retained. For Unpacked BCD the state of bit 4 can be used as the test.

The algorithm in Sep '00 uses "Add 3" before the "times 2" shift. This is best when Packed BCD is converted since for the "top" nibble there is no bit corresponding to bit 4. By "Adding 3" before the shift instead of "Add 6" after, the same effect is obtained using bit 7. However, in this case the carry into the decimal cannot be done until after the shift, hence the two passes through the digits for each bit.

The following is a version using one pass, for Unpacked BCD. I have used the locations BIN0 to BIN2 to hold the three bytes of binary, with the most significant (m.s.) byte in BIN2. The PR goes in the eight bytes DIGIT0 to DIGIT7, with the m.s. digit in DIGIT7. BINCNT and DECCNT hold counts for the two nested loops.

Harry West, via the Net

Congrats on picking up the challenge Harry! In fact I'd already seen in our Chat Zone that you'd been in contact with Peter Hemsley (who started it all off) and that he'd accepted your improvement. Well done. You now hold the BCD Place of Honour - can you be deposed we wonder? Well, readers, what do you think?

BINDEC:

CALL CLRDIG : Clear decimal digits
MOVLW 24 : Decimal count
MOVWF BINCNT

BITLP:

RLF BIN0,F : Shift binary left
RLF BIN1,F
RLF BIN2,F
MOVLW DIGIT0
MOVWF FSR
MOVLW 8 : Count for the decimal digits
MOVWF DECCNT
MOVLW 6 : The Working Register holds 6 throughout. For each bit the inner loop is repeated 8 times, with shift in of the next bit, "times 2" and DecAdj of each digit

ADJLP:

RLF INDF,F : 2*digit, then shift in "next bit" for DIGIT0 or else the carry from the previous digit
ADDWF INDF,F : Add 6, clears Cf and gives 1 in bit 4 if the

BTFSS INDF,4 : addition is needed; zero if not, when

SUBWF INDF,F : we subtract it again. Sets Cf

BSF STATUS,C : Cf could be 0 or 1, so make it 1 as default

BTFSS INDF,4 : Bit 4 is the carry to the next digit

BCF STATUS,C : Reset Cf to zero if bit 4 is clear

BCF INDF,4 : For BCD clear bit 4 in case it's one

INCF FSR,F : Go to next digit, (Cf not affected)

DECFSZ DECCNT,F : End of inner loop. check digit count and

GOTO ADJLP : round again if it's not zero

DECFSZ BINCNT,F : End of outer loop, one pass through digits.

GOTO BITLP : check bit count and repeat if necessary.

RETURN

E-MAIL VIRUSES

Dear EPE,

Barry Fox's article in *News* of September '00 raised the question of whether a virus can hide in plain text E-mails. He is essentially correct in saying that a computer-executable program cannot be transmitted through a text-only E-mail.

However, viruses are more than just computer programs. A virus is an entity that uses its host to replicate itself. If a text E-mail simply says "Copy this E-mail to everybody you know", it is a virus. It utilises the human user as the host to replicate itself. In 1994 an E-mail virus "Good Times" infected thousands of people's E-mail systems, as detailed in <http://www.mdfnsnet.f9.co.uk/Docs/Comp/Viruses/GoodTimes>.

It was essentially a chain letter containing a hoax warning about a virus, recommending that the reader E-mail it on to all their friends. As Clay Skirky on alt.folklore.urban put it: "It works by finding hosts with defective parsing apparatus which prevents them from understanding that a piece of E-mail which says there is an E-mail virus, and then asking them to remail the message to all their friends, is the virus itself."

P.S. A super computer is a machine that runs an endless loop in just two minutes.

Councillor Jonathan G. Harston,
Sheffield, via the Net

Editor Mike comments that we recently received a "self-executing" virus of the type you refer to. It trusted the user to delete all the files on his hard disk and then send on the E-mail!

We wonder whether by publishing your letter through so many thousands of EPE copies that it too has persuaded human hosts to perpetuate it as a virus?

NEW ELECTRONICS eGROUP

Dear EPE,

I wish to inform you of a new electronics egroup which has been set up specifically to address the needs of persons involved in all forms and branches of electronics in the UK, but particularly enthusiasts and students, whatever their experience. The main emphasis is on the sharing of information, designs, advice and support.

Further information, and joining instructions can be found at:

www.egroups.com/group/Electronics-UK, or from: Electronics-UK-owner@egroups.com.

I warmly invite your friends and colleagues to join.

Ross Currie,
Belfast, Northern Ireland, UK,
via the Net

Thanks Ross. We hope readers will flock to join your worthwhile enterprise.

BINARY TO DECIMAL

Dear EPE,

Thanks for publishing Peter Hemsley's BINDEC routine in Sept '00 *Readout*. However, the second instruction could better be written `MOVLW D'24'`. If the default radix happens to be hexadecimal, as in MPASM, the program won't work right as written unless the radix is changed to decimal.

Stan Ockers, via the Net

Thanks Stan. Yes, that would be the case with MPASM, although TASM automatically recognises the value as decimal, not having a facility for setting the radix. In TASM, hex is expressed with a \$ (dollar) symbol before the value.

DATA SHEETS

Dear EPE,

I refer to *Readout* of Sept '00 and Roger Nightingale's query regarding data sheet availability on the web. Since I work in a computer workshop at the University of Dundee, information is a prime requirement to efficiency and fault finding and data sheets are crucially important. Having 24 hours a day access to the web I have been able to find numerous sites for data sheets but none to rival the one at www.bgs.nu/sdw/a.html.

If Roger can't find his required data sheet on this site, then he is in deep trouble.

Sandy Smith,
Dundee, via the Net

Most useful info Sandy, thanks.

PIC PULSOMETER

Dear EPE,

You published my *PIC Pulsometer* project in the Nov '00 issue. It was written in TASM and I owe you a word of thanks. This was my first PIC project, and your *PIC Tutorial* (Mar-May '98) and excellent *Toolkit Mk1* (Jul '98) programmer gave me an easy route into picking up the basics to add to my previous if different experience.

Richard Hinckley,
Congleton, Cheshire

Thank you Richard. We are sure that many readers will appreciate the result your efforts! Why not give Toolkit Mk2 (May-Jun '99) a try now? It has even more facilities and the software has been updated again (see Nov '00 issue).

ANTI-TAMPER LOOP

Dear EPE,

In the application of Alan Bradley's *Anti-Tamper Loop Alarm* in *Ingenuity Unlimited* Oct '00, particularly when being deployed for the protection of a bicycle, motorcycle or car steering wheel, a good practice is to use coaxial cable such as RG58 or similar for the loop. This cable is then threaded through a chain with links of a suitable diameter, leaving several links at either end for the purpose of securing the chain with a padlock.

The cable may then be terminated with BNC connectors, which offer not only good connection reliability, but also, from the point of reducing false alarms, would be unlikely to become inadvertently disconnected through, for example, vibration or innocent, inadvertent movement of the protected item.

In this situation, good security is provided by not only having the security factor of the loop alarm, but also the physical security of the chain, which, if the loop is assembled within it correctly, will be very difficult to cut without cutting the loop and therefore activating the alarm. It also restricts access to the loop for bypass measures.

Ross Currie,
Belfast, Northern Ireland,
via the Net

Ah, hello again Ross! As a cyclist (in good weather only!) I agree with your suggestion. Also see Please Take Note this month.

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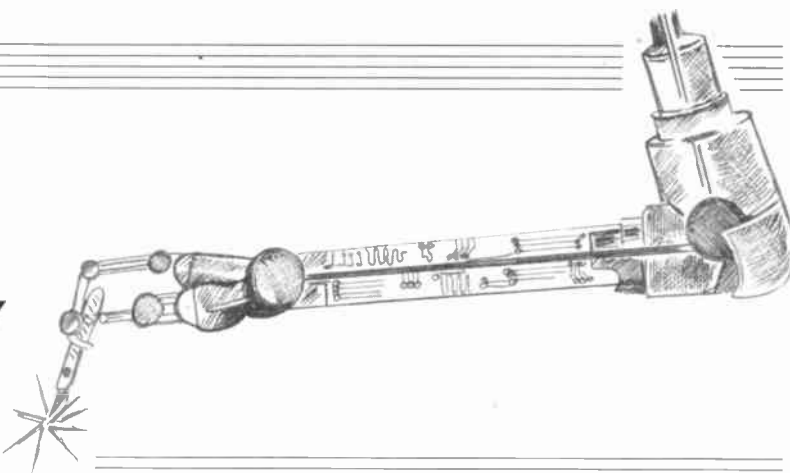
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CIRCUIT SURGERY

ALAN WINSTANLEY
and IAN BELL



Our intrepid circuit surgeons explore switched-mode power supplies, whilst under heavy sedation!

Switched Mode Supplies

Richard Torpey of Merseyside writes by E-mail, asking for advice about designing step-up voltage regulators:

A friend of mine has recently asked me to construct a microphone pre-amplifier for portable use, running from a 9V PP3 cell. The microphone he's using requires a +48V (in practice, anything from +40V to +50V) supply (phantom powering) to operate.

I have identified a circuit which provides the necessary gain and d.c. blocking to satisfy this function, and it runs quite happily off a bench PSU. However, I cannot find an easy solution for obtaining a +48V supply.

A method considered was generating a sine wave to feed a step up transformer primary, and rectifying and smoothing the secondary output, but finding a suitable transformer for this application seems difficult.

I have heard of a solution involving use of a "Cockroft Ladder" voltage-doubling network, but am unsure as to how such a system would be put into practice for the desired application. The most important considerations are, primarily, obtaining a clean, steady +48V output, and also efficiency to preserve battery life. Current consumption will be in the order of milliamps.

As Richard indicates, there are a number of possible approaches to this problem. We will look at a number of solutions to step-up voltage converter design in general over the next couple of months, hopefully Richard will then be able to select a circuit suitable for his application. But before we start, let's check some basic concepts.

Efficiency is often a key parameter in power conversion. Power is given by voltage multiplied by current ($V \times I$), so if a power converter is 100 per cent efficient then $P_{in} = V_{in}I_{in} = P_{out} = V_{out}I_{out}$. If the converter is less than 100 per cent efficient, then P_{out} will be less than P_{in} by the efficiency factor.

In this application we need a high efficiency so that the battery is not drained too

quickly. The ideal situation of $V_{in}I_{in} = V_{out}I_{out}$ also shows us that if we increase the voltage ($V_{out} > V_{in}$), the current available at the output will be proportionally less than I_{in} . With a perfect converter, 5mA at 48V output would draw 27mA from a 9V input. A real converter would draw more current, which should be borne in mind when considering battery life.

Regulation

Another important power supply specification is regulation. In fact, there are two factors to consider here – *line regulation* and *load regulation*. Line regulation indicates how much the output voltage changes as the input voltage changes, and it's calculated using:

$$\text{Line regulation} = \frac{V_{out} \text{ at max input} - V_{out} \text{ at min input}}{V_{out} \text{ required}} \times 100\%$$

Load regulation indicates how much the output varies with varying load and is calculated using:

$$\text{Load regulation} = \frac{V_{out} \text{ at 50\% load} - V_{out} \text{ at full load}}{V_{out} \text{ required}} \times 100\%$$

There may be a small a.c. signal superimposed on the d.c. output of a supply. This is known as a "ripple voltage" and is usually expressed simply in volts, but could also be given a percentage of the supply voltage.

Richard suggests the use of a sine wave generator feeding a transformer as a possible approach. The transformer provides the voltage step-up in accordance with its turns ratio and must be driven by a varying voltage (only a.c. signals are coupled to the secondaries of transformers).

In mains power supplies the input to the transformer primary is a 50Hz or 60Hz sine wave, depending where you live. For d.c. to d.c. "converters" (a power supply circuit that raises a lower d.c. voltage to a higher one), neither a sine wave nor a frequency as low as this need be used. Higher frequencies enable smaller transformers to

be used, and furthermore if it operates above audio frequencies, then it will allow for silent operation (otherwise some transformers may emit an annoying whine or whistle).

Pulsed inputs to the transformer (or other type of inductor) are commonly used in "switching power supplies" (ones which use an oscillator to generate pulses which can be converted into a higher voltage output), as they are relatively easy to generate using control logic. This logic often uses pulse modulation (switching pulses on or off, or modifying their length) to control the output voltage as the load varies.

Royer Converter

A classic power converter circuit in which the transformer input is a switched waveform rather than a sine wave, is the "Royer Converter" described by G.H. Royer in 1954. This is shown in its basic form in Fig.1.

The circuit is self-oscillating, with feedback provided from a transformer winding. The oscillation is "square wave" in nature rather than sinusoidal because the transformer is driven into saturation (an appropriate transformer must be used to achieve efficient operation).

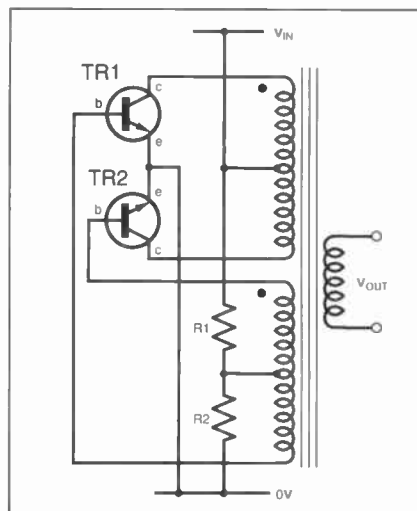


Fig.1. Basic Royer power converter.

The resistor network provides bias and ensures that the circuit starts oscillating when power is applied. The transistors switch on and off out of phase with each other, with a duty cycle of 50 per cent. The voltage induced in the secondary winding depends on V_{in} and the transformer turns ratio. Appropriate transistors should be used which have a high gain (h_{FE}), low $V_{CE(sat)}$, low on-resistance ($R_{CE(sat)}$) and high collector-base breakdown voltage. Transistors specifically designed for high current switching applications should be used.

In Fig.2 is shown a modified Royer converter based on a circuit from a design note by Zetex (www.zetex.com), who are renowned for high current, high performance transistors including the ZTX650, ZTX849 and ZTX449, which are suitable for use in these circuits. The circuit is a slight modification of Fig. 1, which itself does not need a centre-tapped feedback winding.

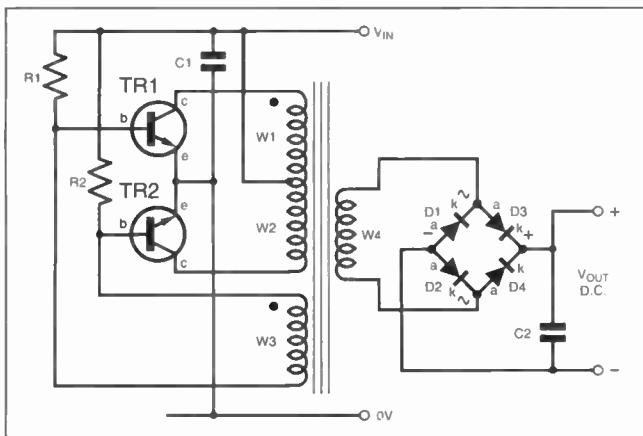


Fig.2. Royer step-up d.c. to d.c. converter.

The Zetex circuit uses two ZTX449 transistors, two 560 ohm resistors and two ceramic 100nF decoupling capacitors, together with a suggested toroidal transformer, with windings W1 and W2 (primary) having 10 turns, W3 (feedback) at 4 turns and W4 (secondary) at 28 turns. Note that this circuit has not been proven by us: if you decide to wind your own toroid, simply wind the correct number of turns using as thick an enamelled copper wire as can be accommodated by the ferrite core.

The output is 12V at 2W from a 5V supply at 77 per cent efficiency, and has an operating frequency of over 80kHz. Increasing the input voltage or number of secondary windings will give a higher output voltage (adjust the resistors and capacitors to suit).

Other Zetex switching transistors (or equivalent) may be used in more demanding versions of the circuit. We have shown the rectifier and smoothing capacitor in Fig. 2, but will not do so in all the circuits in order to save space. As Zetex says, circuits like this look deceptively simple, but many components interact in a complex way.

Having said that "the use of sinewaves is not necessary", there are step-up converters that *do* use sinusoidal oscillation, which can be useful at times. The use of sinewaves cuts the level of harmonics of the basic switching frequency, which can otherwise be responsible for radio frequency noise and

interference (r.f.i.). A modified Royer circuit, in which sinusoidal operation occurs due to the presence of the inductor L1 and capacitor C1, is shown in Fig.3.

Switch Mode

Finding a suitable transformer for a particular step-up power supply design can be very difficult; it is possible to wind your own transformer using the various ferrite core kits etc. which are sold for this purpose, but this adds another dimension to the design problem that not everyone would want to tackle. Useful results can often be obtained by experimenting to optimise the circuit.

However, it is not necessary to use a transformer to produce a step-up converter – some switch mode power supply (SMPS) configurations only require an inductor, and certain voltage multipliers and charge-pump circuits achieve step-up neatly by using capacitors (but voltage multipliers are usually driven from a transformer secondary). We will look at each of these options next, and also in next month's column.

An example SMPS circuit, using a National Semiconductor LM2586-ADJ device, is shown in Fig.4. An SMPS design is often regarded as being quite difficult – which is true if you do not follow manufacturer's design guidelines, and also because they are demanding circuits requiring the use of appropriately specified components together with high quality construction. At this point it should be mentioned that the higher voltages generated with ease by these efficient circuits must be treated with due respect, using suitably-rated parts, with good insulation and reasonable standards of assembly.

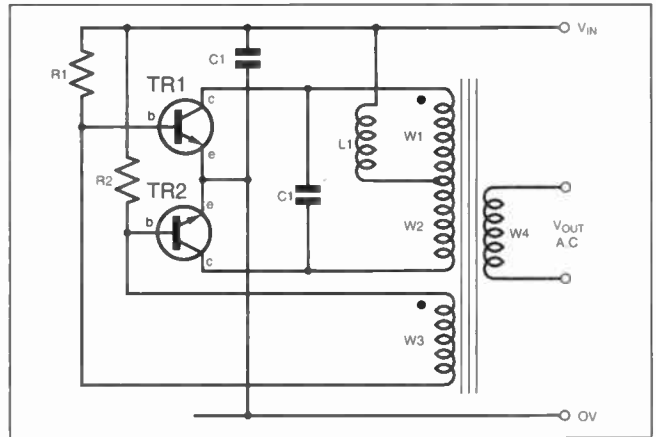


Fig.3. Version of Royer circuit with sinusoidal operation.

Design On-line

Rather than struggle, we took the easy, modern route and obtained this circuit using on-line tools available on National Semiconductor's Power Web Site at www.national.com/appinfo/power/. This is a particularly interesting site which allows you to design and simulate SMPSs on-line using National's versatile WebBench^(tm) and WebSIM^(tm) tools. The web site even allows you to organize your designs with secure password protected storage. Design details including your specifications, bill of materials, schematic, and simulations results are stored on the server, and are available on-line.

Note that the WebSIM simulation tools are installed on a server owned by National Semiconductor, not on the user's machine, as would be the case with most simulators. The user gains access to the simulator using a browser, executing the simulation on the server instead of on their own machine.

This enables very large amounts of computing power and memory to be used by the simulator environment. The simulation tools can be constantly upgraded, ensuring that users always have the most up-to-date version.

We created an SMPS for an 8V to 10V input and 47V output using National's on-line tools, and we simulated the steady state output from the circuit using WebSIM to obtain the results shown in Fig.5. Note that there is about 400mV of ripple on the supply. Using a large value for C_{out} could reduce this.

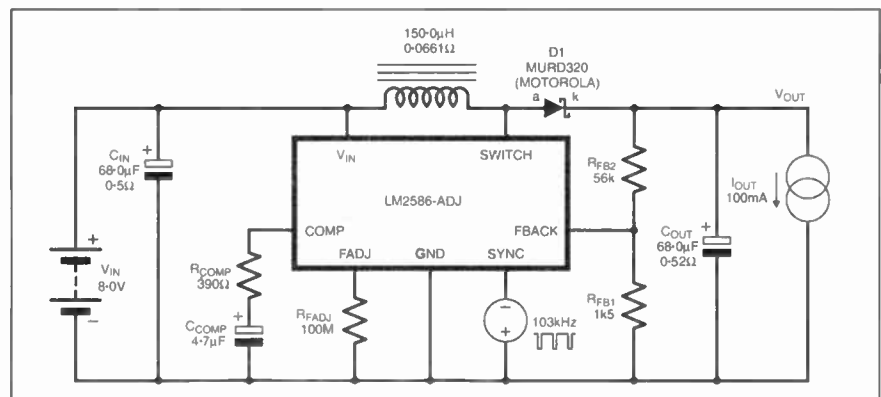
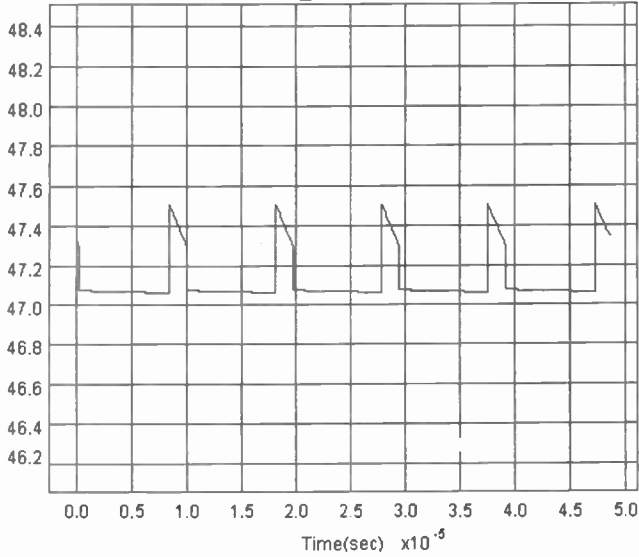


Fig.4. Switched-mode step-up converter for 8V to 10V input giving 47V output. Courtesy National Semiconductor's web site (see text).

Probe V_OUT Waveform



This SMPS may not be very appropriate for the reader's application though (which calls for a few milliamps only) as its efficiency will be impaired at low output currents. This circuit is not particularly suitable for output currents less than 100mA. However, we did not attempt to find an optimal SMPS for this purpose.

As we said earlier, SMPS circuits are demanding on the components used: you cannot use any old diode from the junk box for D1 for example – you must use a suitable high-speed power switching diode. SMPS and similar circuits tend to destroy low frequency rectifier diodes such as the 1N4001 very quickly. In switched-mode power supplies, circuit capacitors must be able to stand high pulse currents and also have a low effective resistance, and inductors should have low resistance as well.

Next month, we'll take a good look at voltage multiplier circuits which use diode-capacitor networks. These are often used where a higher d.c. voltage is needed. For many users, they form a more practical proposition than a demanding switched-mode power supply. *I.M.B.*

Fig.5 (left). Simulation of the steady state response of the SMPS circuit in Fig.4 using National Semiconductor's WebSIM™ on-line simulator.

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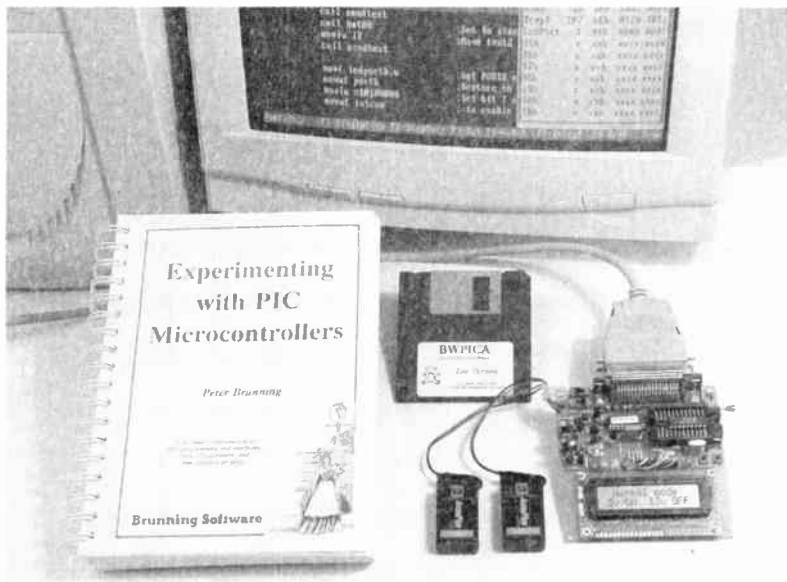
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Experimenting with PIC Microcontrollers

This is the easiest way to start programming and interfacing the PIC16F84 and PIC16C711 microcontrollers. The system consists of the book, a programmer/experimental module, and an integrated suite of programmes to run on a PC.

The importance of the information being in a real book rather than given as computer files cannot be over emphasised. The book lies open on your desk while you use the computer to work through the experiments, but that is just the beginning of the ease of use of this system. We start with the simplest possible experiment and the book gives step by step instructions. As we finish typing each line it is tested by the programme to ensure that it can be assembled and the machine code is displayed at the top right of the screen. Then without leaving the programme we assemble the text into PIC code, and use the simulator to single step the programme. Watching the data in the registers change and seeing this in decimal, binary and hexadecimal numbers at the same time solves the problems at a stroke. You see it happen and understand what you have done, and when our programmes use the alphanumeric liquid crystal display the simulator shows what will be displayed. Then without leaving the programme we write the code into the test PIC and run the programme in the real world. BWPICA does it all there are not three programmes to continually swap between.

The 24 experiments are all performed using the programmer/experimental module. Flashing LEDs, text display, real time clock, period timer, beeps and music, including a rendition of Beethoven's *Für Elise*. Then there are two projects to work through; building a sinewave generator covering 0.2Hz to 20kHz, and investigating measurement of the power taken by domestic appliances. The system works through from absolute beginner to experienced engineer level.

Programming PICs

The assembler understands normal PIC terminology. This has two distinct advantages for beginners over the usual system; it is not necessary to start programmes with a list of definitions, and the assembler recognises errors such as *call intcon* because it knows that *INTCON* is a register not a subroutine name.

The programmer module uses its own PIC to control the timing and voltages required to programme the test PIC. The programming is performed and verified at normal 5 volts, then verified with $\pm 10\%$ volts applied to ensure that the device is programmed with a good margin and not poised on the edge of failure. The system will also programme similar PICs (83, 710, 71, 620, 621 etc).

The module is supplied with a test PIC fitted, and requires two PP3 batteries which are not supplied.

Assembler

The first book *Experimenting with PC Computers* with its kit is the easiest way ever to learn assembly language programming, simple circuit design and interfacing to a PC. If you have enough intelligence to understand the English language and you can operate a PC computer then you have all the necessary background knowledge. Flashing LEDs, digital to analogue converters, simple oscilloscope, charging curves, temperature graphs and audio digitising.

C & C++

The second book *Experimenting with C & C++ Programmes* uses a similar approach. It teaches the user to programme by using C to drive the simple hardware circuits built using the materials supplied in the kit of parts. The experimental circuits build up to a storage oscilloscope using relatively simple C techniques to construct a programme that is by no means simple. When approached in this way C is only marginally more difficult than BASIC and infinitely more powerful. C programmers are always in demand. Ideal for absolute beginners and experienced programmers.

The Kits

The kits contain the prototyping board, lead assemblies, components and programming software to do all the experiments. The 'made up' kits are supplied ready to start the first experiment. The 'unmade' Kits require the prototyping board and leads to be assembled and soldered before you can start. The 'top up' kit CP2t is for readers who have purchased a kit to go with the first book, and contains all the components and programming software but not the prototyping board or leads. The kits do not include the book.

Hardware required

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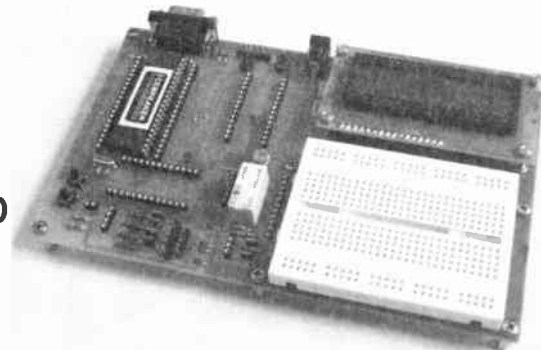


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Christmas Project

TWINKLING STAR



BART TREPAK

Add a "sparkle" to your festive decorations.

If you are tired with the old star which usually decorates your Christmas tree and would prefer something more eye catching, then this Twinkling Star circuit could be just what you need!

The star itself can be bought or made from tinsel stuck onto cardboard or a sheet of polystyrene sprayed gold or silver, or indeed simply your old star if it is suitable. The details of this are left to you, this article concerns itself only with the electronics!

TWINKLE TWINKLE

The easiest way to add some interest to the star is to simply light it up and for this light emitting diodes (l.e.d.s) are eminently suitable. They are available in a wide variety of colours and a festive display can be made without resorting to lamps and coloured lenses.

A static display is not very interesting and a flashing display is far more attractive. Rather than simply connecting all the l.e.d.s to an oscillator to make them all flash together, a twinkling effect is more effective and desirable. Twinkling, by its nature of course, suggests some sort of random flashing and this could be achieved by building a number of oscillators each driving its own l.e.d. or group of l.e.d.s.

With a 5-pointed star for example, five separate oscillators would be required each running at a slightly different rate. This would produce an overall random effect but it would be obvious that each l.e.d. was in fact switching on and off quite regularly.

With digital circuits, where the output is a logical function determined by the current inputs (and perhaps even previous states), it is impossible to produce a truly random output where the occurrence of the next state cannot be predicted with any certainty. In these circumstances, a pseudo random code generator (PRCG) based on shift registers is normally used. As the name suggests, this is a circuit which produces a seemingly random series of pulses, although on closer inspection the series is found to repeat at regular intervals.

How long the sequence takes to repeat depends on the number of shift register stages used. By having a suitably large number, the sequence can be made random enough for the application. Such circuits are used extensively in the generation of audio white noise and input signals for logic systems for test purposes, as well as in computer games to introduce the element of chance. They are even used in secure remote controls, which send a different code each time they are used, thus preventing the infra-red transmission being copied and used later by an unauthorised person.

PSEUDO RANDOM

To get an idea of how a PRCG works, it is best to consider the simple arrangement of the 4-stage shift register with XNOR (exclusive-NOR) feedback shown in Fig.1.

Each shift register stage has an output which can be either 0 or 1. With each successive clock pulse, the output of each stage assumes the state of the preceding stage.

The output of an XNOR gate is high when the inputs have equal logic values. If the values are unequal, however, the output will go low. In Fig.1, the inputs of the gate are connected to stage outputs Q3 and Q4. The gate's output feeds into the data input (D) of the first stage. Consequently, input D is continually changing its logic state in response to the logic on outputs Q3 and Q4.

Assuming that initially all the stage outputs are each set at 0, the sequence for the shift register is as shown in Table 1.

Table 1. Logic sequence for Fig.1.

STEP	Q1	Q2	Q3	Q4	D
0	0	0	0	0	1
1	1	0	0	0	1
2	1	1	0	0	1
3	1	1	1	0	0
4	0	1	1	1	1
5	1	0	1	1	1
6	1	1	0	1	0
7	0	1	1	0	0
8	0	0	1	1	1
9	1	0	0	1	0
10	0	1	0	0	1
11	1	0	1	0	0
12	0	1	0	1	0
13	0	0	1	0	0
14	0	0	0	1	0
15	0	0	0	0	1

It will be seen that after 15 clock pulses, the outputs again contain all 0s and the sequence will then repeat. Thus, if l.e.d.s are connected to each of the Q outputs, they will be on for the periods marked 1, and off for those marked 0. To a casual observer each l.e.d. will appear to be switching randomly, although with a short

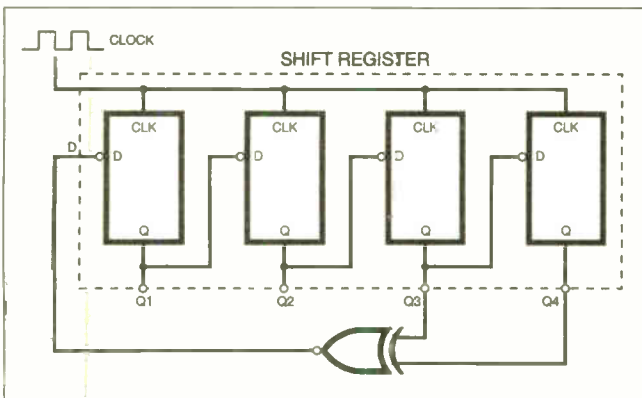
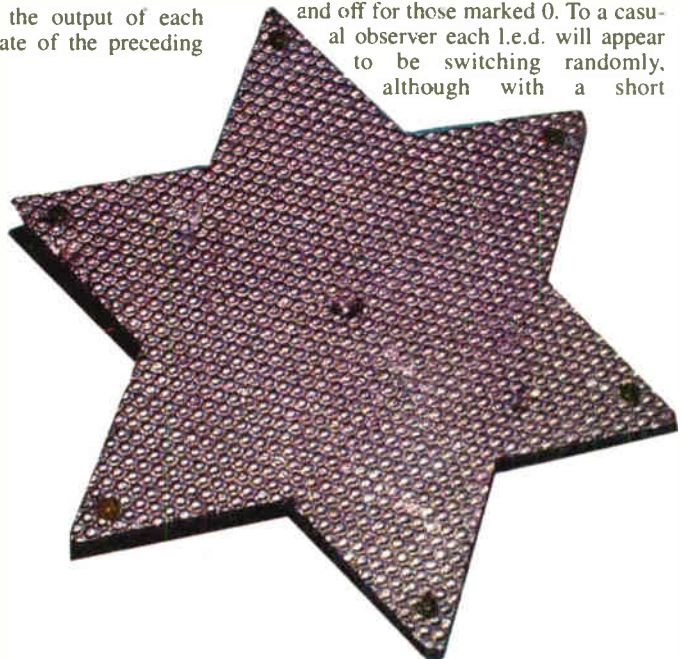


Fig.1. A four-stage pseudo random code generator based on shift registers and an exclusive-NOR gate.



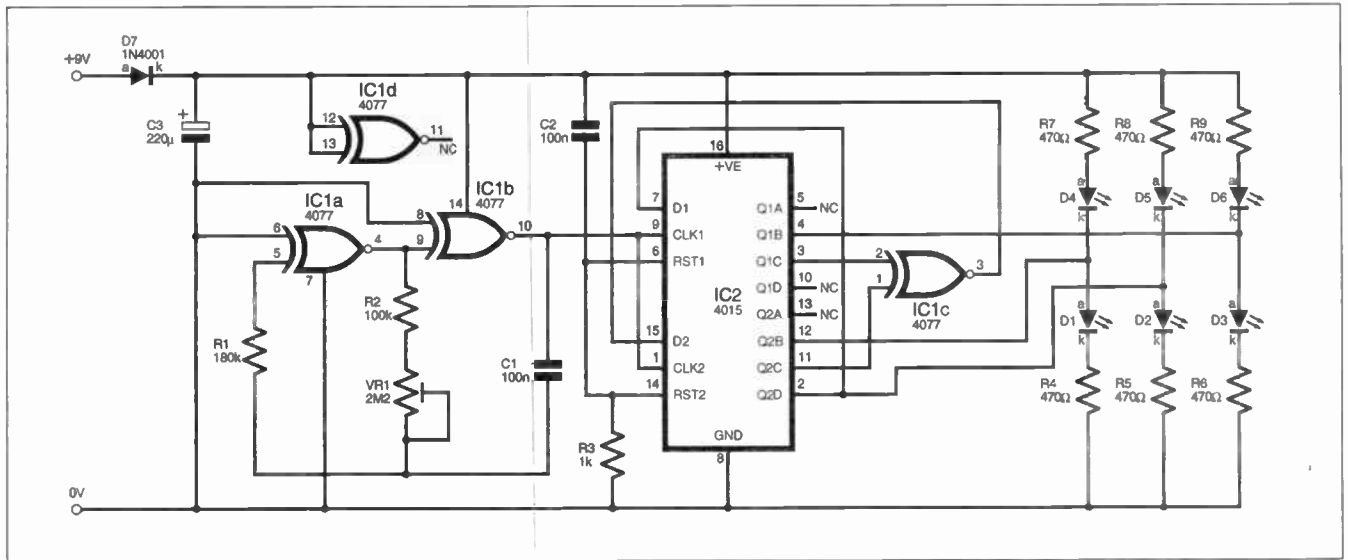


Fig.2. Complete circuit diagram for the Twinkling Star.

sequence like this, a pattern may soon be spotted.

The theory of pseudo random generators is quite complex but a few simple rules can be discerned. It will be noticed, for example, that the switching pattern for each output is the same, except that it is delayed by a certain number of clock periods. Thus the second stage is the same as the first but delayed by one clock period, while the fourth stage is the same but delayed by three periods.

In general, the maximum number of states will be given by $2^n - 1$, where n is the number of stages. In the above example where $n = 4$, the number of different states is $2^4 - 1 = 16 - 1 = 15$.

To increase the maximum number of states, the additional stages must be connected "within" the XNOR feedback loop. Adding them on the end of the shift register "outside" the feedback loop would, of course, only repeat the same patterns again.

The above sequence is known as a "maximum length sequence" and to obtain this, the feedback connections must be chosen carefully. If the outputs had been taken from the second and fourth stages, for example, the sequence would repeat after only six clock pulses. However, if the first and fourth stages were used, the output pattern would also happen to be a maximum length of 15.

You can prove this for yourself if you can run the short QBASIC program in Listing 1. The length of the sequence can be extended by adding more zeros to the length of A\$. The XNOR gate inputs A and B can be "connected" to any Q output by changing the

Listing 1.

```
'TWINKLE TEST
CLS : A$ = "0000": B = LEN(A$) - 1
INPUTA = 3: INPUTB = 4
LOOPIT:
IF MID$(A$, INPUTA, 1) = MID$(
  A$, INPUTB, 1) THEN D$ = "1"
ELSE D$ = "0"
IF VAL(A$) = 0 THEN PRINT
  "START SEQUENCE"
PRINT A, A$, D$: A$ = D$ + LEFT$(
  A$, B): A = A + 1
HOLD: IF INKEY$ = "" THEN
  GOTO HOLD
GOTO LOOPIT
```

number allocated to them in variables INPUTA and INPUTB. The sequence is stepped through pressing any key.

As the program proves, if more stages are used, the feedback will need to be taken from other outputs to obtain a maximum length sequence. Sometimes even more than two outputs are needed (try modifying the program for this), which then requires the use of a network of XNOR gates.

Thus an 8-stage shift-register, for example, happens to require more than two outputs to be used while a seven stage shift-register requires only two outputs to be connected (1 and 7, or 3 and 7) and produces a sequence of 127 states before repeating.

The mathematics for determining the feedback connections are beyond the scope of this article (or of the simple program) but for those interested, the feedback connections for various length shift registers required to obtain maximal length sequences are shown in Table 2.

Note that only those requiring two input feedback are shown.

Table 2: Maximum length sequences

Number of Stages	Feedback Taps	Sequence Length
3	1 and 3	7
4	1 and 4	15
5	3 and 5	31
6	1 and 6	63
7	1 and 7	127
	or 3 and 7	
9	4 and 9	511
10	3 and 10	1,023
11	2 and 11	2,047
15	1 and 15	32,767
	or 4 and 15	
	or 7 and 15	
17	3 and 17	131,071
	or 5 and 17	
	or 6 and 17	
18	7 and 18	262,143
20	3 and 20	1,048,575
21	2 and 21	2,097,151
22	1 and 22	4,194,303
23	5 and 23	8,388,607
	or 9 and 23	
25	3 and 25	32,554,431
	or 7 and 25	
39	4 and 39	5.5×10^{11}

FORBIDDEN STATE

In the previous example, the initial shift-register state was assumed to be all zeros. This was done not only because this was an easily recognised state, enabling the repeating pattern to be more easily seen, but also for a much more important reason. The "all-ones" state would also give an easily recognised condition but this is a "forbidden state" because it would simply cause 1s to be continually shifted along the register and provide no output at all (try it with the program).

In practical circuits this state must therefore be prevented from occurring as it is quite possible that it might occur at switch on.

In this Twinkling Star circuit, however, the problem is side-stepped by using a simple power-on reset circuit.

CIRCUIT DIAGRAM

The complete circuit diagram for the Twinkling Star is shown in Fig.2.

Two XNOR gates, IC1a and IC1b form an oscillator whose frequency is determined by the values of capacitor C1 and the total resistance of resistor R2 in series with preset potentiometer VR1. The latter is used to alter the frequency.

IC2 is a dual 4-bit shift-register. The two registers are connected in series to produce an 8-bit register. XNOR gate IC1c provides the feedback and is connected to the third and seventh stages of the register to give a total of 127 different output states.

The outputs chosen for driving the l.e.d.s are taken from the second, fourth and sixth stages. These provide exactly the same states but separated in time by two clock cycles so that all the l.e.d.s will appear to be switching randomly.

To ensure that there are no periods when all the l.e.d.s are off, each output drives two l.e.d.s., which require opposite logic states to turn them on and off.

Any colour can be chosen for the l.e.d.s, although it may be necessary to change the values of some of the ballast resistors if a uniform brightness between different colours is required.

As the current consumption is fairly high, the circuit should be powered by a small mains adaptor delivering about 9V d.c. Also see later.

COMPONENTS

Resistors

R1	180k
R2	100k
R3	1k
R4 to R9	470Ω (6 off)

See
SHOP
TALK
page

Potentiometer

VR1	2M2 min. vertical preset
-----	--------------------------

Capacitors

C1, C2	100n ceramic, 5mm spacing (2 off)
C3	220μ radial elect. 16V

Semiconductors

D1 to D6	l.e.d., colour of choice (6 off)
D7	1N4001 rectifier diode
IC1	4077 quad XNOR gate
IC2	4015 dual 4-bit shift register

Miscellaneous

Printed circuit board, available from the *EPE PCB Service*, code 276; d.i.l. 14-pin socket; d.i.l. 16-pin socket; 2-pin screw-terminal block, p.c.b. mounting; "star" material (see text); connecting wire; solder, etc.

Approx. Cost
Guidance Only

£14

excluding case

CONSTRUCTION

The circuit is constructed on a printed circuit board whose details are shown in Fig.3. This board is available from the *EPE PCB Service*, code 276.

Construction should begin with the lowest profile components (i.e. resistors and diode), and then proceed to the capacitors and i.c.s. The latter should be fitted with sockets as they are CMOS devices and prone to damage if mishandled. Touch a grounded (earthed) item before handling them.

In use, the p.c.b. is mounted on the back of the star. This could be cut out from card, polystyrene or some other sheet material on which the l.e.d.s can be mounted. To obtain the maximum flexibility in positioning the l.e.d.s and the size of the star, these are mounted off the p.c.b. and connected to it with suitable lengths of insulated wire.

It is probably best to construct the star first and then mount the l.e.d.s and p.c.b. The wiring details are shown in Fig.3. Care should be taken to ensure that the l.e.d.s are connected the right way around as reversal could cause them not to light.

If you choose to use a 12V d.c. supply, two or three l.e.d.s could be wired in series in place of each l.e.d. shown. This would enable a larger number of l.e.d.s to be mounted on the star, although all l.e.d.s in the same group would light simultaneously, of course. The values of the series resistors would then also have to be adjusted to maintain a suitable level of brightness, and without overloading the output capability of IC2. The l.e.d.s should *not* be connected in parallel.

When complete, there should be a total of nine wires coming from the star for connection to the p.c.b. These should be colour

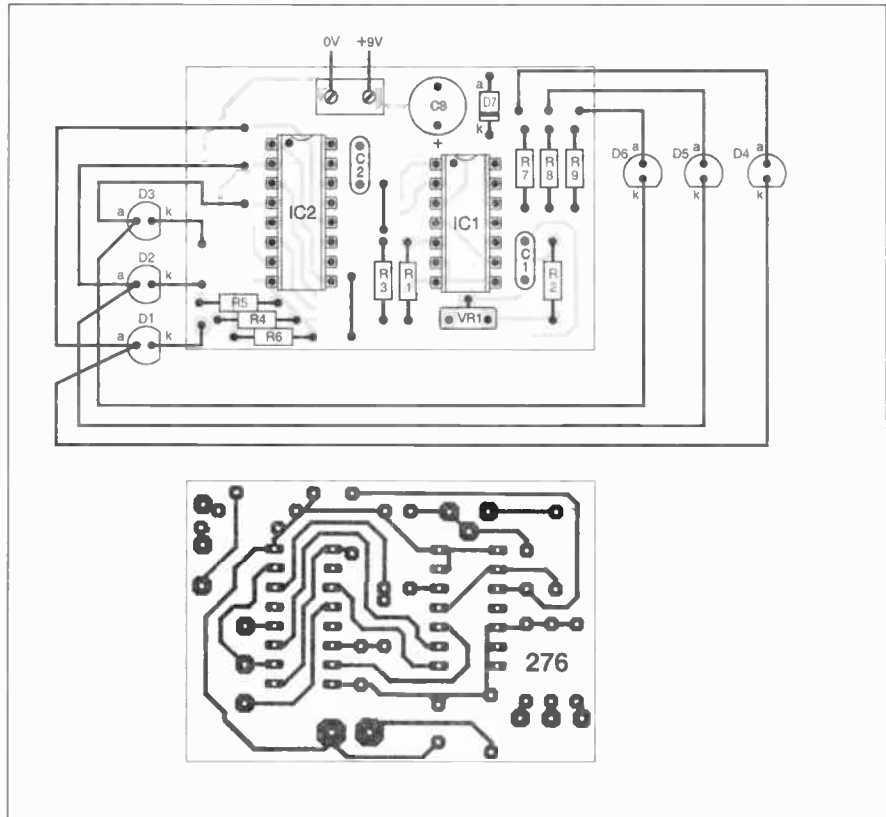


Fig.3. Printed circuit board component layout, interwiring to off-board l.e.d.s and full-size copper foil master.

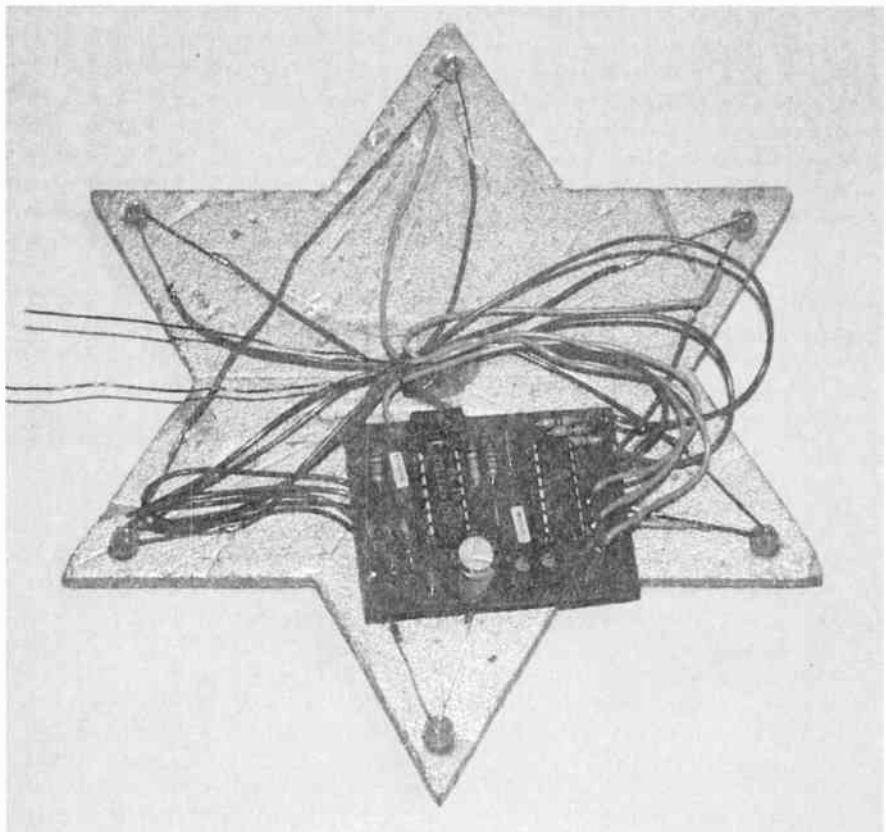
coded so that there is no confusion about which should be connected to the positive and negative (0V) lines.

Provided the circuit has been wired correctly, it should work without any adjustments. Resistors R4 to R9, however, may need selecting depending on the relative

brightness of the l.e.d.s used. The speed of the flashing can be adjusted according to taste by means of preset VR1.

Finally, there is no reason why this circuit should not be used with some other kind of tree top ornament, a twinkling fairy, snowman or Father Christmas. □

Completed prototype "star" with p.c.b. mounted on the back.



CHRISTMAS BUBBLE

OWEN BISHOP

Keep the party balloons intact, watch light bubbles burst instead!



Festive Supplement

THIS project gets its name because it looks like a large bubble, repeatedly swelling up and bursting. The effect is produced by arrays of l.e.d.s arranged in three concentric circles with a jumbo l.e.d. in the centre. First the central l.e.d. lights, then the rings around it light up in order, expanding outward until the bubble finally "bursts" then swells again.

This is a flexible project that can be "programmed" for a wide range of effects. There is more than one way to burst a bubble and there are more things to do with any of the l.e.d. arrays. By varying the connections and the layout of the l.e.d.s. all manner of repeating displays may be realised.

Effects can include spinning lights and travelling lights. There is also a spare area of the board on which, for little extra cost, you can set up a more elaborate display or install another complete display driven by the same counter circuit. This simply entails adding two more sets of components repeating those from IC3 onwards.

HOW IT WORKS

The circuit is quite a simple one, for which the schematic diagram is shown in Fig.1. An oscillator, IC1, generates pulses

at about 5Hz. This rate is suitable for producing a lively display. The pulses go to a decade counter, IC2. Its ten outputs repeatedly go high one at a time, in order from 0 to 9.

The next stage consists of four 4-input NOR gates, IC3a/b and IC4a/b, which are connected variously to the counter outputs. If any one or more inputs of a particular NOR gate is high, the output of the gate is low. The output from each gate goes to one of four *pnp* transistors, TR1 to TR4. Pulling low the base (b) of the respective transistor turns it on and current flows to the l.e.d. group, causing the group to light.

For example, as connected in Fig.1, l.e.d. D1 comes on for counts 0 to 4 and goes out for counts 5 to 9. The l.e.d.s in the second group, D2 to D5, are wired in parallel. They come on for counts 1 to 4 and are out for the other counts.

The full sequence for all four groups is shown in Table 1. To make the action clear, it only indicates when l.e.d.s are switched on. They are switched off if not marked.

Other combinations of inputs could alternatively be connected instead to produce other effects, which is why a wide range of lighting sequences is possible with this project.

The breadboard layout in Fig.5 shows the connections required to correspond with Table 1 and the circuit in Fig.1. You may choose different connections if you prefer.

Since different numbers of l.e.d.s are switched, the values of resistors R3 to R10 are chosen accordingly. Table 2 shows the details and selection criteria in relation to a 6V d.c. power supply. The values must be changed if other quantities of l.e.d.s are used should you choose to modify the circuit, or if a different power supply voltage is used.

The groups of l.e.d.s are arranged as shown in Fig.2. All the l.e.d.s in a ring are connected together in parallel. The bubble spreads during counts 0 to 4. The central l.e.d. goes out at count 5. It "bursts" at counts 6 and 7, briefly reappears in outline at count 8 and is gone again at count 9. The sequence then repeats indefinitely, taking about two seconds for each repeat.

Table 1: L.E.D. Switching Sequence.

Count	Centre	Inner ring	Middle ring	Outer ring
0	ON			
1	ON	ON		
2	ON	ON	ON	
3	ON	ON	ON	ON
4		ON	ON	ON
5		ON	ON	
6				
7				
8				ON
9				

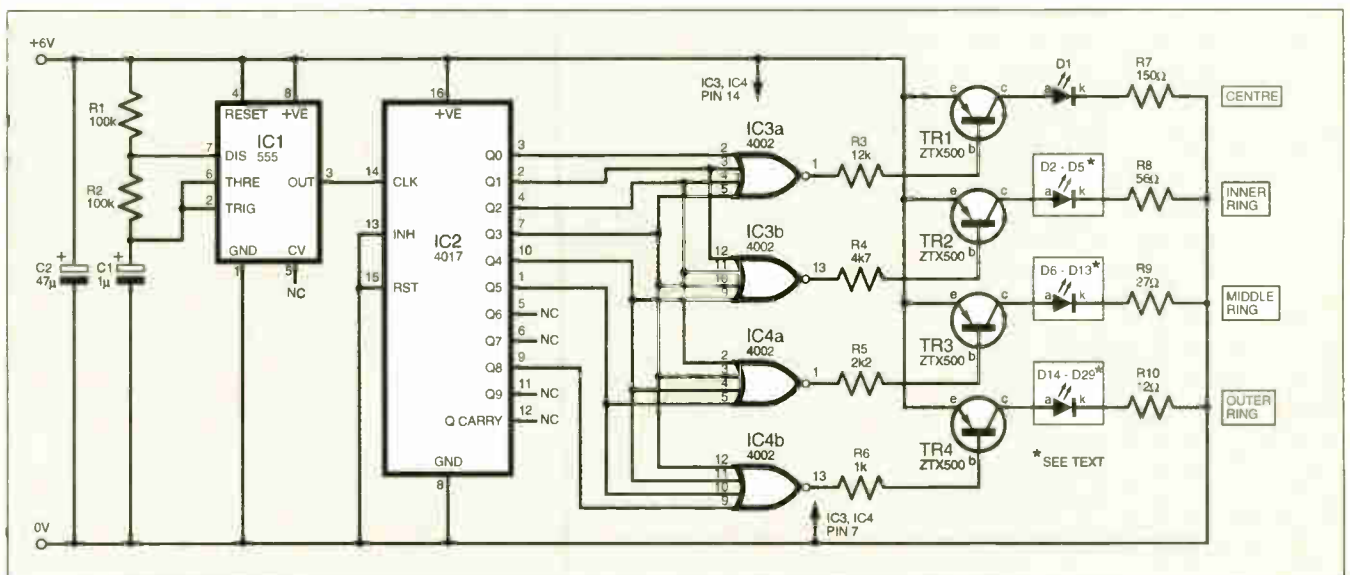


Fig.1. Full circuit diagram for the Christmas Bubble.

As the project is a decoration for Christmas and other festivities, it is likely to be run for several hours at a time. It is powered either by a mains-powered 6V d.c. battery eliminator, which should be capable of supplying at least 500mA. Alternatively, use four D-type cells.

DISPLAY CONSTRUCTION

The display is assembled on a piece of thin card about 120mm square. To make it more decorative, use coloured card or stick coloured paper or cutouts on it.

The l.e.d.s are mounted by pushing their leads through holes in the card (see Fig.3) as far as the lugs will allow. Mark out the circles on the rear of the card and use a stout pin to make pairs of holes about 2.5mm apart, as in Fig.4.

Working on one ring at a time, push the leads of the l.e.d.s through the holes from the front. Make sure that the cathodes (k) of all the l.e.d.s are toward the centre of the circle. Cut each lead to



about half-length, then bend it back to touch the rear of the card. Take a length of solid-strand connecting wire and strip off the insulation. Run the wire around the circle leaving a gap as shown.

Tuck the wire under the bent cathode wires and then use fine pliers to kink the cathode wire more sharply to grip the circular wire. Solder each joint, working as quickly as possible to avoid damaging the

l.e.d.s. Repeat this operation with another wire to join the anodes (a). Repeat the whole operation for each ring and for the central l.e.d.

CONTROL BOARD ASSEMBLY

The component requirements depend on the numbers of l.e.d.s used and the patterns in which they are switched. The components list caters for the Bursting Bubble as illustrated in Fig.1, Fig.2, and Table 1.

The control board shown in Fig.5 carries everything except the battery and the l.e.d.s. Assemble the circuit around IC1, for which a socket should be used. Check your connections and then apply power. Confirm that the output from pin 3 is a series of pulses at about 5Hz.

Assemble the circuit for IC2, again using a socket and checking your soldering. When power is applied, each output gives a 0.2s pulse every two seconds.

Next make the connections between IC2 and IC3/IC4, again using sockets for the latter. Link the like-notation points together, e.g. link Q1 at IC2 to Q1 at both IC3 and IC4. Although only one connection per "Q" is shown at IC2, the horizontally adjacent holes may be used as well for the same notation.

Table 2: Resistor selection detail for a 6V d.c. supply.

Group	Number of L.E.D.s	Total current (mA)	Base resistor	Series resistor
Centre	1 jumbo	20	R3 12k	R7 150Ω
Inner ring	4	60	R4 4k7	R8 56Ω
Middle ring	8	120	R5 2k2	R9 27Ω
Outer ring	16	240	R6 1k	R10 12Ω

COMPONENTS

Resistors

- R1, R2 100k (2 off)
- R3 12k
- R4 4k7
- R5 2k2
- R6 1k
- R7 150Ω
- R8 56Ω
- R9 27Ω
- R10 12Ω

See **SHOP** **TALK** page

All 0.25W, 5% carbon film or better

Capacitors

- C1 1μ radial elect. 10V
- C2 47μ axial elect. 10V

Semiconductors

- D1 l.e.d., 10mm, any colour
- D2 to D29 l.e.d., 5mm, any colour, any shape (28 off)
- IC1 555 timer
- IC2 4017 decade counter
- IC3, IC4 4002 4-input NOR gate (2 off)
- TR1 to TR4 ZTX500 pnp transistor (4 off)

Miscellaneous

Stripboard 29 strips x 39 holes; 1mm terminal pins (6 off), 8-pin d.i.l. socket; 14-pin d.i.l. socket (2 off), 16-pin d.i.l. socket; min. crocodile clip; battery holder for four D-type cells; connecting wire (single-strand and multistrand); solder; card and materials for decoration.

Approx Cost Guidance Only

£10 excluding batts

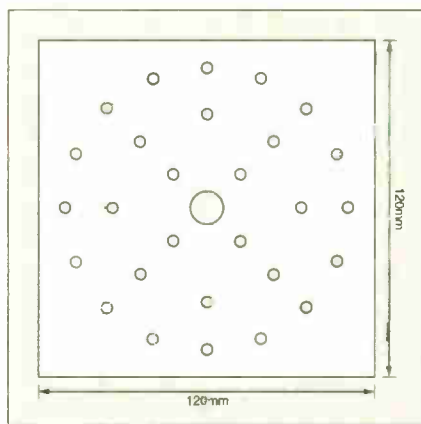


Fig.2 (above left). Front panel display layout and measurements.

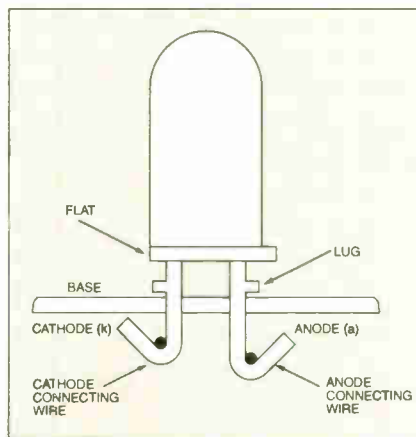


Fig.3 (above right). Suggested method of mounting l.e.d.s on the display wire rings.

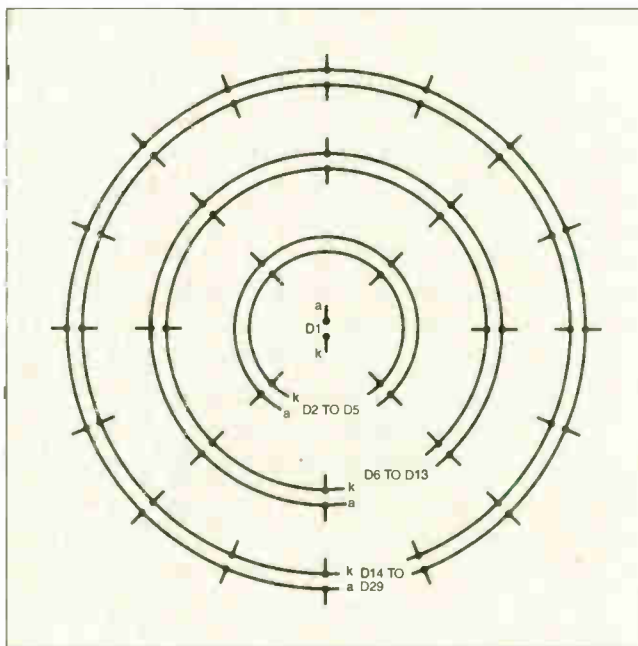
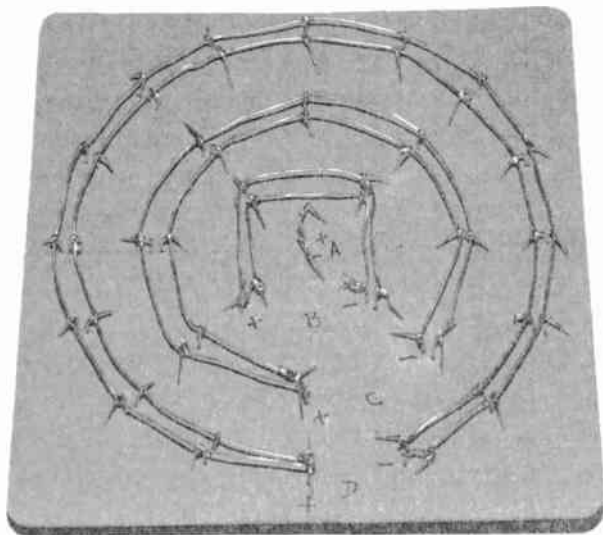
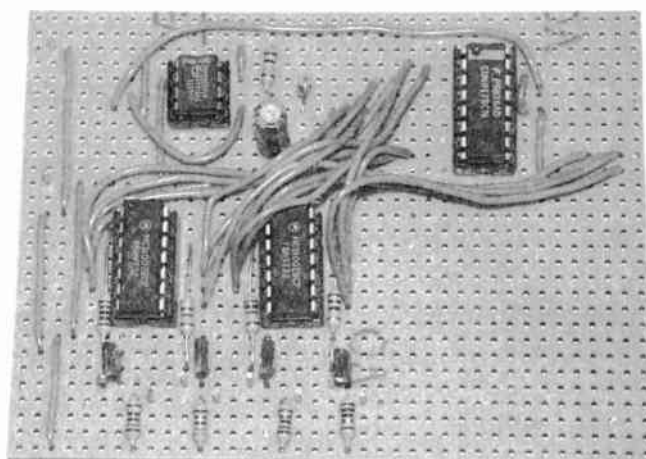


Fig.4 (right). Connecting up the l.e.d.s in groups.



The l.e.d.s soldered to the display wire rings.



Completed prototype stripboard component layout.

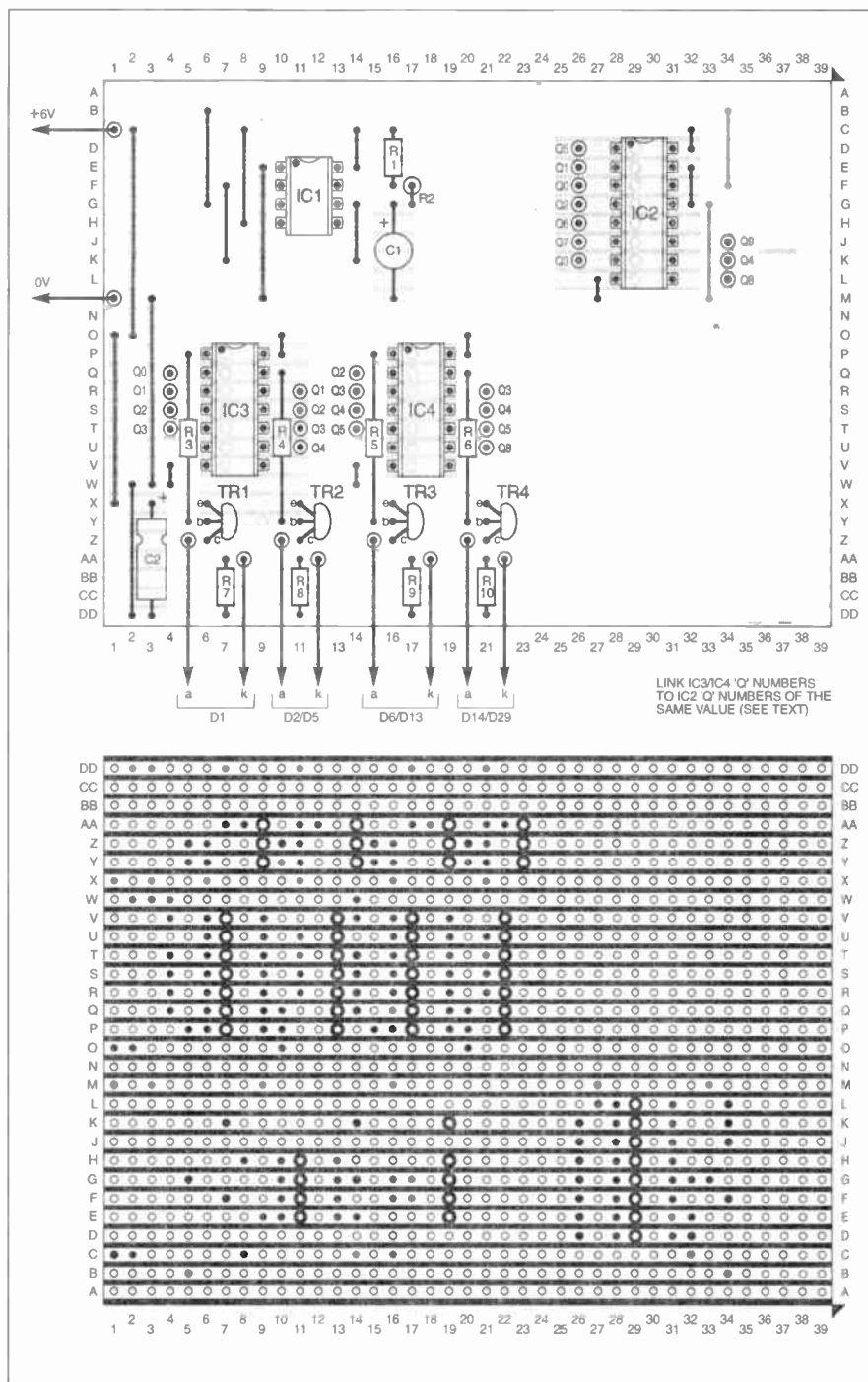


Fig.5. Stripboard component layout and underside copper break details.

Using thin flexible wire, join the ends of the l.e.d. rings to the notated points on the control board. The cathode (k) rings connect to the dropper resistors (pins on row AA) and the anode rings to the collectors (c) of the transistors (pins on row Z), as marked.

You are now ready to view the effect. If any of the l.e.d.s fail to light, check the soldering. Also, check that the l.e.d.s are mounted the right way round, with the "flat" on the rim indicating the cathode pin. If the rows light up out of sequence or at the wrong times, carefully check the connections between IC2 and the inputs of the gates.

A power switch is not really essential for this project. The 0V power line is soldered directly to the negative tag of the battery holder. Make the lead long enough so that the battery can be sited in some inconspicuous place away from the control board and display. Solder a crocodile clip to the positive supply lead. This just clips on to the positive tag of the battery box when power is required.

WRAPPING UP

The final step is to make the project more presentable as an object to hang on the Christmas tree, over the mantleshelf, or outside the front door. The battery box may be camouflaged by wrapping it with Christmas wrapping paper, to that it looks like a present on the tree. Lodge the "parcel" in the angle of a stout branch.

The control box is hidden behind the display panel and may be attached to it by large blobs of Blu-Tack or a double-sided adhesive pad. Beware of creating short-circuits. To improve the appearance further, make a shallow tray of thin cardboard to mount on the back of the display panel to cover the control board.

There are several different sequences that can be devised with the rings of l.e.d.s as in Fig.4. It is also possible to connect them in other ways. For instance the l.e.d.s of the outer two rings can be re-connected in eight sectors of three l.e.d.s each, with opposite sectors connected. Suitable "programming" would give the effect of a rotating fan.

As said earlier, different connections between the counter and the NOR gates could also be made. Make sure that the soldering of any relocated wires is satisfactory before re-applying power.

A Happy and Scintillating Christmas to all readers! □

Christmas Project

FESTIVE

FADER

STEVE DELLOW

Relax your Noelistic senses with smoothly changing lighting effects.

EVERY electronics hobbyist fantasizes about the festive season – it's the greatest opportunity of the year to show off your talents and prove that you can actually build something useful! The reason? The "Christmas Tree Lights Controller", of course! It's your annual chance to dazzle your friends and family with your skills . . . maybe.

Unfortunately, most of the options seem to have been explored by the mass manufacturers. Lights that flash in every conceivable type of sequence and pattern, playing medleys of the most obscure carols ever composed. Not exactly guaranteed to sustain the Christmas cheer.

DESIGN BASIS

The design concept of the circuit described here was to cater for the other end of the market – something a bit more subtle (if that's possible).

Consider the atmosphere late on Boxing Day when the trifle's just been round for the umpteenth time, and the Best Value multipack of beer is proving a little difficult to dispose of.

As you're finally dozing off, the mother-in-law breezes in and crows "Why aren't the Tree Lights on?". And before you can get a cushion over your head, *BattleStar Gallactica* erupts in the corner!

Bad news! What you'd prefer is something a bit gentler on the senses – a display that gently fades up and down over a period of time to assist in generating a more relaxed atmosphere.

Specifically, the circuit uses automatic phase-angle control to produce the slow cycling of a mains load backwards and forwards between the extremes of fully off and fully on.

In our application here, we have chosen to connect a set of Christmas Tree Lights as the load, but the concept can be extended to a wide variety of applications, providing an interesting insight into how low-voltage electronics can be used to safely control substantial amounts of power.

MAINS WAVEFORM

To understand the circuit operation, we must first look at the mains waveform which is available to power the load. The mains electricity supply delivers an a.c. signal to our houses, generally in the shape of a sine wave, as illustrated in Fig. 1.

The size of the potential difference at any moment in time is directly proportional to how much power we can extract from the supply. As you can see, this varies between the peak when we can get a lot of power, and the



zero-crossing points when no power is delivered.

When we switch the lights under normal circumstances we apply the full waveform cycle to our load, and they run at full power. However, in this case, we want to have control of how much power is applied to the load, so we can dim the lights up and down. So in effect we need to have some control over the amount of the mains waveform that is applied.

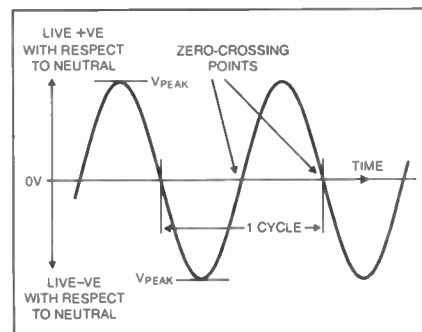


Fig. 1. Mains a.c. waveform.

The two main ways of achieving this are *burst-fire control* and *phase-angle switching*. Both are employed extensively in industrial and domestic equipment to achieve power control. Burst firing is used mainly for loads with a large "inertia" such as heaters, and lets through the a.c. waveform in bursts of complete cycles. In other words, it applies a measured number of full cycles depending on how much power is to be delivered, see Fig. 2.

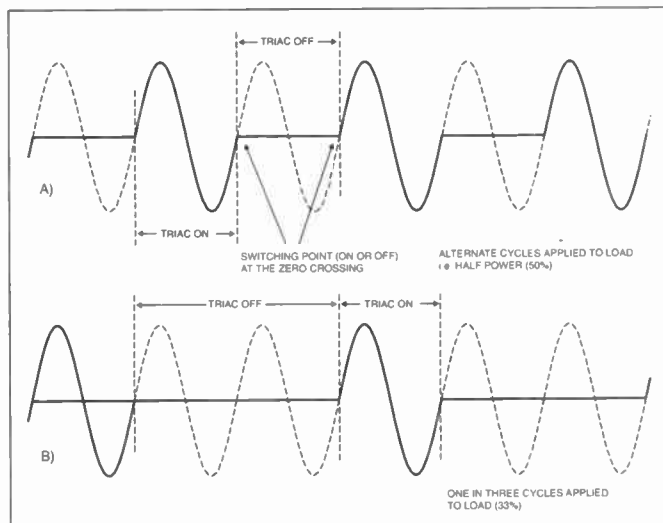


Fig. 2. Burst fire control.





COMPONENTS

Resistors

- R1, R6, R13, R15, R16 10k (5 off)
- R2 to R4, R10, R11 47k (5 off)
- R5 1k
- R7 510Ω
- R8 51Ω
- R9 330Ω
- R12 220k
- R14 100k

See **SHOP TALK** page

Potentiometer

- VR1 10k multitrn preset

Capacitors

- C1, C2 100μ radial elec. 16V (2 off)
- C3, C6 1μ ceramic
- C4, C5, C10, C11 100n ceramic
- C7, C8 47μ radial elec. 16V (2 off)
- C9 220μ radial elect 16V

Semiconductors

- D1, D2 1N4148 signal diode (2 off)
- TR1, TR2 BC109 npn general purpose transistor (2 off)
- CSR1 TIC206D 600V 1A triac
- IC1 78L05 -5V voltage regulator, 100mA
- IC2 79L05 -5V voltage regulator, 100mA
- IC3, IC4, IC7 ICL7621 dual op.amp (3 off)
- IC5 LM317L variable voltage regulator
- IC6 MOC3020 optically isolated triac

Miscellaneous

- REC1 50V 1A rectifier, d.i.l.
- T1 mains transformer, twin 6V secondaries, 3VA
- SK1 Euro style fused mains connector
- SK2 13A mains socket, panel mounting

Printed circuit board, available from the EPE PCB Service, code 277; plastic or metal case 190mm x 110mm x 55mm (see text); p.c.b. mounting supports (4 off); mains cable to suit; connecting wire; solder etc.

Approx Cost Guidance Only

£20 excluding case

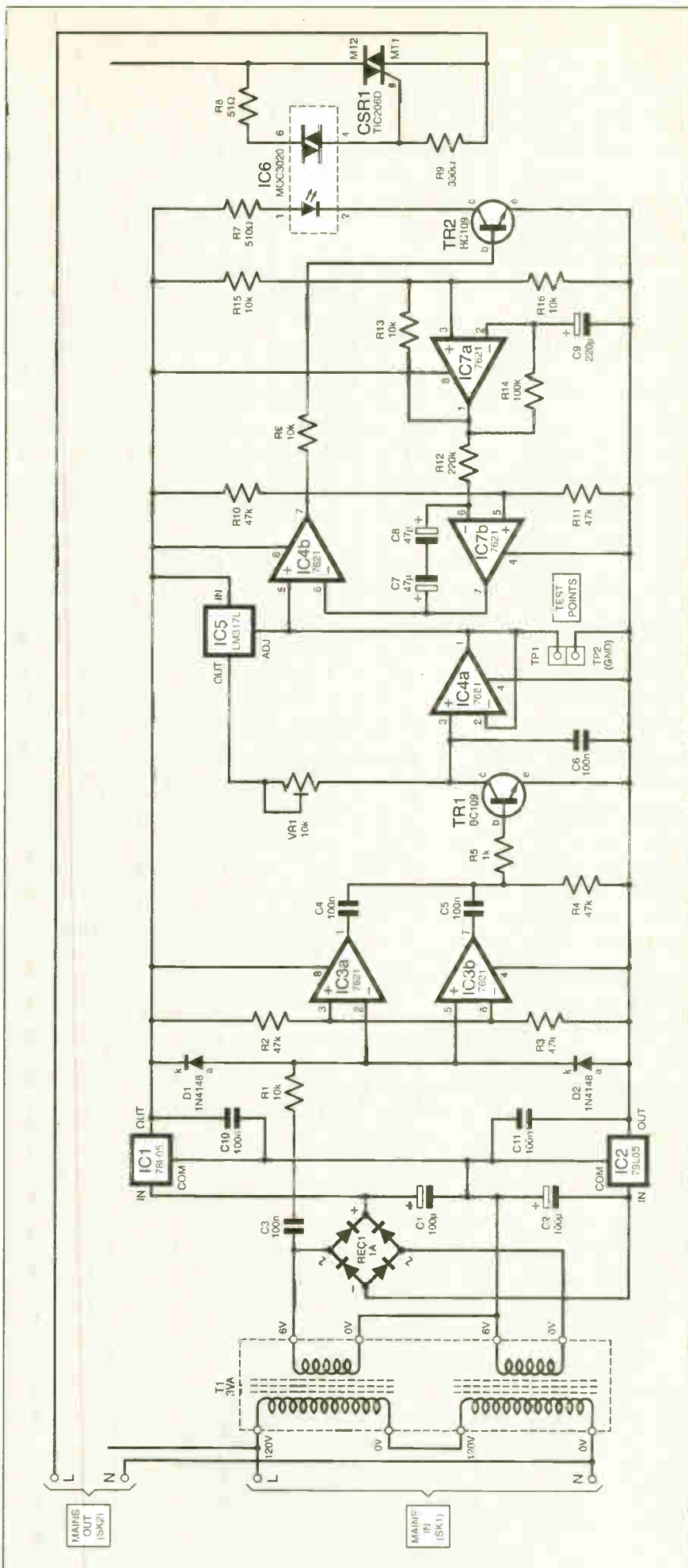


Fig.3. Complete circuit diagram for the Festive Fader

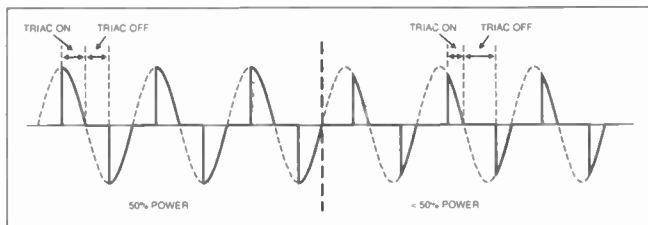


Fig.4 (above).
Phase angle control.

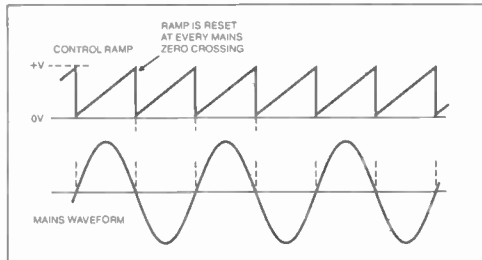


Fig.5 (left).
Synchronised control ramp alongside mains waveform.

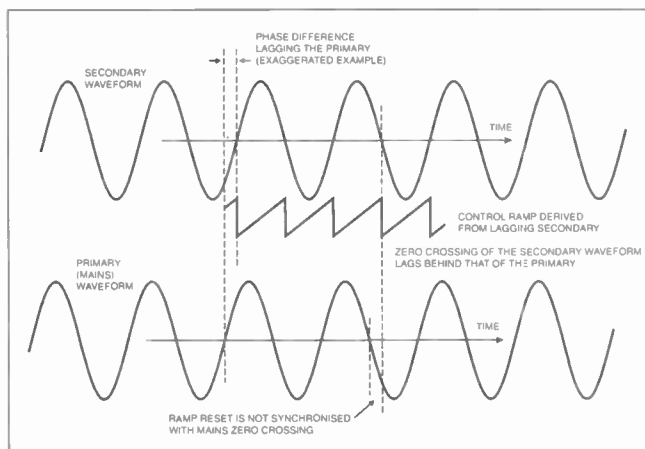


Fig.6. Transformer phase shift problems.

The cycle “blocks” always begin and end at zero-crossing points to reduce switching interference to a minimum. This is fine for large loads but not so for lights, which tend to flicker annoyingly when controlled in such a way – not what we really want.

The alternative is phase-angle control which “chops” up the a.c. waveform, as illustrated in Fig.4. By choosing the point where we apply our “chop” we can accurately control how much energy is passed through into the load, and so we have a means of controlling the brightness of our lights.

There is a risk of interference with this style of switching, but the lights are generally quite low power systems, so it’s not normally a problem.

CIRCUIT DESCRIPTION

Referring to the Festive Fader circuit diagram in Fig.3, power for the system is taken via a small mains transformer, T1, a 3VA type being sufficient to supply the circuit’s needs. The secondaries are joined together in series and fed through a full-wave bridge rectifier, REC1, to convert the a.c. signal to d.c. for the internal power supply.

This raw d.c. is smoothed by capacitors C1 and C2 to give about 9V across each component. The voltage regulator configuration that follows the smoothing capacitors may be a little confusing at first – creating a +10V d.c. supply, but using two regulators to do it! Why not use just a single one and save money?

The reason is that it is necessary to maintain everything symmetrical about the centre tapping of the transformer secondary, so that our zero voltage detection system works.

Since we are using phase angle switching, we need to have a reference that tells us exactly where we are in the mains signal to permit accurate switching of the mains-controlling triac (CSR1).

The chosen method here is to create a rising voltage ramp (or sawtooth) that starts from 0V at the beginning of each half cycle of the mains. This ramp rises linearly until it is reset at the end of the period – see Fig.5.

This will give us the means to control the lamps from fully off to fully on – if we switch the triac on at the far left hand end of the ramp, the lamps will get most of the energy from the associated mains cycle i.e. be very bright. However, if we switch them

when the ramp is a lot “higher”, then less energy is delivered and the lamps are proportionally dimmer. It’s so easy!

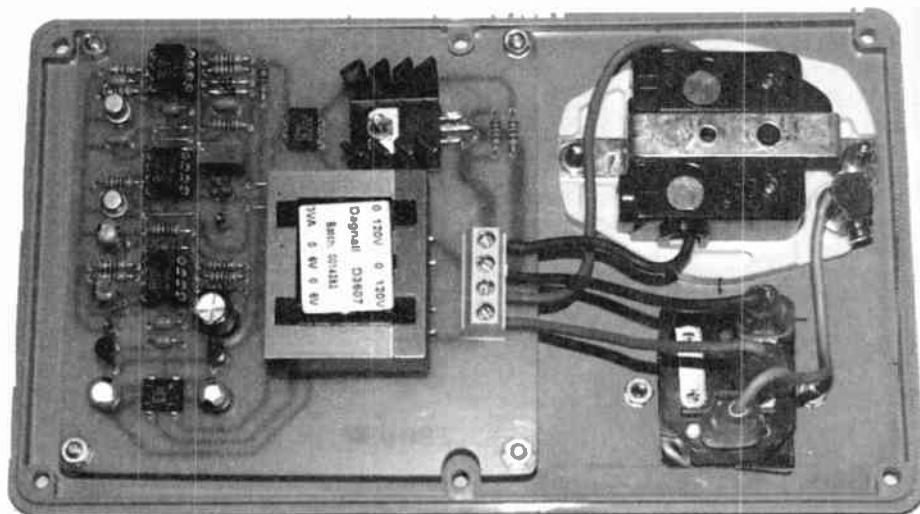
ZERO CROSSING

To start (and reset) this synchronising ramp we need to find the zero crossing points of the mains waveform. There are a number of ways of doing this, and a large proportion involve making direct connections to the primary of the transformer. This results in there being a number of “live” components on the circuit board – which can be quite hair-raising when you’re trying to do fault finding!

The argument in favour is that the mains signal in the secondary windings on most cheap transformers cannot be guaranteed to be in-phase with that of the primary. This means that the ramp would be “shifted” in time with respect to the actual mains signal, and we wouldn’t be switching things at the right moment, see Fig.6.

To understand the information in Fig.6, read it in the following manner:

1. Look at the primary waveform
2. Compare it with the secondary waveform (note that it lags behind the primary)
3. The control ramp is derived from the secondary waveform and its zero crossings
4. Compare the ramp reset points with the zero crossings of the primary waveform
5. Start again.



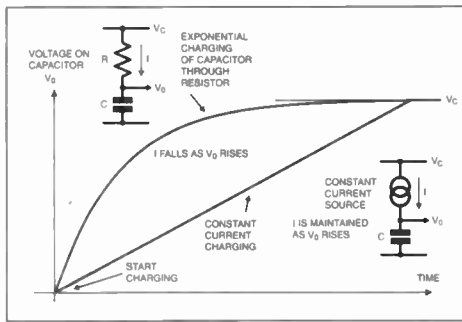


Fig. 7. Constant current charging of a capacitor.

point detection. Their outputs switch between “high” and “low” every time the input signal crosses the threshold (which is effectively the zero-crossing point) and produce a square wave signal at the outputs.

If you look carefully at the connections to the inputs, you will see that this results in the two generated square waves being in anti-phase. When the signal voltage rises through the threshold, IC3a output switches high, but IC3b output goes low, and vice versa when the signal input falls back through the threshold.

So, having created a zero-crossing detector circuit we need to consider how it can be used to synchronise the timing ramp.

RAMP SYNCHRONISATION

The ramp is created by charging up a capacitor under controlled conditions. Generally, we tend to charge up these devices through a resistor connected to a fixed voltage source. This produces the classic exponential curve, because the charging voltage across the resistor falls as the voltage on the capacitor rises. In other words, as time progresses, there's less “push” to force more charge onto the capacitor. See Fig. 7.

However, if we were able to maintain a steady push then the voltage would rise, not in a curve but in a straight line. This steady push is known as a constant current, and is created by adding circuitry that maintains a fixed voltage across the charging resistor. In this circuit, op.amps IC4a and IC4b plus variable-voltage regulator IC5 and preset VR1 are used for this task.

They combine to produce a constant current to charge capacitor C6, and the result is a rising voltage ramp, the height of which is ultimately limited by the supply rail. Op.amp IC4a is wired simply as a unity gain buffer to make sure that the charge (and therefore voltage) on C6 is undisturbed by the operation of the regulator.

The process is relatively simple. Regulator IC5 is used to create a constant voltage across VR1. Because there is a constant voltage across this resistance, there must be a constant current through it. This current pushes charge into C6 and as it does so, the voltage on the capacitor rises.

The regulator responds to this and maintains the voltage across VR1 so that the current is kept flowing at the same rate. The resistance is made variable to allow the ramp to be optimised, and a test point is included to allow it to be displayed on an oscilloscope.

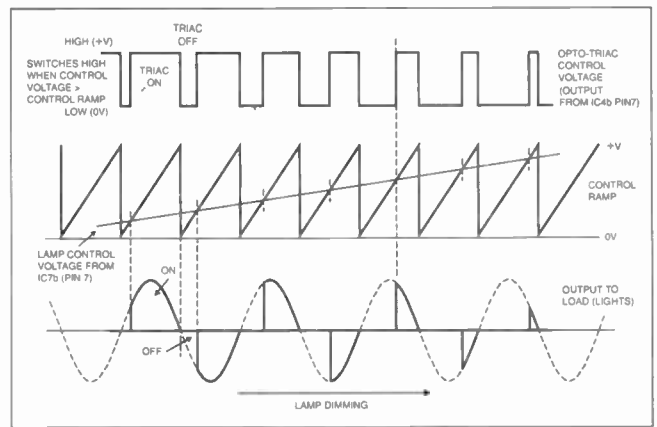


Fig. 8. Opto-triac control voltage.

We have created a ramp, then, but we need to reset it to 0V at the start of each mains half-cycle. This is where we bring in the zero-crossing signal created earlier. A transistor is connected across the ramp capacitor, C6, and by switching this on very briefly at the zero-crossing point, the charge on the top of the capacitor is removed, thus returning the ramp to 0V.

The transistor switching signal is created by differentiating the waveforms from the outputs of IC3a/b. Nice, short switching pulses are produced by the action of capacitors C4 and C5, which allow only the a.c. component of the signal through, i.e. when the voltage changes. The transistor responds only to the positive going pulses and the result is a good, clean reset.

FADE CONTROL

Now that we have a synchronised ramp, the next step is to create some sort of control signal that will command the triac to fade the lights up and down. The simplest method has got to be voltage control – when the voltage is at one extreme, say 0V, we want the lamps to be fully on, and at the other, fully off.

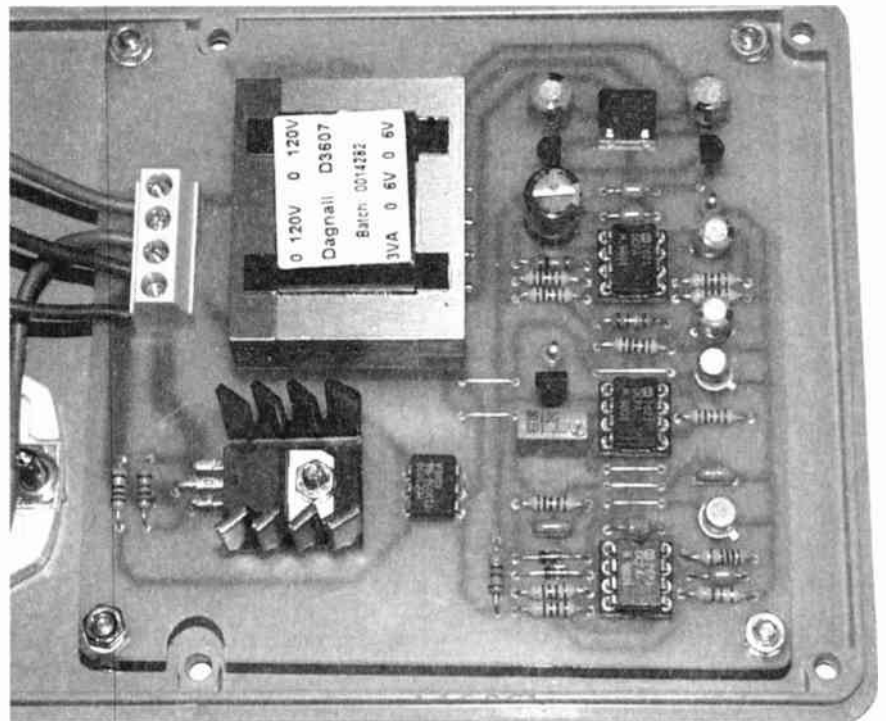
This works in well with the ramp voltage, which is also moving between the

supply rails. Lighting experts may at this juncture remind us that the brightness of an incandescent lamp is not linearly related to the point at which you switch it on during the mains cycle. It's not even a simple matter of doing a mathematical integration, as there are other physical effects that come into play.

For this application it was decided to keep things simple, and the “error” is not really noticeable unless you're sitting right under the Christmas tree with a light meter!

Op.amps IC7a and IC7b are used to produce a slow linear ramp which moves backwards and forwards between 0V and the supply rail – it's really a triangle wave. The rate is set so slow that the signal actually spends time clamped to supply or ground at the top and the bottom before it starts to move back again. These are the points where the lights would be fully on or fully off.

The first op.amp, IC7a, is set up as a slow square-wave oscillator. You can see the output switching simply by using a voltmeter. The rate is set by resistor R14



Finished circuit board. It is essential to use an insulating kit to mount the triac heatsink on the p.c.b.

and capacitor C9. Their values could be modified to speed things up a bit, or slow them down even further!

The second stage (op.amp IC7b) operates as an active integrator, turning the square wave input into a ramp. Here, the total value of capacitors C7 and C8 in relation to the value of resistor R12 control the gradient of the ramp, i.e. how fast the lights fade.

TRIAC CONTROL

The final step is to apply a control signal to the triac, and we return to the comparator function of an op.amp to help us here. By applying the zero-crossing ramp and the control voltage to the inputs of op.amp IC4b, we produce a switching waveform, as shown in Fig.8.

Whenever the half-cycle ramp is greater than the control voltage, the op.amp's output switches high and transistor TR2 turns on. This turns on the light emitting diode (i.e.d.) inside the opto-triac package of IC6, which triggers the integral triac into conduction.

The current capability of IC6's triac is limited, so an external device with greater capacity is added to cope with a real load such as our lights. The specified triac will cope with loads up to about 1kW.

So, if the triac is conducting, this means that mains current will flow – and the lights will be on; but only for that half-cycle! When the mains goes back through zero, the triacs stop conducting – so we have to trigger them *every* half cycle. This is why we keep resetting the ramp – we have to keep retriggering the triacs to keep the lights working! Consequently, the circuit is kept very busy while we relax to the gentle fading of the lights . . .

CONSTRUCTION

Since there are mains connections involved in this design it is strongly recommended that the published printed circuit board (p.c.b.) is used rather than stripboard (which isn't voltage rated for such work anyway). The p.c.b. assembly and tracking details are shown in Fig.9. The board is available from the *EPE PCB Service*, code 277.

This circuit should only be constructed by those who are adequately familiar with constructing mains operated circuits.

Before beginning any construction work, get yourself well prepared! Make sure the soldering iron tip is nice and clean, all your tools are close at hand, then find a really quiet spot away from any likely disturbances. Close reference to the circuit layout is essential – time spent here will save much grief later on.

Start by soldering in the wire links – there are six of them in all – plus the two test points. Then move on through the following sequence – resistors, capacitors, bridge rectifiers, and diodes. Then onto the transistors and regulators taking care to get their orientation absolutely right.

If it's all looking good, fit the transformer to the board – which may need a firm push to get its pins snugly down through the holes.

FIRST TEST

Test that the power supply is up and running before fitting the more expensive bits! First, make a careful check of all your soldering – look out for solder splashes, dry joints, solder bridges between tracks, etc. Once again, time spent here can save money . . . use a good magnifying glass to be absolutely sure.

When you've finished, carefully wire up a fused mains supply (one that's Residual

Current Circuit Breaker (RCCB) protected is best) to the inputs marked L and N (Live and Neutral), then make sure that the board is firmly supported on an insulating surface.

Keeping well clear of the live parts, switch on. The check here is to confirm that the 10V d.c. supply is correctly running – there are a number of points in the circuit where this can be measured using a multimeter. Be sure that you know where you're putting your probes before you try

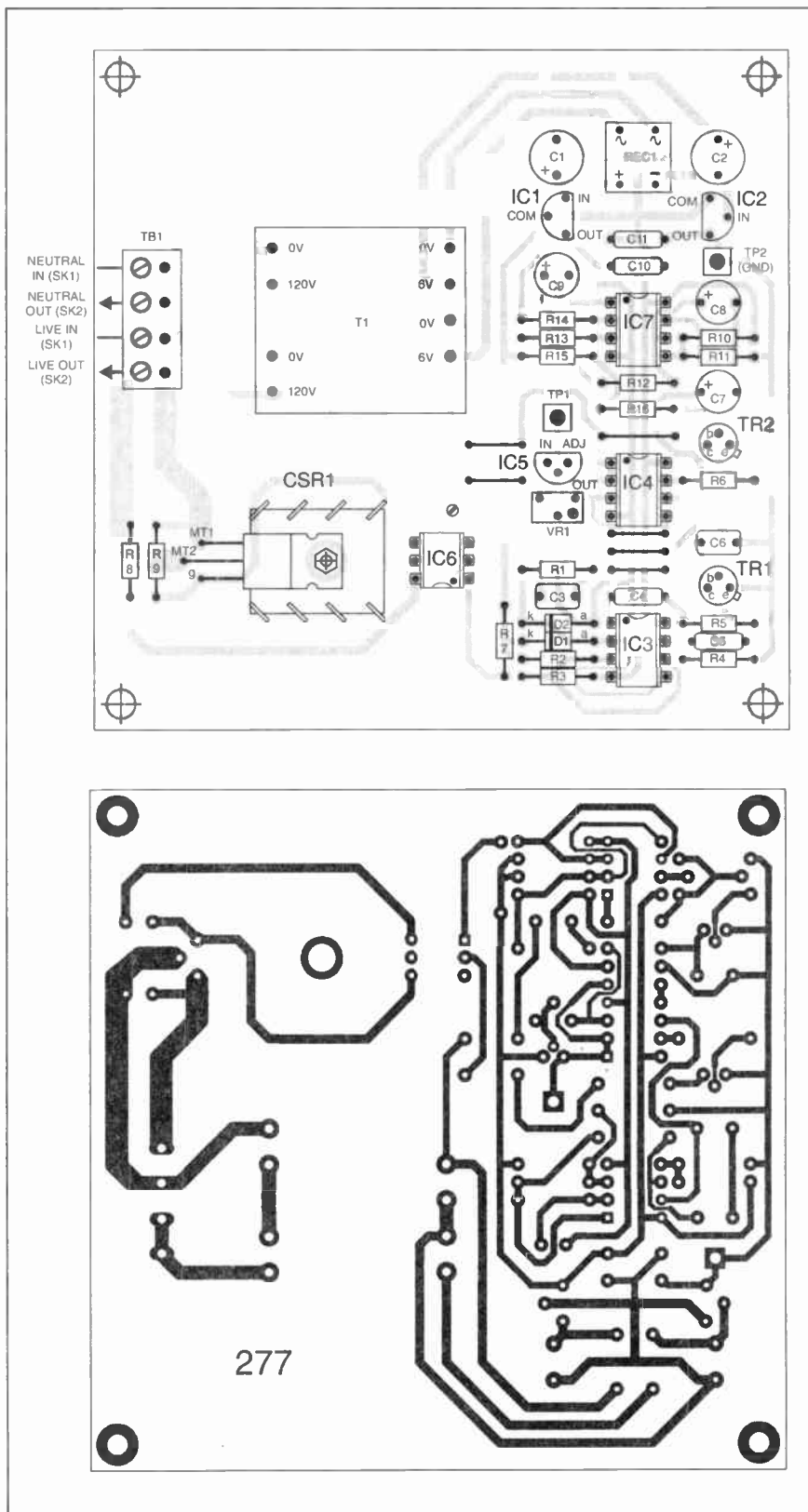


Fig.9. Festive Fader printed circuit board component layout and full-size underside copper foil master pattern. Beware some tracks carry the mains supply.

Festive Supplement

and make a measurement. Always keep your other hand in your pocket to avoid short-circuits through your body.

TRIAC MOUNTING

Once you're confident that everything's getting power, switch off and disconnect the mains connections completely. The integrated circuits (IC3 to IC7) and triac CSR1 can now be soldered into the board. As usual, check carefully that you're putting them in the right way round.

With mains loads up to 60W, the triac can be mounted vertically into the board, but if heavier loads are planned, there's space for it to be bolted down with a small TO220 heatsink. In this case, it is essential that you use an insulating kit, otherwise the heatsink body will be at mains potential!

ALIGNMENT

With everything now in place, it's time for some real action! Another visual check of the installation is definitely worthwhile, then wire up to the mains once again, this time connecting a small mains lamp (60W is fine) to the *Live out* and *Neutral out* terminals.

It would be sensible at this stage to secure the board to some firm surface using mounting pillars in conjunction with the holes provided in the p.c.b. This will keep everything under control while setting up the circuit.

It helps if an oscilloscope is available, but it isn't essential. Adjust preset VR1 to mid-travel, check your mains connections again, then switch on. After about ten seconds, everything should have settled, and the lamp will be fading slowly up and down. The scope is useful to check what shape the ramp is, and this can be done by using the test points.

Ground the 'scope to the 0V point at the edge of the board, and then hook onto test point TP1 for the signal. The ramp should start from 0V, rise to about 9V or 10V then reset to 0V again, repeating every 10ms, so set your scope's gain and timebase accordingly, and check whether this is the case.

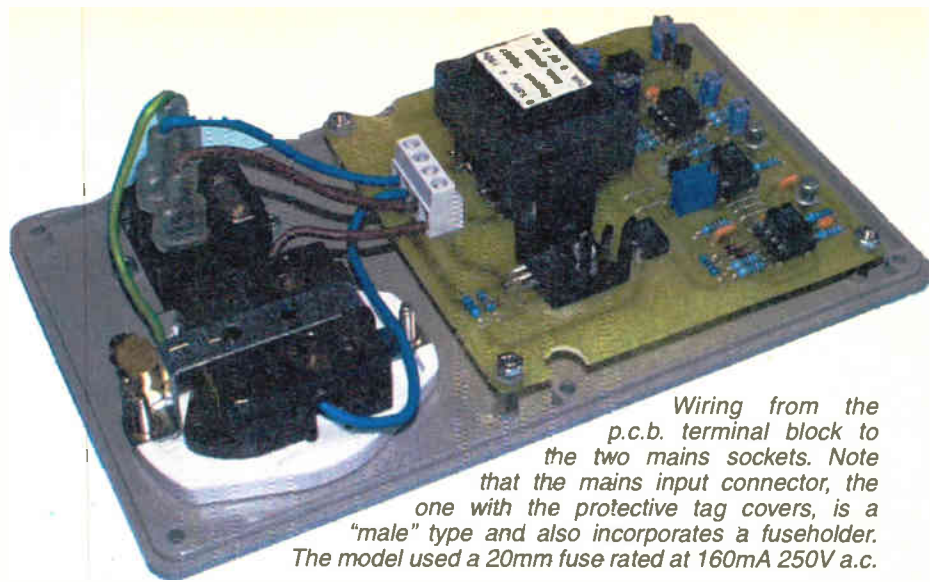
Adjust preset VR1 and see what happens to the shape of the ramp – leave it at a setting where the lamp seems to fade at a consistent rate between its two extremes, and spends about the same time at those limits.

If you're feeling adventurous, look at the output waveform on IC4b (pin 7), and you'll see that when this is permanently high the lamp is fully on, but in the low state the lamp is off. When the lamp is fading, you'll see two abrupt waveform edges appear. The falling edge coincides with the reset of the ramp, but the rising edge defines the point in the mains half cycle at which the triac is turned on.

Set the 'scope to trigger on the falling edge, watch the sequence, and see how it relates to what state the lamp is in. It's not the end of the world if you haven't got a 'scope – the ramp can be set just as effectively by watching how the lamp responds – it shouldn't take long.

PHASE CORRECTION

It was mentioned earlier that there is a remote chance that the transformer secondary may be sufficiently out of phase with the primary to cause control problems. This could be a lead or a lag error – one will cause the lamp to suddenly go to



Wiring from the p.c.b. terminal block to the two mains sockets. Note that the mains input connector, the one with the protective tag covers, is a "male" type and also incorporates a fuseholder. The model used a 20mm fuse rated at 160mA 250V a.c.

full on at the end of its fade down, and the other will result in the opposite effect.

The simple fix for this is to make some adjustments to the values of resistors R2 and R3. If you're sure that you've got a problem, remove these resistors and substitute a 100k Ω potentiometer, connect its outer terminals to the respective d.c. supply lines and its wiper to the board at the R2/R3 junction point.

Adjust the potentiometer to find a suitable voltage that takes account of the lead or lag error. Then measure its resistances to either side of the wiper to find the new values and fit resistors of about the same value (within the usual E24 series range). Basically, by changing the reference voltage, you're moving the zero-detection point backwards and forwards in time to take account of the error.

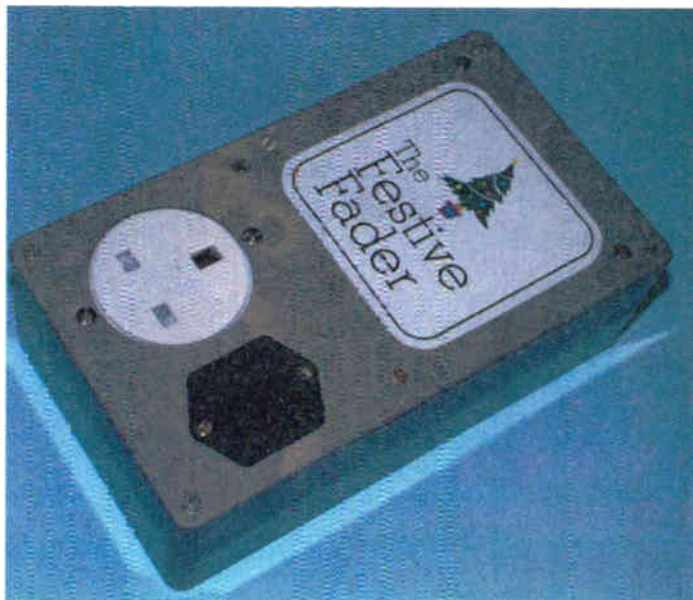
ENCLOSURE

Once you're happy with the operation of the circuit, it's time to start putting everything safely in a box. As mentioned earlier, mounting holes are provided to allow the board to be fixed down and pillars are recommended to allow it to stand away from the surface.

For all mains connections, make sure that the cable is overrated for the job – 6A type is probably best. If you decide to use a direct cable connection for the mains input to the terminal connector, make sure that some good strain relief is included inside the box. A single cable tie round the cable is NOT good practice – a P-clip installation is far more professional (and lasts longer)!

The mains earth must be connected to both mains input and output connections. If a metal enclosure is used this *must* also be earthed.

Completed unit showing the two mains sockets. The input connector is the "male" type.



For the output side of things a commercial mains socket would appear to be the order of the day, and a chassis mounted type is recommended so as to keep everything compact. As shown in the photographs, connect the earth from the input mains cable directly across to the output socket. If a metal case is being used, make sure that this gets a good earth connection too.

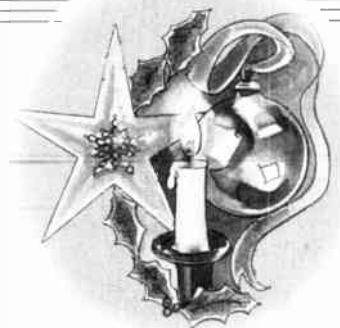
IN USE

This Christmas Tree Lights Controller has proved to be a useful and interesting addition to the festive display. Note that it is intended for use with light sets that connect *direct* to the mains, and *not* via a transformer, so check before you connect up.

As mentioned earlier, there's no need to restrict it to seasonal use, and it probably has a wide variety of other applications, maybe not even using lights as the load. Multiple units would produce a subtle but interesting light-show, and it would be quite easy to extend the basic circuit to a multi-channel system by adding extra control stages.

The other option is to create a totally manual controller by using a potentiometer that gives 0V to +10V into pin 6 of IC4b. Whatever circuit you decide to go with, remember that there's mains power around, so be absolutely sure before you change things! □

PICTOGRAM



ANDY FLIND

*Become a novelty flasher at the Mad Hatter's (or other's) Xmas party!
(Andy caps it nicely!)*

THE idea for this project germinated around the time the author's son announced forthcoming nuptials. As the groom's father, yours truly was expected to take a fairly prominent part in the proceedings, and those in key roles were requested by the happy couple to wear full traditional dress including, for the men, top hats.

It wasn't long before the notion of a ring of flashing light emitting diodes (l.e.d.s) around this spectacular piece of headgear was conceived. Well, some of us never quite grow up, as our wives frequently tell us.

The resulting display could, of course, equally well be applied to a belt or headband, or to almost any festive decoration.

PIC-TURE THIS!

The task seemed ideally suited to a design using a PIC microcontroller. It's easy enough to make a bunch of l.e.d.s flash with some CMOS logic, but with a PIC the circuit becomes even simpler, and seemingly endless flashing and chasing patterns can be generated with very simple software. Before the advent of these devices these sequences would have required whole boardfuls of components, and would have been almost impossible to implement for this application.

The prototype was practically "thrown together" on stripboard. After all, there are many other matters to be attended to before a wedding and time for such frivolous activities as electronics was in chronically short supply.

HAT TRICK

As the hat was obtained from a dress hire service a way of attaching it without causing damage had to be found, which will be described later.

After the event it was realised that the circuit had almost unlimited possibilities for amusing and eye-catching displays and decorations so it was re-designed onto a small printed circuit board and provided with an optional built-in display. The software was improved and tidied up, and the final result is now suitable for general release to other constructors.

Port A, to drive 12 l.e.d.s. Each has a current limiting resistor, R3 to R14, to control the l.e.d. current. The l.e.d.s used are rated at 2mA, but they are slightly over-driven in this circuit, which results in their being bright enough for most indoor situations.

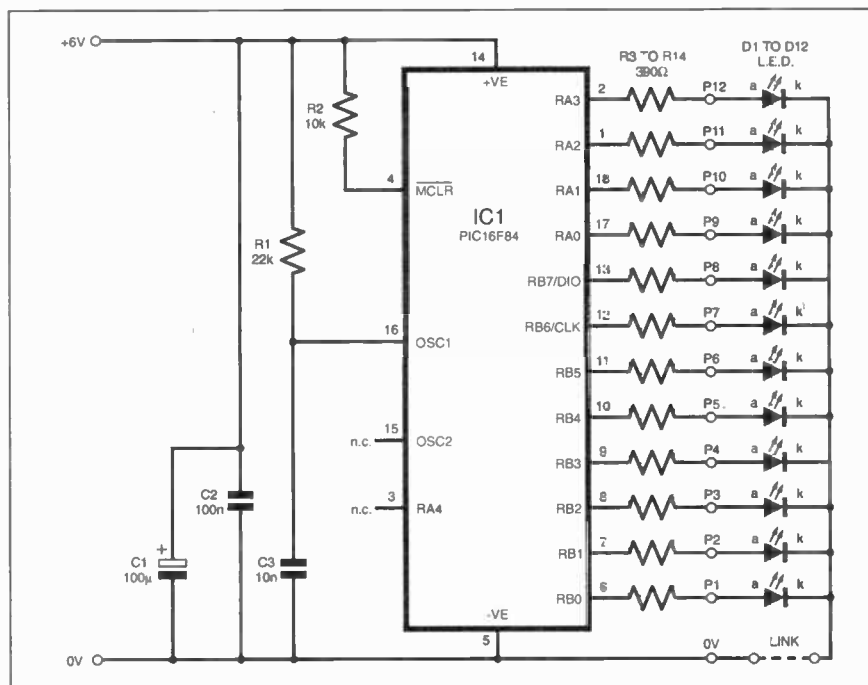


Fig.1. Complete circuit diagram for the PICtogram.

The full circuit for the PICtogram novelty project appears in Fig.1. For the greatest simplicity and lowest cost the PIC's RC (resistor-capacitor) clock option is used with a frequency set by resistor R1 and capacitor C3 to about 4kHz. Since the PIC divides the oscillator frequency by a factor of four, this results in processing taking place at about 1ms per step, which may help to make program timing simple to calculate.

In practice, the effects of the patterns are very subjective, it's easier to just "fiddle" with the timing factors until it "looks right". The MCLR connection is pulled high by resistor R2, which has a higher than usual value to allow it to be programmed "in circuit" by John Becker's excellent *PIC Toolkit Mk2* programmer. The high value is needed to allow the 12V programming voltage to pull this pin high enough to obtain programming mode.

Twelve outputs are used, consisting of the whole of Port B and the first four of

It was found necessary to reduce the loading on pins RB6 and RB7 for in-circuit programming. An easy, single-connection way to achieve this is by disconnecting the common-cathode connection to all the l.e.d.s via the link shown in Fig.1.

For extended programming sessions a switch can be connected across this point so that it can be easily disconnected whilst programming actually takes place. The link also allows the built-in l.e.d. display to be easily disabled if the unit is programmed with the help of this and then connected to an external display.

Capacitors C1 and C2 are the usual supply decouplers used in battery operated circuits of this type.

There is a choice of methods for mounting the l.e.d.s. The printed circuit board holds the PIC and its components, plus facilities for mounting 12 l.e.d.s in a small circle.

As discussed later, the l.e.d.s could alternatively be mounted on a separate plastic

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strip and connected back to the p.c.b. by a ribbon cable. In this case the ring of l.e.d.s on the p.c.b. would be omitted.

CONSTRUCTION

The PICtogram project is built up on a small printed circuit board (p.c.b.), which also accommodates a ring of programming/display l.e.d.s. This board is available from the *EPE PCB Service*, code 279.

The p.c.b.'s assembly and layout details are shown in Fig.2. It has the built-in ring of l.e.d.s with the common-cathode disconnection link at their centre. A line of connections for external output to the optional l.e.d. assembly is included (terminals P1 to P12, 0V).

Construction of this board is carried out by fitting the link wire, resistors, ceramic capacitors, and then the electrolytic capacitor C1. This capacitor is positioned horizontally on the board to achieve a low profile.

Ideally, a socket should be used for the PIC IC1, but this makes it the tallest component on the board! For the lowest possible profile IC1 could, perhaps, be simply soldered in place if it is to be programmed in situ. However, note that soldering a commercially pre-programmed PIC would probably negate its guarantee.

The l.e.d.s for this board are fitted with their cathodes (k) all facing towards the centre of the circle.

PROGRAMMING TIME

Following construction, it's programming time! A ready-programmed PIC is available for this project with a total of eighteen flashing, rotating and special patterns which are invoked in a sequence which it is hoped will be found both interesting and pleasant. This PIC can be simply fitted into place and the unit powered up, when it should operate with no problems.

COMPONENTS

Resistors

- R1 22k
- R2 10k
- R3 to R14 390Ω (12 off)

See
SHOP
TALK
page

Capacitors

- C1 100μ radial elect. 10V
- C2 100n ceramic
- C3 10n resin-dipped ceramic

Semiconductors

- D1 to D12 red l.e.d., 2mA type (12 off)
- IC1 PIC16F84 microcontroller preprogrammed (see text)

Miscellaneous

Printed circuit board, available from the *EPE PCB Service*, code 279; optional min. s.p.s.t. switch (see text); solder pins; connecting wire; solder etc.

Approx. Cost
Guidance Only

£15

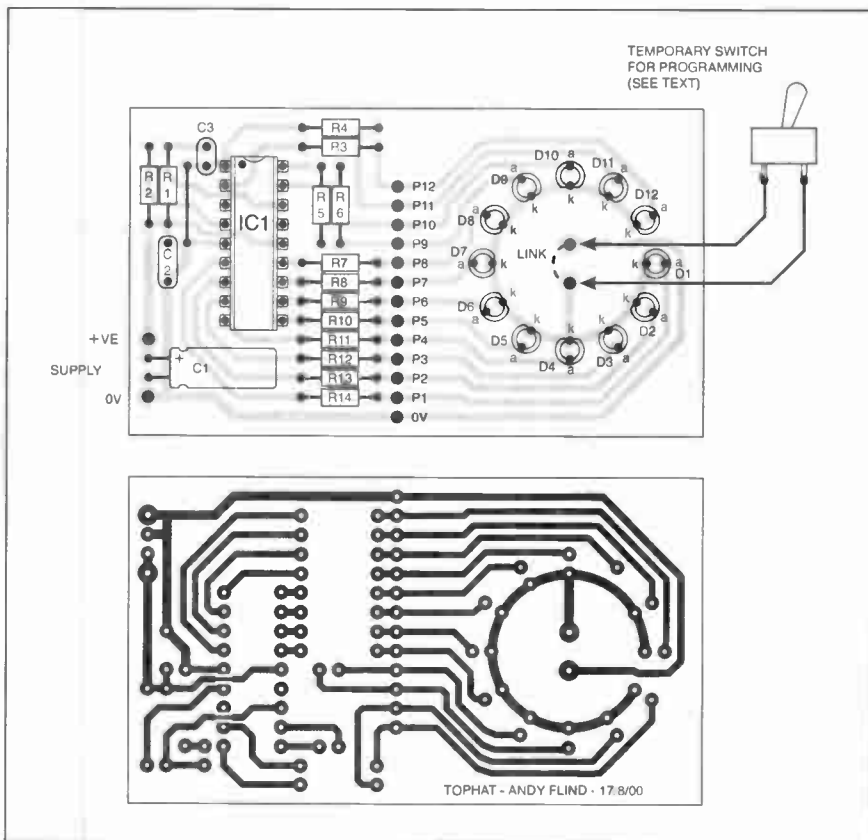
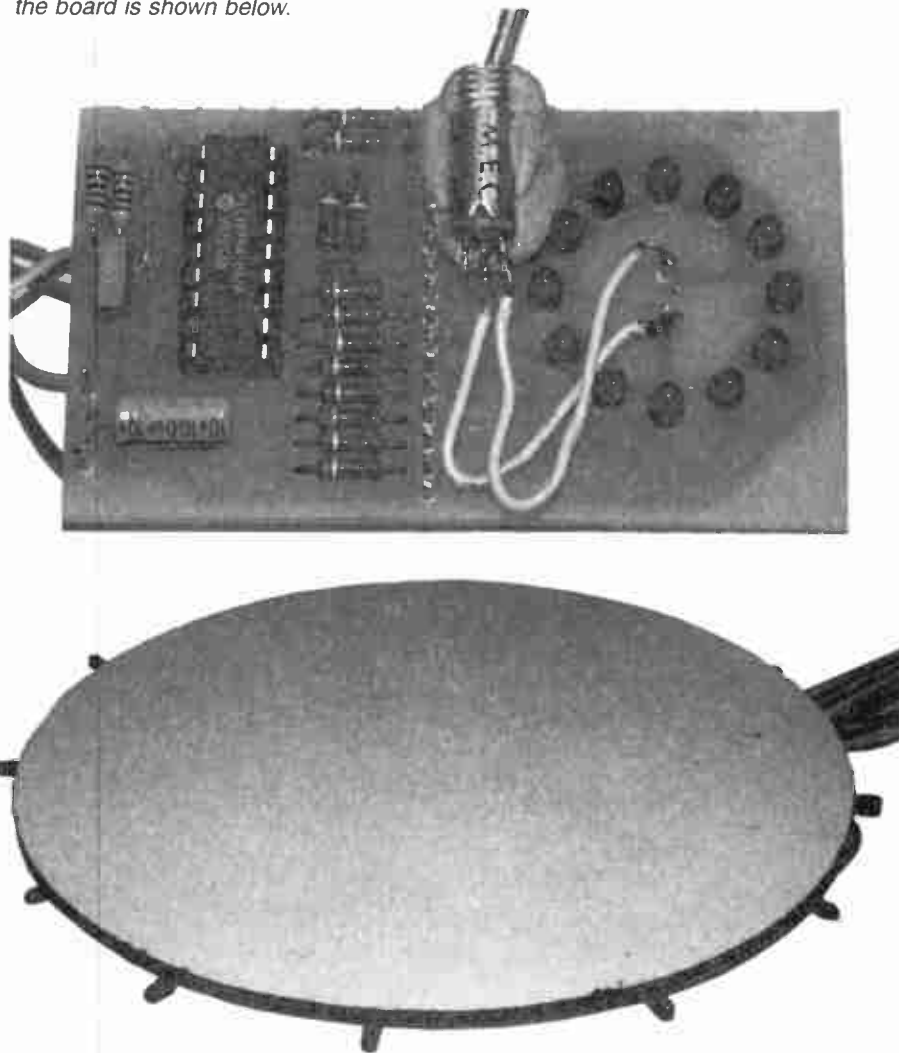


Fig.2. PICtogram printed circuit board (p.c.b.) component layout and full-size copper foil pattern. The completed p.c.b. with the "programming" switch attached to the board is shown below.



Producing an illuminated plaque or wheel by placing the l.e.d.s around the perimeter of a cardboard disc.

However, the real fun is to be had in writing the programs for a project of this kind and hopefully this is what many readers will choose to do. As mentioned earlier the author used John Becker's *Toolkit Mk2* for programming, though it has to be admitted that the one used had been modified to include, amongst other things, a beefier 5V regulator.

If a "standard" *Toolkit Mk2* is used, it would be advisable to keep an eye on the temperature of the regulator when the l.e.d.s are being driven, or to use an external supply for this project.

The connections for in-circuit programming are shown in Fig.3. *Toolkit Mk2* makes programming a very simple operation since the 12V programming voltage and reset pulses to MCLR, plus most other housekeeping jobs, are carried out automatically by the associated software.

It will be necessary to disconnect the link to the l.e.d. cathodes whilst programming is taking place though, and the temporary connection of a switch for this is shown in Fig.2.

Most constructors will probably start by obtaining an unprogrammed PIC and a copy of the software source code, most of which should be fairly simple to follow.

As this is an ideal project to hone initial programming skills, some notes on the methods used may be helpful. The original was written as TASM assembly source code but it shouldn't be too difficult for MPASM users to follow and modify as desired (*Toolkit Mk2* can translate between TASM and MPASM).

ROUTINE EVENTS

The program uses three basic types of routine. One consists of loading a pattern onto the output, implementing a delay, then loading another pattern. Or, perhaps, just turning all the l.e.d.s off and doing another delay, at a set speed for a set number of times, using a loop. This is the simple flashing routine, and there are eight of these in the original program.

Another routine consists of loading a pattern and then causing it to rotate clockwise or anti-clockwise with a simple procedure to rotate the bits in the output files which can be used in a repeating loop. There are three of these patterns, in both clockwise and anti-clockwise versions, a total of six in all.

Finally, there are the more complicated patterns, where bits are turned on and off individually, which give the most pleasing results of all but produce a lot more code. There are four of these.

All these routines are written as self-contained subroutines so that a "program" can be built up simply using a string of "calls". Timing is carried out by a simple loop called "dly" placed right at the end of the program. This uses a variable called "rate" which is loaded before "dly" is called. Another variable called appropriately "reps" controls the number of times each pattern will repeat.

The effect generated by the flashing l.e.d. patterns is highly subjective so there really is no real alternative to testing each

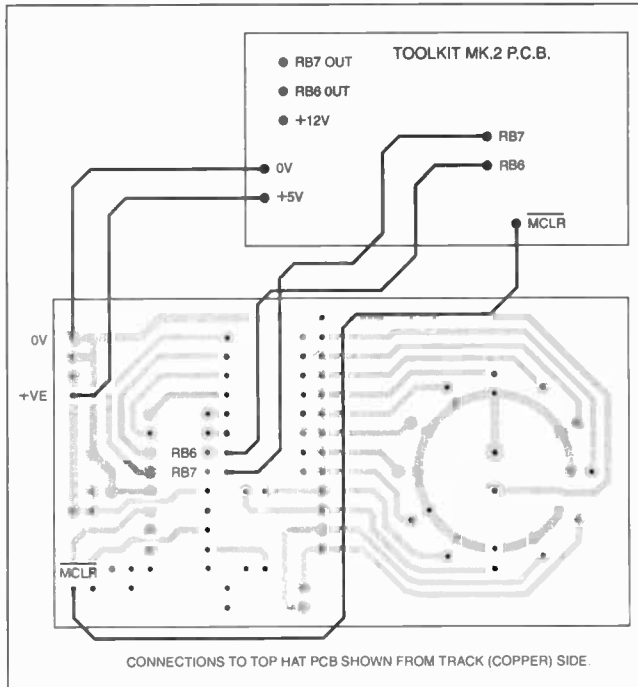


Fig.3. In-circuit programming set-up using the Toolkit Mk2 programmer.

one as it is written to see what it looks like, and then adjusting the timing and repeats to get the preferred effect.

A glance at the full source code will show that the routines, when assembled into a full program, take up a lot of space. This creates problems simply of scrolling through them all in the edit screen during the trial-and-error creation process, and for those of us who do this sort of work with

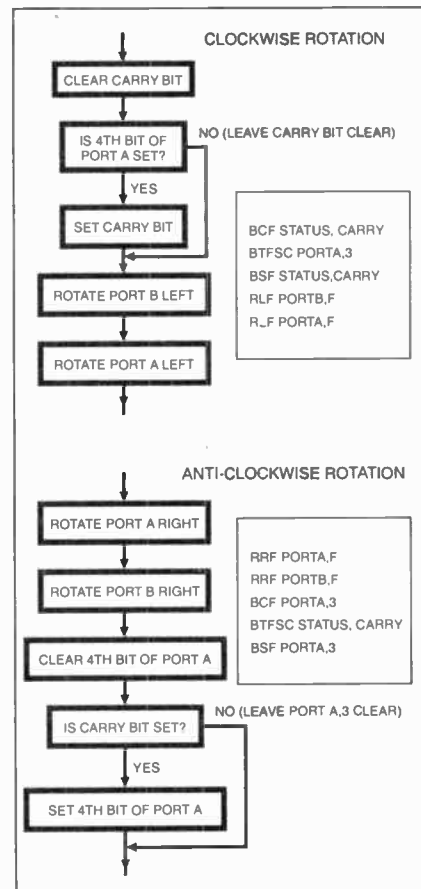


Fig.4. Flow diagram for rotating a "file-and-a-half" left and right.

ancient computers that have been relegated to the workshop, whilst keeping the gigahertz Pentium mega-machine elsewhere for web surfing, it can lead to slow processing and programming.

John Becker has recently released a new version of *Toolkit Mk2* software (discussed in *EPE* Nov '00) which provides the incredibly useful "include" directive to overcome this difficulty.

Let's say a piece of code, perhaps a complete flash routine, has been tested and is working perfectly. It can be saved on its own under a filename, perhaps "flash_1.asm", and replaced in the main program with the statement ".include flash_1.asm" (or "\$include flash_1.asm"). At assembly time, the assembler will simply add the code into the main program and convert it into object code as if it were there in full.

The advantages of this are twofold. First, there will be far less to scroll through when entering the text editor and navigating to the section currently under development. Secondly, an

"include" can be excluded from the assembly process with a single ";" (semicolon) placed before it, making it much easier to turn whole chunks of program on and off and, in the case of older computers, speeding assembly and programming times.

MPASM will have similar commands and directives, and programmers not familiar with them should investigate as they are incredibly helpful. Their inclusion (sorry!) in the latest *Toolkit* software is most welcome.

Rotating bits around a file and a half, Port B and the first four bits of Port A, may appear difficult to beginners in programming. In fact, the procedure is quite simple and involves only a few lines of code. The trick is to read and set the status carry bit at the appropriate points so that the desired value is read in on the next rotation command. Flow diagrams and some code for this are shown in Fig.4.

Hopefully these notes will encourage would-be programmers to dip their toes in the water, as this is an ideal project to experiment with. It helps to have a picture of the l.e.d. layout labelled with their port and bit assignments whilst designing the

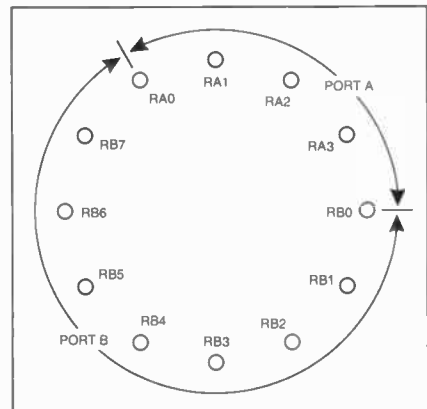


Fig.5. Circuit board l.e.d. layout, with their port and bit assignments.

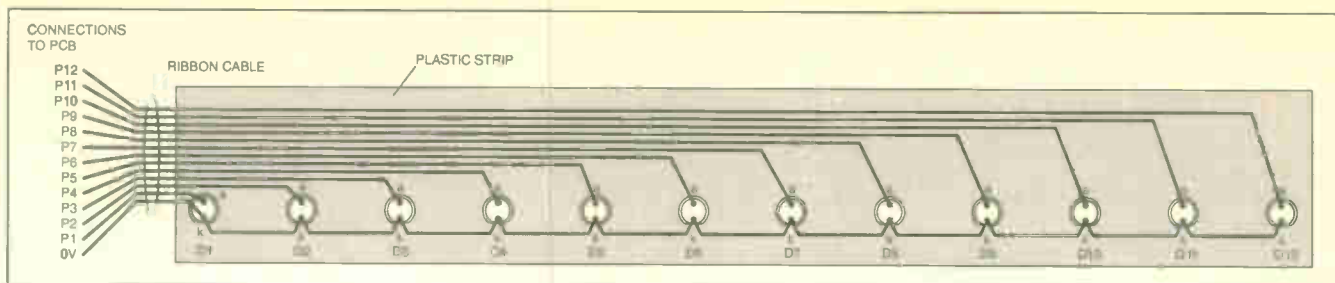


Fig.6. Interwiring between the I.e.d.s in the hat or head band.

more complex patterns, so this is shown in Fig.5.

The number of patterns and sequences that can be developed is practically endless; perhaps we should hold a competition for the most spectacular one produced! (Well, perhaps not. But the code might be worthy of a mention in Readout!. Ed.)

Incidentally, John Becker's PICtutor is probably still the best introduction around for the complete newcomer to the PIC and would easily enable anyone to learn how to program this project.

ON DISPLAY

The original top hat display was constructed using plastic shelf-edging strip obtained from a DIY store. A piece of this was cut just long enough to go around the hat and be secured in place with elastic. This strip was drilled with a line of holes which were a tight fit for the I.e.d.s, which were pressed in and connected to the board with ribbon cable as shown in Fig.6.

The board was taped into some bubble-wrap packaging with the battery connector hanging out of one side, and this was pressed onto the connector of a holder containing four AAA cells; there was no on-off switch. The battery and board sat in the rim of the hat and, like the I.e.d. band, were kept in place with elastic. This was, after all, a serious rush job!

Readers possessing their own top hat could install the board and I.e.d.s directly into the hat with some kind of secret switch, where it would be almost invisible until switched on, for a far better effect. Those attending a lot of weddings might like to consider this option! (But do be



The two ends of the I.e.d. carrying plastic strip are secured with elastic to form a headband, hatband, belt etc: Take your PIC!

aware that these days proper top hats are quite valuable.)

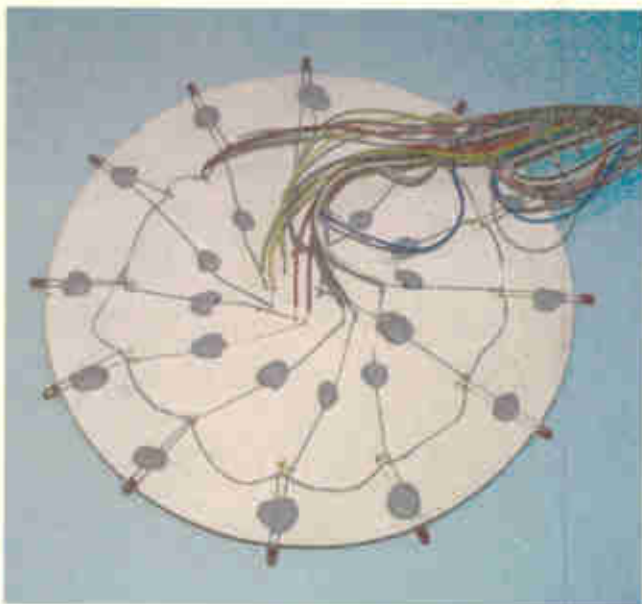
Other possibilities for this project are limited only by the constructor's imagination, though as a suggestion for a very simple method the board could be installed in a small case of some kind using its built in I.e.d. display to make a very unusual brooch, badge, or decoration. The I.e.d.s don't have to be all red, by the way, any other colour could be used instead.

If you are programming your own PIC, note that it needs to be initialised for RC

oscillator, Watchdog off, Start-up Timer on (Power on Reset).

Finally, for readers with the question on their lips, the answer is *No*. this project was not actually worn when walking up the aisle in the church!

The author has been asked this, several times, so the answer has to be provided! It was used late in the evening when the disco was in full swing, the lights were low, and most of the guests were sufficiently well lubricated to appreciate the novelty. □



Off-board I.e.d.s arranged around a cardboard disc.



Programming switch soldered to the "link" pins on the p.c.b.

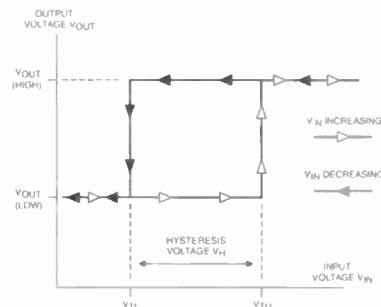
Special Series

THE SCHMITT TRIGGER

ANTHONY H. SMITH

Part 2

In this short series, we investigate the Schmitt trigger's operation; explore the various ways of implementing its special characteristics and also look at how we can use it to create oscillators and pulse width modulators.



Op.amp and Comparator Triggers

In the first part of this series, we looked at discrete Schmitt triggers based on bipolar transistors. Although effective and flexible, we saw how they could be somewhat difficult to design, especially where the interconnection of several transistors demanded careful attention to biasing levels and resistance values.

In this article, we look at Schmitt triggers based on operational amplifiers (op.amps) and comparators, devices which free us from most of the effort required in designing discrete, transistor-based circuits. By considering the op.amp or comparator as a "black box" having just input and output terminals and power supply pins, we can ignore its inner workings to a large degree, and instead concentrate on using it as a highly versatile circuit building block.

However, designing with op.amps and comparators is not a trivial undertaking: they bring their own set of requirements, terminology, and design rules, and if not applied correctly they will either malfunction or suffer permanent damage. The practice of designing with op.amps and comparators is a vast subject that is way beyond the scope of this article. Nevertheless, we'll deal with the main points and examine several practical circuits that illustrate different aspects of Schmitt triggers based on them.

INVERTING SCHMITT TRIGGER

In Fig.2.1a (next page) is shown a simple, inverting Schmitt trigger requiring only two resistors (R_1 and R_2), an op.amp (IC1), and an optional voltage reference (V_{REF}). In some cases, a small "speed-up" capacitor, C_S , may be connected across R_2 to improve the transient response. Before examining the operation of the circuit, we'll deal with some basic op.amp behaviour

The op.amp's input terminals are denoted "+" for the non-inverting input, and "-" for the inverting input. The term non-inverting implies that a voltage applied to that terminal will cause the output voltage to "move" in the same direction, i.e., with the same polarity.

For example, applying a small negative voltage to the non-inverting input will result in a much larger negative voltage at the output. The opposite is true of the inverting input, where a small negative voltage would be "inverted" at the output and result in a much larger positive voltage.

The op.amp is a differential amplifier, meaning that it amplifies the voltage difference between the input terminals. Ideally, an op.amp would have infinite open-loop differential gain. In practice, this can never be achieved, but most op.amps do have very high differential gain, usually in the order of 100,000 or more. The venerable 741, for example, has a typical open-loop gain of around 200,000, meaning that a differential voltage of just $10\mu\text{V}$ will swing the output by 2V.

If the input terminals of an ideal op.amp were shorted together to make the differential input voltage zero, the output voltage would also be zero. In practice, however, all op.amps feature a small "input offset voltage" (usually denoted V_{IO} , or sometimes V_{OS}) which results in a non-zero output voltage when the inputs are shorted together.

For example, an input offset voltage of +2mV would require applying an actual differential voltage of -2mV in order to

"neutralise" the offset and make the output voltage zero. General-purpose op.amps like the 741, LM358 and MC33171 have V_{IO} in the millivolt range, whereas precision devices such as the OP177 have offsets that are a thousand times smaller, typically just $10\mu\text{V}$.

For the circuits we'll be examining in this article, we can assume V_{IO} is negligible, although for precision Schmitt triggers it must be taken into account.

One thing we cannot ignore, however, is the effect of input bias currents. Ideally, an op.amp's input terminals would have an infinitely large impedance, such that they would draw no current from an input voltage source. In practice, all op.amps exhibit an "input bias current" which, as the name suggests, is the current necessary to bias the input transistors.

Usually denoted I_B , the input bias current may flow into or out of the input, depending on the op.amp type, and tends to be larger for devices fabricated using a bipolar process.

For example, the inputs of bipolar op.amps like the LM358 and MC33078 draw bias currents of a few tens or hundreds of nano-amperes. Devices fabricated using JFET or MOSFET technology, on the other hand, exhibit much smaller bias currents. The TLC271, for example, has a MOSFET input stage with typical input bias currents of just 0.7 picoampere at room temperature. More about input bias currents later.

POWER SUPPLIES

The diagram in Fig.2.1a shows the op.amp connected to positive and negative power rails, $+V_S$ and $-V_S$, respectively. Typically, dual supplies like this may range from $\pm 5\text{V}$ to $\pm 15\text{V}$, depending on the application, although some op.amps and comparators can operate on rails as low as $\pm 1\text{V}$.

For dual rail (sometimes called "split supply") circuits, it's important to remember that there is a third power supply connection, namely 0V (or "ground"). Although the op.amp is not usually connected directly to 0V, the power supply, the input voltage source(s) and the output load usually are connected to 0V in some way, and the input and output voltages are almost always measured with respect to 0V.

A slight variation on this arrangement is found in "single rail" applications, where the negative rail is omitted and the op.amp's negative supply terminal is connected to 0V. Single rail circuits are increasingly used in applications where an analogue signal of some kind must interface with digital logic operating on a single rail, typically +5V or +3.3V. The Schmitt trigger provides an extremely powerful way of interfacing analogue and digital circuits, and we shall look at single rail Schmitt triggers later.

COMMON MODE

We've mentioned that the op.amp amplifies differential signals: ideally, any common-mode voltage will be totally rejected and will have no effect on the output. A common-mode voltage is one which appears in common to both inputs.

Suppose, for example, we shorted both inputs together and connected them to +2.5V (with respect to 0V), the common-mode

voltage would be +2.5V. If we then connected one input to -1V and the other to -2V, the differential voltage would be 1V, and the common-mode voltage would be the mean voltage between the inputs, in this case -1.5V.

In practical circuits, op.amps do not provide total rejection of the common-mode voltage, although the *common-mode rejection* (the degree to which the common-mode signal is rejected) is usually so good that common-mode effects can be ignored.

Still on the subject of common-mode signals, the "common-mode input voltage range" defines the range of common-mode voltages that can be tolerated by a given op.amp. This is not usually the same as the "differential voltage range" which defines the range of voltage that can appear between the inputs without causing malfunction or damage.

Both of these parameters depend on the supply voltage. The LM741, for example, has a maximum differential input voltage rating of $\pm 30V$. Exceeding this limit could cause permanent damage. When operating on $\pm 15V$ supply rails, the common-mode input voltage range is typically $\pm 13V$, which means that the voltage at each input must not go within 2V of either supply rail or the op.amp might not function properly.

The LM358, however, is specifically intended for single rail applications. For example, when operating on a single +5V rail, the common-mode input voltage may go as high as +3.5V and may go all the way down to 0V. Modern op.amps and comparators frequently offer "rail-to-rail" performance. This means that the input voltage range, or output voltage range, or sometimes both, may cover the entire range from one supply rail to the other.

The LMC6482, for example, is a "Rail-to-Rail Input and Output" op.amp. When operated on, say, $\pm 5V$ rails, the input voltage may be permitted to take any value between -5V and +5V, and the output voltage will typically swing to within 20mV of each rail (i.e., $\pm 4.98V$) for load resistances greater than 100k Ω .

When used in "linear" applications (i.e., applications in which *negative* feedback is applied to keep the op.amp within its linear range), the op.amp's input voltage ratings are often not excessively taxed. However, when used in Schmitt trigger circuits, the *positive* feedback frequently forces the inputs to cover a wide range, resulting in large common-mode and differential voltages.

Consequently, it's essential to check the worst-case, maximum input voltage range for a given application to ensure the op.amp or comparator will function correctly.

POSITIVE FEEDBACK

Having discussed basic op.amp theory, we can now return to Fig.2.1a and examine the operation of the inverting Schmitt trigger.

To simplify the analysis, assume the reference voltage V_{REF} is zero (i.e., R1 connected to 0V) and that V_{IN} is at some negative voltage, such that the voltage at the op.amp's inverting input is lower (more negative) than that at the non-inverting input, denoted $V+$. If the resulting positive differential input voltage is greater than a few millivolts, the op.amp's output will be in positive saturation, V_{SAT+} , i.e., the output will be at its maximum positive level.

The non-inverting input voltage, $V+$, will sit at a value determined by the ratio of R1 and R2, and by the value of V_{SAT+} . If V_{IN} now rises above the level of $V+$, the differential input voltage becomes negative forcing V_{OUT} also to go negative. This causes $V+$ to go negative, which increases the negative differential voltage, and ultimately forces V_{OUT} into negative saturation, V_{SAT-} .

As with the discrete Schmitt triggers described in Part One, the positive feedback via R2 causes *regenerative* behaviour which reinforces the switching action, causing a rapid transition from one output state to the other.

The value of V_{IN} required to "trigger" this change of state is denoted the "upper threshold voltage", V_{TU} , and is given by:

$$\text{Upper Threshold Voltage, } V_{TU} = \frac{R1 \times V_{SAT+}}{R1 + R2} \quad (\text{volts})$$

Since V_{OUT} has gone into negative saturation, $V+$ now sits at a negative voltage. If V_{IN} , and hence the inverting input terminal, is now taken more negative than $V+$, the differential voltage will again become positive and regenerative action will force V_{OUT} into positive saturation, V_{SAT+} . The value of V_{IN} required to initiate this opposite change of state is denoted the "lower threshold voltage", V_{TL} , and is given by:

$$\text{Lower Threshold Voltage, } V_{TL} = \frac{R1 \times V_{SAT-}}{R1 + R2} \quad (\text{volts})$$

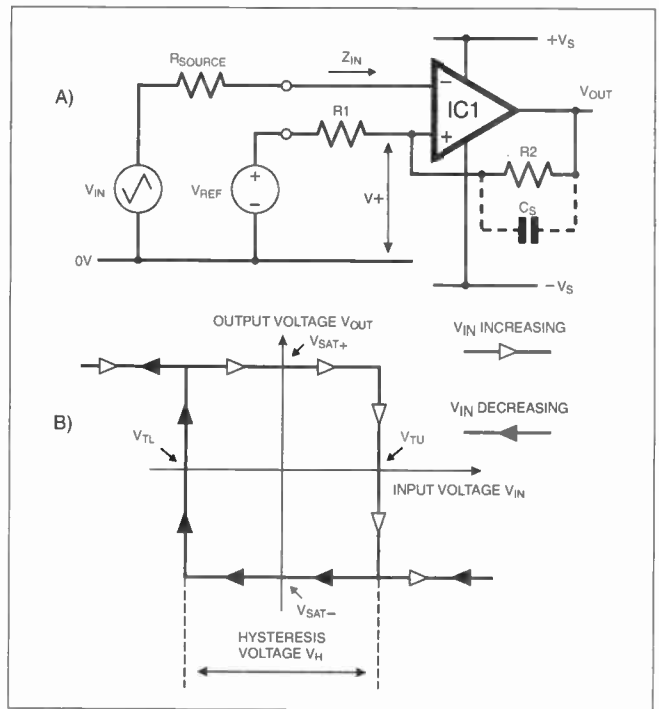


Fig.2.1. Circuit diagram for an Inverting Schmitt Trigger (a) and its voltage transfer characteristic (b).

Note that when V_{IN} goes positive and crosses the upper threshold, the output goes negative, hence the term *inverting* Schmitt trigger. We can see at a glance that the circuit is inverting because V_{IN} is applied to the op.amp's inverting input terminal.

The diagram in Fig.2.1b shows the circuit's "voltage transfer characteristic", i.e., the relationship between input and output voltage. Starting at the top left-hand corner and following the white arrows as the input voltage increases, we see that the output remains at V_{SAT+} until V_{IN} crosses the upper threshold, V_{TU} , at which point the output rapidly changes state and goes into negative saturation, V_{SAT-} .

Rapidly increases in V_{IN} have no effect on V_{OUT} . As V_{IN} decreases (shown by the black arrows), V_{OUT} remains at V_{SAT-} until V_{IN} crosses the lower threshold, V_{TL} , where V_{OUT} abruptly changes state and goes back into positive saturation.

The transfer characteristic shown assumes that V_{SAT+} is equal and opposite to V_{SAT-} and that V_{TU} is equal and opposite to V_{TL} , resulting in a "hysteresis loop" that is symmetrical about the origin. However, this is not always the case: depending on the application, it may be necessary to make the magnitude of the thresholds unequal, or to make them both positive or both negative. Also, as we shall see shortly, V_{SAT+} is not always equal and opposite to V_{SAT-} .

The thresholds can be varied by appropriate choice of R1 and R2, and by introducing a non-zero reference voltage (so far, we have assumed that $V_{REF} = 0$).

Referring again to Fig.2.1a, assume we apply a positive value of V_{REF} : whatever the value of V_{OUT} , this will result in $V+$ becoming more positive. The effect of making V_{REF} positive is to shift the thresholds "upward", i.e., more positive. Similarly, making V_{REF} negative would shift the thresholds negative. To incorporate the effect of V_{REF} , the threshold equations become:

$$\text{Upper Threshold Voltage, } V_{TU} = \frac{(V_{REF} \times R2) + (R1 \times V_{SAT+})}{R1 + R2} \quad (\text{volts})$$

and:

$$\text{Lower Threshold Voltage, } V_{TL} = \frac{(V_{REF} \times R2) + (R1 \times V_{SAT-})}{R1 + R2} \quad (\text{volts})$$

The "hysteresis" voltage is the difference between the thresholds:

$$\text{Hysteresis voltage, } V_H = V_{TU} - V_{TL} = \frac{R1 \times (V_{SAT+} - V_{SAT-})}{R1 + R2} \quad (\text{volts})$$

Note that V_H is completely independent of V_{REF} : this is an important aspect of the circuit, since it allows the thresholds to be shifted by varying V_{REF} without affecting the hysteresis voltage.

The circuit's response to a triangle wave input voltage is shown in Fig.2.2a. V_{REF} has been set to a sufficiently large positive voltage, such that both thresholds are also positive; in Fig.2.2b, a negative value of V_{REF} has shifted both thresholds negative.

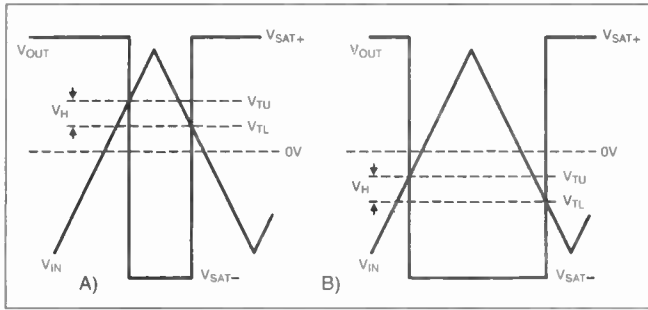


Fig.2.2. Response to a triangle wave input for positive (a) and negative (b) V_{REF} .

Varying V_{REF} allows the thresholds to be shifted over a wide range of positive and negative values. This can be a particularly useful feature: having chosen R1 and R2 to set the desired hysteresis voltage, V_{REF} may then be selected to set the mid point of the hysteresis band equal to the quiescent value of the input signal, such that the circuit can accommodate small-amplitude input signals whilst providing maximum noise immunity.

INPUT IMPEDANCE

Our analysis of the circuit has ignored the effects of input offset voltage, V_{IO} ; this is a reasonable approach provided the circuit does not demand absolute precision. However, the op.amp's input impedance cannot always be neglected.

Generally, the impedance Z_{IN} seen "looking into" the inverting input can be represented by the same kind of model introduced in Part One, namely a parallel combination of resistance, capacitance, and a current sink (or source) to represent the input bias current.

At low frequencies we can usually ignore the effects of input capacitance, and if we assume the input resistance is large (several megohms) we can concentrate on the effects of input bias current.

For example, consider the LM6171, a high speed op.amp capable of operation at frequencies in excess of 10MHz. The input bias current, I_B , is typically $1\mu A$, but can be as high as $3\mu A$. If the input voltage source resistance, R_{SOURCE} , is very small, I_B will have negligible effect.

However, for a source resistance of, say, $100k\Omega$, a bias current of $2\mu A$ would drop $0.2V$ across R_{SOURCE} , resulting in significant errors in the threshold levels.

Even if R_{SOURCE} is zero, we must still consider the effects of I_B at the non-inverting input: if R1 and R2 are relatively large, the input bias current will cause a voltage drop across them which again will offset the threshold levels. To avoid these problems, either use small values for R1 and R2, or select an op.amp (or comparator) that has very small input bias currents.

TESTING THE CIRCUIT'S PERFORMANCE

To demonstrate the circuit's performance, it was decided to use an LF351 op.amp. As well as offering fast response, the LF351 has a JFET input stage with typical input bias currents of just 50 picoamperes, allowing it to accommodate large resistance values without affecting the thresholds.

With $R1 = 10k\Omega \pm 1\%$, $R2 = 100k\Omega \pm 1\%$, and with the supply rails set to precisely $\pm 15.00V$, the circuit's response to a 100Hz triangle wave input voltage was measured. It was found that the op.amp's output saturation levels were $V_{SAT+} = +14.25V$ and $V_{SAT-} = -13.55V$.

Therefore, with $V_{REF} = 0$, the thresholds should be $V_{TU} = +1.30V$ and $V_{TL} = -1.23V$. The actual, measured values were $V_{TU} = +1.31V$ and $V_{TL} = -1.21V$. Pretty good!

Next, a reference voltage was introduced. With $V_{REF} = +5.00V$, the thresholds were $V_{TU} = +5.88V$ and $V_{TL} = +3.36V$, very close to their theoretical values of $V_{TU} = +5.84V$ and $V_{TL} = +3.31V$.

Finally, with $V_{REF} = -5.00V$, the thresholds were $V_{TU} = -3.26V$ and $V_{TL} = -5.80V$, again in close agreement with their theoretical values of $V_{TU} = -3.25V$ and $V_{TL} = -5.78V$.

Note that for each value of V_{REF} , the hysteresis voltage, V_H , remains fairly constant at $\approx 2.5V$.

ZENER CLAMP OUTPUT SCHEME

We see from the previous example that the output saturation levels are not equal in magnitude, i.e., $|V_{SAT+}| \neq |V_{SAT-}|$, which results in an asymmetry in the thresholds. Furthermore, the output saturation levels may change from part to part, and may also vary with temperature and load.

Since V_{TU} and V_{TL} depend directly on V_{SAT+} and V_{SAT-} , this can make it difficult to establish the thresholds precisely and repeatedly. To some extent, this problem can be resolved by using an op.amp (or comparator) with rail-to-rail output swing, but even then the saturation levels would be affected by any variation in the supply voltages.

In Fig.2.3 are shown two methods which can be used to establish greater control over the output voltage levels. In Fig.2.3a, a back-to-back Zener "clamp" has been added to the output and feedback is now taken from the clamp via R2, rather than from the op.amp's output.

The Zener clamp is "bi-directional": as the op.amp output swings between its positive and negative saturation levels, the output voltage, V_{OUT} , at the junction of R3 and ZD1 also swings positive and negative. We can define these levels V_{Z+} and V_{Z-} , such that $V_{Z+} = V_{Z1} + V_{D2}$ and $V_{Z-} = V_{Z2} + V_{D1}$, where V_{Z1} and V_{Z2} are the reverse Zener voltages, and V_{D1} and V_{D2} are the Zeners' forward diode drops.

If the Zeners are well matched, i.e., if $V_{Z1} = V_{Z2}$ and $V_{D1} = V_{D2}$, the magnitude of V_{Z+} and V_{Z-} will be equal.

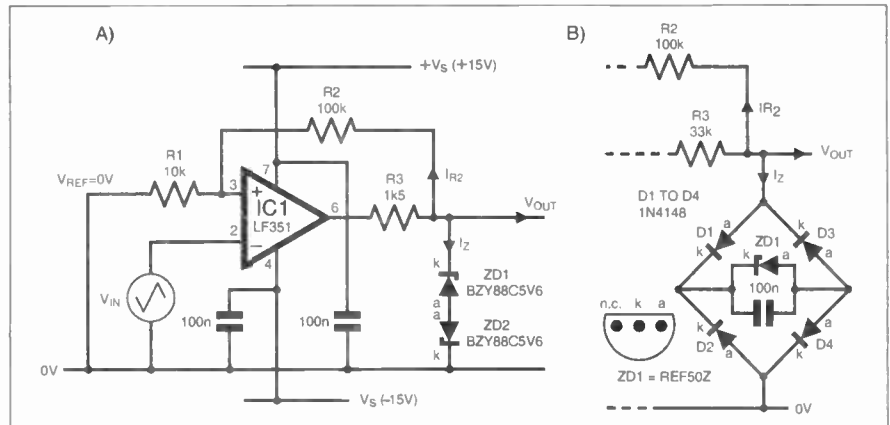


Fig.2.3. Two methods which can be used to give greater control over output voltage levels, (a) using a back-to-back Zener clamp and (b) using a diode bridge.

For example, using 5.6V Zeners as shown in Fig.2.3a, it was found that the voltage at V_{OUT} was perfectly symmetrical at $\pm 6.0V$, and with $R1 = 10k\Omega \pm 1\%$ and $R2 = 100k\Omega \pm 1\%$ as before, and with $V_{REF} = 0$, the thresholds were also symmetrical at $\pm 0.63V$.

Note that R3 must be small enough to provide adequate current, I_Z , to bias the Zeners properly, and must also provide the feedback current, I_{R2} , that flows in R2. Provided R3 is chosen carefully, this technique will provide a relatively constant, symmetrical bipolar voltage swing at V_{OUT} .

A STABLE BRIDGE

The output clamp method can be improved still further using the scheme shown in Fig.2.3b. Here, the D1-D4 diode bridge maintains a positive potential at the cathode of regulator diode ZD1 for both positive and negative swings at V_{OUT} . If we assume the forward voltage drops across each of the bridge diodes are equal and denoted V_D , the output voltage swing is $V_{OUT} = \pm(2V_D + V_Z)$, where V_Z is ZD1's reverse voltage.

Although ZD1 could be a Zener, even better performance can be obtained using a precision shunt voltage reference diode. Here a choice was made to use the REF50Z, a micropower 5.0V reference diode, although other devices such as the REF12Z (1.26V) and REF25Z (2.5V) could be used to provide different clamping voltages.

Note that R3 has been increased from $1.5k\Omega$ to $33k\Omega$, since the REF50Z requires much less bias current than the back-to-back Zeners.

This "reference-in-a-bridge" approach generated an output voltage swing of $\pm 6.02V$, and with $R1 = 10k\Omega \pm 1\%$, $R2 = 100k\Omega \pm 1\%$ and $V_{REF} = 0$ as before, the thresholds were $V_{TU} = \pm 0.575V$ and $V_{TL} = -0.570V$.

USING COMPARATORS AND SINGLE RAILS

The examples so far have focused on a circuit using an op.amp working on dual supply rails. However, in many applications, it may be better to take advantage of the superior switching qualities offered by a comparator (see panel "Comparator Essentials"). We must also consider the biasing requirements of single rail applications and the use of "open-collector" (or "open-drain") outputs.

In Fig.2.4a is shown an inverting Schmitt trigger using one half of the popular LM393 comparator. Although the LM393 can work on dual supplies from $\pm 1\text{V}$ to $\pm 18\text{V}$, it is particularly suited to single rail operation since the common-mode input range goes all the way down to the negative rail (0V for single rail applications).

The reference voltage is generated by the potential divider comprising resistors R1a, R1b and the positive supply:

$$V_{\text{REF}} = \frac{+V_s \times R1b}{R1a + R1b} \quad (\text{volts})$$

For dual rail applications, a negative reference may be generated by connecting R1a to $-V_s$.

Since the LM393 has an open-collector output, pull-up resistor R_{PU} is required to pull the output voltage up toward $+V_s$ when the output transistor turns off. However, R_{PU} must be included in the expression for V_{TU} since it effectively appears in series with R2.

The thresholds are given by:

Upper Threshold Voltage,

$$V_{\text{TU}} = \frac{V_{\text{REF}} \times (R2 + R_{\text{PU}}) + R_{\text{TH}} \times (+V_s)}{R_{\text{TH}} + R2 + R_{\text{PU}}} \quad (\text{volts})$$

and: Lower Threshold Voltage,

$$V_{\text{TL}} = \frac{(V_{\text{REF}} \times R2) + (R_{\text{TH}} \times V_{\text{SAT-}})}{R_{\text{TH}} + R2} \quad (\text{volts})$$

R_{TH} is the Thévenin equivalent resistance of the R1a-R1b potential divider:

$$R_{\text{TH}} = \frac{R1a \times R1b}{R1a + R1b} \quad (\text{ohms})$$

Note that the expression for V_{TU} is only true for a lightly loaded output (for example, driving a CMOS logic gate). For heavier loads which prevent R_{PU} pulling the output all the way up to $+V_s$, the expression must be modified by removing R_{PU} and replacing $+V_s$ with $V_{\text{SAT+}}$, the maximum positive output voltage, which must be determined for the particular application.

CUT THE CHATTER

A problem sometimes encountered when comparators are misapplied is "chatter" at the output. With slowly varying input signals, comparators tend to produce multiple output transitions when the input signal crosses the reference potential.

As the input traverses the linear region, the comparator behaves as a very high gain, open-loop amplifier. The slightest noise on the input is amplified by the enormous gain of the comparator causing "chatter" at the output.

For example, the LM393 has a typical open-loop voltage gain of 200V/mV (i.e., 200,000), so to cause a 5V output transition requires an input noise amplitude of only $5/200,000 = 25\mu\text{V}$.

Stray capacitances around the comparator can result in a.c. feedback from output to input causing oscillation around the threshold, another source of output chatter.

Fortunately, hysteresis may be used to eliminate these problems. Usually, applying just a little positive feedback, say a few millivolts, may be enough to prevent the chatter. Naturally, for signals with larger noise content, the hysteresis, and hence the positive feedback, must be increased.

Chatter can sometimes be difficult to spot on an oscilloscope, but causes unacceptable errors in counting circuits.

SINGLE RAIL TESTS

A single rail version of the circuit in Fig.2.4a was built by connecting the comparator's negative supply terminal (pin 4) to 0V. Resistance values were selected for $R1a = R1b = 36\text{k}\Omega \pm 1\%$ to give $R_{\text{TH}} = 18\text{k}\Omega \pm 1\%$. With $R2 = 100\text{k}\Omega \pm 1\%$, $R_{\text{PU}} = 10\text{k}\Omega \pm 1\%$, and $+V_s = +5.00\text{V}$, the "negative" saturation voltage, $V_{\text{SAT-}}$, was measured as $+50\text{mV}$. The thresholds were $V_{\text{TU}} = 2.82\text{V}$ and $V_{\text{TL}} = 2.10\text{V}$, in close agreement with the theoretical values, namely $V_{\text{TU}} = 2.85\text{V}$ and $V_{\text{TL}} = 2.13\text{V}$.

The value of $V_{\text{SAT-}}$ is so small that it can almost be ignored and eliminated from the expression for V_{TL} which reduces to:

$$V_{\text{TL}} = (V_{\text{REF}} \times R2) / (R_{\text{TH}} + R2)$$

Bear in mind, however, that $V_{\text{SAT-}}$ will tend to increase as R_{PU} is reduced. For example, if R_{PU} is reduced to, say, $1\text{k}\Omega$, the LM393's output transistor will sink around 4mA when it turns on, and the corresponding saturation voltage may be as large as 400mV.

HIGH FREQUENCY RESPONSE

So far, we've looked at circuit response using low frequency signals, on the order of 100Hz. However, at high frequencies, where the input signal has a very fast rate of change, the comparator's response time causes an apparent shift in the thresholds.

The waveforms in Fig.2.5 illustrate those obtained from the single rail LM393 circuit when a 250kHz triangle wave input was applied. Initially, the non-inverting input, $V+$, sits at a potential equal to V_{TU} , but when V_{IN} crosses this threshold the output does not change state immediately. Instead, there is a delay denoted $t_{\text{PD-}}$ (for "negative-going propagation delay") before the output leaves its positive saturation level and starts to head negative.

However, it cannot change from positive to negative saturation instantaneously, but takes a finite time to "slew" from $V_{\text{SAT+}}$ to $V_{\text{SAT-}}$. The combined effects of propagation delay and slew rate constitute the response time, and result in the *apparent* value of V_{TU} being significantly higher than the real value of V_{TU} .

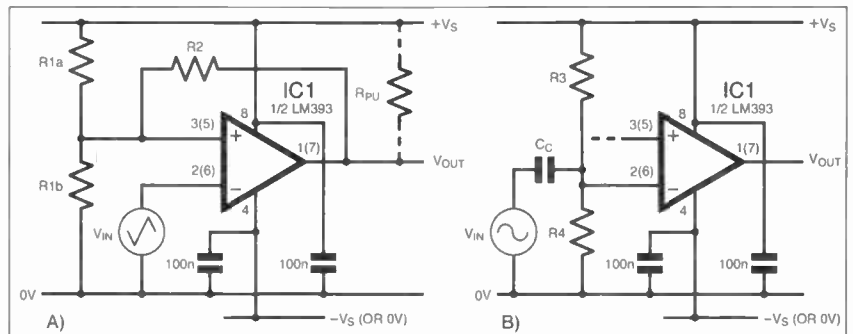


Fig.2.4. A single rail Schmitt trigger circuit using an "open-collector" comparator.

A similar effect occurs when the input signal crosses the lower value of $V+$, i.e., V_{TL} . Again, there is a delay denoted $t_{\text{PD+}}$ (for "positive-going propagation delay") before the output leaves its negative saturation level and starts to move positive. However, this time, the slew-rate effects are more pronounced since the open-collector output depends on the pull-up resistor to swing the output positive.

Since the resistor must charge the comparator's output capacitance plus any stray and load capacitance, the output waveform now acquires an exponential shape. By the time the output waveform crosses the input signal, the *apparent* value of V_{TL} is considerably lower than the real level of V_{TL} .

At low frequencies, where the input signal changes at a relatively slow rate, the effects of comparator response time are usually negligible. However, you should be aware of these effects at high frequencies since they limit the Schmitt trigger's ability to respond to rapidly changing signals.

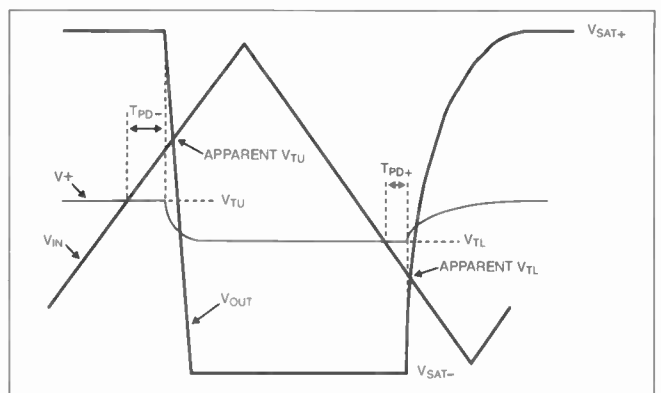


Fig.2.5. Effects of comparator response time on apparent thresholds.

Comparator Essentials

Although op.amps can be used to compare one voltage level with another, the *voltage comparator* is often the better choice. Like the op.amp, the comparator is essentially a high-gain differential amplifier, in that a very small differential input voltage will drive the output into positive or negative saturation.

However, by enhancing certain characteristics such as gain and slew rate, the comparator is optimised for non-linear applications in which the main function is to compare rather than to amplify voltages.

An important comparator a.c. parameter is *response time*, the time delay between an input step voltage and the resulting large-scale change in output voltage. Response time includes the propagation delay through the i.c. and the effects of output slew rate, and varies considerably from one type of comparator to another. For example, the typical response time for the National Semiconductor LM393 is 1.3µs, whereas for the LM360 it is just 14ns.

Comparators are expected to be operated with non-zero differential voltages; this is not necessarily so with op.amps which are mainly intended to be operated "closed-loop" where the differential voltage is close to zero. For example, the TLC372 dual comparator has a differential input range equal to the supply voltage (which can be as high as 18V), whereas the OP97 precision op.amp has input protection diodes which limit the differential input voltage to just ±1V. Always read the data sheet thoroughly to check that a given device is being used properly.

OUTPUT STAGE

Although comparators occasionally feature "push-pull" output stages like op.amps, they often have "open-collector" (or "open-drain") outputs. For example, the NE521 (a high speed, dual comparator) has a push-pull output stage, which means the output voltage is constrained to lie between ground (0V) and the positive supply.

The dual LM393, on the other hand, has an open-collector output (emitter connected to ground), which allows it to drive loads connected to rails higher than its own supply voltage.

The output stage of the LM311 is even more flexible, since both the collector and emitter of the output transistor are "floating", such that it can drive loads referred to ground, to the positive supply or to the negative supply. With the emitter grounded, the collector can drive loads connected to voltages as high as 40V and can sink currents up to 50mA.

Open-collector outputs can be very flexible when interfacing to logic devices, and are also suited to "wired-OR" operation, rather like open-collector TTL gates.

INPUT PARAMETERS

Input bias current, I_B , can vary considerably from one comparator type to another. For example, the LM393 has $I_B = 25nA$ (typical) at 25°C, whereas the TLC393 (dual, open drain) has $I_B = 5pA$ (typical) at 25°C – five thousand times less! Also, note that the TLC393 supply current is approximately one-twentieth that of the LM393, even though the devices are functionally equivalent.

For certain devices, bias current can vary with differential input voltage. The LM311, for example, has a typical input bias current of 100nA at 25°C for zero differential input voltage, but this can vary by ±75nA if the differential input is taken beyond ±8V. Note that 100nA will drop 10mV across a 100kΩ input resistance.

Always check the common-mode input range: this is not necessarily equal to the supply voltages, and is often significantly less. For example, when working on ±15V supplies, the LM311's input voltage range is -14.5V to +13.0V.

WIDE VARIETY

Like op.amps, comparators come in a many different "flavours". Speed (response time), input offset voltage and bias current are some of the parameters to be considered when choosing a suitable device, although supply current, output type and cost can often be equally important. Table 1 lists some of the most popular comparators and details some of the main parameters. Note that this is not an exhaustive list and there are many others to choose from!

If you would rather use an op.amp as a comparator, consider its speed (bandwidth and slew-rate), its ability to drive loads, and its output swing (especially if interfacing to logic circuits). Lastly, remember that comparators are not meant to be operated in linear mode, and so are not internally frequency compensated. Generally, therefore, comparators do not make good op.amps and should not be used as such!

Table 1: Popular Comparators and Their Main Characteristics

Part Number	Manufacturer	Single/Dual/Quad	t_R (typ.)	I_B max.	V_{IO} max. (mV)	I_S max.	Total Supply Voltage (V)		Single Rail Operation?	Output Type	Comments
							min	max			
CA3290	Harris	D	1.2µs	40pA	20	3mA	5	36	Yes	OC	BiMOS design
LM311	National Semiconductor	S	200ns	250nA	7.5	7.5mA	5	36	Yes	OC	popular; inexpensive; flexible output stage
LM319	National Semiconductor	D	80ns	1µA	8	12.5mA	5	36	Yes	OC	fast, but input current is high
LM339	National Semiconductor	Q	1.3µs	250nA	5	2.5mA	2	36	Yes	OC	popular; inexpensive; very low operating voltage
LM361	National Semiconductor	S	1.4ns	30µA	5	20mA	11	30	No	PP	very fast, but input and supply currents are high; differential output may be strobed
LM393	National Semiconductor	D	1.3µs	250nA	5	2.5mA	2	36	Yes	OC	popular; inexpensive; dual version of LM339
LMC6762	National Semiconductor	D	10µs	0.04pA (typ.)	1.5	20µA	2.7	1.5	Yes	PP	input and output voltage range is rail-to-rail; very low power; extremely low input current
LT1016	Linear Technology	S	10ns	10µA	3	35mA	5	10	Yes	PP	very fast, but power hungry; differential outputs may be latched.
LT1017	Linear Technology	D	20µs	15nA	1	90µA	1.2	40	Yes	PP	micropower; very low operating voltage
LT1018	Linear Technology	D	6µs	75nA	1	250µA	1.2	40	Yes	PP	low power; very low operating voltage
MAX931	Maxim	S	12µs	1nA	10	3.2µA	2.5	11	Yes	PP	micropower; includes 1.18V bandgap voltage reference; comparator has adjustable hysteresis
MAX941	Maxim	S	80ns	300nA	2	700µA	2.7	6	Yes	PP	fast; low power; rail-to-rail input voltage range
NE521	Philips Semiconductors	D	10ns	20µA	7.5	35mA	9	11	No	PP	very fast; outputs may be strobed
NE529	Philips Semiconductors	S	15ns	20µA	6	25mA	10	20	No	PP	very fast; differential outputs may be strobed
TLC372	Texas Instruments	D	650ns	5pA	5	300µA	2	18	Yes	OD	low power; very low supply voltage and input current
TLC393	Texas Instruments	D	2.5µs	5pA	5	40µA	3	16	Yes	OD	micropower; very low supply voltage and input current; compare with LM393
TLC3702	Texas Instruments	D	2.7µs	5pA	5	40µA	3	16	Yes	PP	push-pull output version of TLC393
TLC3704	Texas Instruments	Q	2.7µs	5pA	5	80µA	3	16	Yes	PP	quad version of TLC3702

NOTES: All specifications are given for an operating temperature of +25°C. t_R = Response Time (depends on input overdrive). I_B = Input Bias Current
 V_{IO} = Input Offset Voltage. I_S = Supply Current. Total Supply Voltage = difference between positive and negative supply rails.
 V_{IO} = Input Offset Voltage. I_S = Supply Current. Total Supply Voltage = difference between positive and negative supply rails.
 OC = Open Collector; OD = Open Drain; PP = Push-Pull.

A.C. COUPLING

We've seen how the Schmitt trigger's reference voltage can be set to match the mid-point of the hysteresis band to the quiescent, or average, voltage level of the input signal. However, for signals that lie outside the common-mode range of the comparator, a.c. coupling can be used to remove the d.c. level and thus bring the a.c. content of the signal within the comparator's input range.

The circuit diagram in Fig.2.4b shows how the single rail Schmitt trigger can be modified for a.c. coupling. Resistors R3 and R4 establish a suitable d.c. potential at the comparator's inverting input. Usually, it is best to make this potential equal to the mid-point of the comparator's common-mode input range.

For example, when operating on a single +5V rail, the LM393's common-mode input range is zero to 3.5V, so R3 and R4 would be selected to set the d.c. level at the inverting input to 1.75V. The a.c. signal is capacitively coupled via C_C to the inverting input, allowing the circuit to accept a.c. signals up to $\pm 1.75V$ in amplitude, or 3.5V peak-to-peak.

Resistors R1a and R1b would be chosen to set the mid point of the hysteresis band equal to 1.75V, and by selecting R2 and R_{PU} to set the hysteresis voltage just less than the minimum peak-to-peak amplitude of the input signal, the Schmitt trigger will provide maximum noise immunity.

A word of warning, though. When dealing with a.c. signals such as pulse trains whose duty cycle can vary enormously, capacitive coupling can cause problems: as the duty cycle changes, so, too, does the average d.c. level of the waveform, such that the waveform at the inverting input tends to shift up and down. If this shift is excessive, the signal fails to cross one of the thresholds, and the circuit doesn't trigger. Always check that the circuit will respond properly at the extremes of the input signal's duty cycle.

NON-INVERTING SCHMITT TRIGGER

By swapping over the input voltage and reference voltage connections of the inverting Schmitt trigger (Fig.2.1), we obtain the non-inverting Schmitt trigger shown in Fig.2.6a.

The voltage $V+$ at the non-inverting input now depends not only on V_{OUT} , R1 and R2, but also on V_{IN} . We can understand the circuit's operation by referring to the voltage transfer characteristic in Fig.2.6b, which shows the case for $V_{REF} = 0$ and assumes V_{SAT+} is equal and opposite to V_{SAT-} .

Starting at the bottom left-hand corner, where V_{IN} is at its most negative value, the output is in negative saturation and so $V+$ is also a negative voltage. As V_{IN} increases (shown by the white arrows) it eventually reaches a positive level where $V+$ just rises above 0V, causing the comparator output to change state. The value of V_{IN} where the output rapidly changes from V_{SAT-} to V_{SAT+} is the upper threshold voltage, V_{TU} .

If V_{IN} is now reduced (shown by the black arrows), the output remains in positive saturation until V_{IN} has gone sufficiently negative to make $V+$ go just below 0V. At this point, where $V_{IN} = V_{TL}$, the output abruptly changes from positive to negative saturation, V_{SAT-} .

Notice how the hysteresis loop moves in an "anti-clockwise" direction, whereas that of the inverting Schmitt trigger (Fig.2.1b) follows a clockwise path.

By introducing the reference voltage, V_{REF} , we can shift the thresholds up or down: when V_{REF} is positive, the thresholds are moved in a positive direction, and vice-versa. The expressions for the thresholds (assuming $R_{SOURCE} = 0$) are:

Upper Threshold Voltage,

$$V_{TU} = \frac{V_{REF} \times (R1 + R2) - (R1 \times V_{SAT-})}{R2} \quad (\text{volts})$$

and:

Lower Threshold Voltage,

$$V_{TL} = \frac{V_{REF} \times (R1 + R2) - (R1 \times V_{SAT+})}{R2} \quad (\text{volts})$$

The "hysteresis" voltage, the difference between the thresholds, is:

Hysteresis voltage,

$$V_H = V_{TU} - V_{TL} = \frac{R1 \times (V_{SAT+} - V_{SAT-})}{R2} \quad (\text{volts})$$

Again, like the inverting Schmitt trigger, we see that V_H is completely independent of V_{REF} .

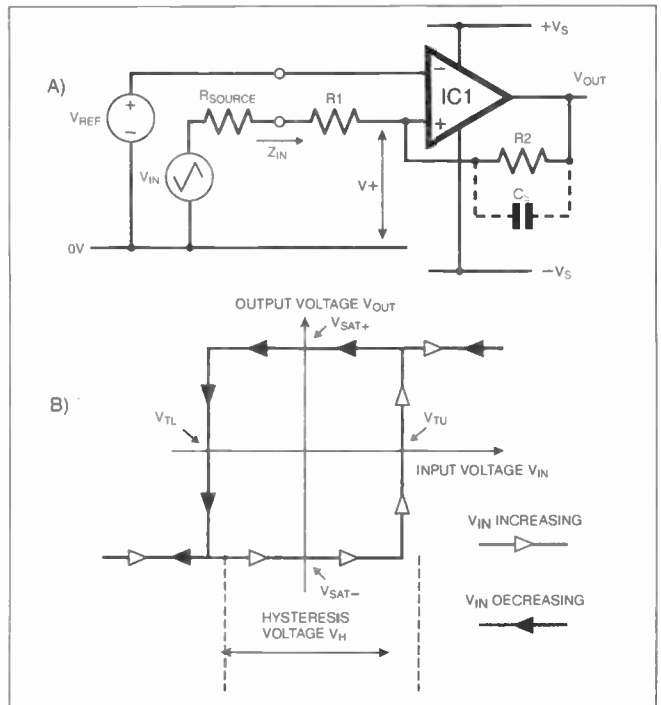


Fig.2.6. Circuit for a non-inverting Schmitt trigger (a) and its voltage transfer characteristic (b).

POSITIVE AND NEGATIVE RESISTANCE

We saw that the inverting Schmitt trigger's input impedance was dominated by the input bias current of the op.amp or comparator. For the non-inverting circuit, the impedance Z_{IN} seen by the voltage source depends largely on R1, R2 and V_{OUT} , and appears as either a positive or negative resistance.

For example, when V_{IN} is above V_{TU} , V_{OUT} is in positive saturation and current flows from IC1's output, through R2 and R1 and into V_{IN} . Thus, Z_{IN} appears as a *negative* resistance.

On the other hand, when V_{IN} is below V_{TL} , V_{OUT} is in negative saturation, and current flows from V_{IN} , through R1 and R2 and into IC1's output, such that Z_{IN} now behaves like a *positive* resistance.

If R_{SOURCE} , the output resistance of the voltage source, is very small or zero, the changing nature of Z_{IN} has negligible effect on circuit behaviour. However, if R_{SOURCE} is similar, or greater, in size to R1 and R2, the changing input current will cause a changing voltage drop across it, causing the apparent thresholds to shift relative to their nominal values.

In these circumstances, it is necessary to modify the threshold and hysteresis voltage equations by replacing R1 with $(R_{SOURCE} + R1)$, since R_{SOURCE} effectively appears in series with R1.

NON-INVERTING DESIGN PROCEDURE

The values for V_{SAT+} and V_{SAT-} can be obtained from the data sheet or determined from in-circuit measurements: the latter can often be more accurate, especially where saturation levels are heavily dependent on output loading.

For a desired hysteresis voltage, R1 and R2 can be selected by rearranging the expression for V_H :

$$R2 = R1 \times (V_{SAT+} - V_{SAT-}) / V_H$$

Then, knowing the desired value for V_{TU} , the appropriate reference voltage may be evaluated from:

Reference Voltage,

$$V_{REF} = \frac{V_{TU} \times (V_{SAT+} - V_{SAT-}) + (V_H \times V_{SAT-})}{V_H + V_{SAT+} - V_{SAT-}} \quad (\text{volts})$$

We'll follow a design example based on the LM6482, a dual, rail-to-rail input and output op.amp. Let's assume we require a hysteresis voltage of 1.0V and $V_{TU} = 1.5V$, and the circuit is to run on a single 5V supply.

With the output lightly loaded, it was found from in-circuit measurements that $V_{SAT+} = 5.00V$ and $V_{SAT-} = 20mV$. Using the above equations, we find that $R2 = 4.98 \times R1$, and $V_{REF} = 1.253V$.

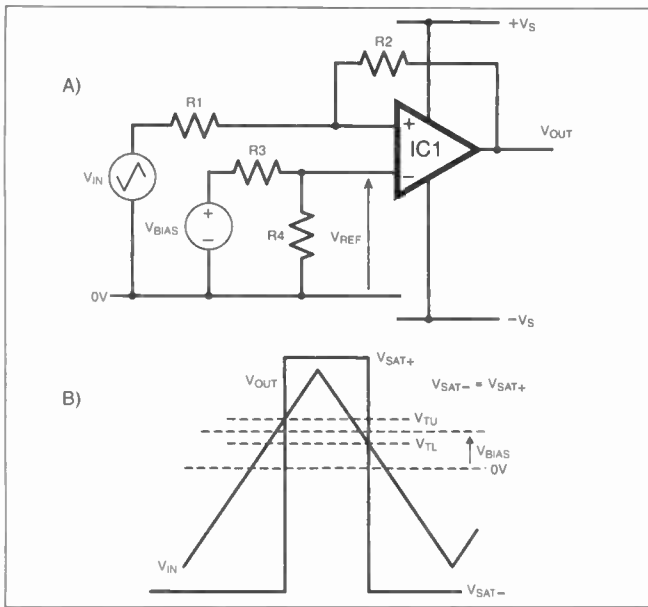


Fig.2.7. Using a bias voltage to control the mid-hysteresis level.

Using $R1 = 20k\Omega \pm 1\%$, $R2 = 100k\Omega \pm 1\%$, and with the supply voltage set to precisely 5.00V and V_{REF} set to 1.25V, measurements showed the upper threshold voltage as $V_{TU} = 1.53V$ and the hysteresis voltage as $V_H = 1.06V$.

Note that these results were obtained using a 200Hz triangular input waveform. It was found that performance was good up to around 2kHz: at higher frequencies, the op.amp's response time started to affect the thresholds in the manner described earlier.

For example, with V_{REF} increased to 2.5V, the nominal thresholds are $V_{TL} = 2.00V$ and $V_{TU} = 3.00V$. At 2kHz, the measured values were $V_{TL} = 1.87V$ and $V_{TU} = 3.09V$, whereas at 20kHz the apparent thresholds were $V_{TL} = 1.50V$ and $V_{TU} = 3.45V$. Clearly, if accurate performance were to be required at frequencies above 2kHz, it would be necessary to use a faster op.amp.

MID-HYSTERESIS LEVEL

We saw earlier that the hysteresis voltage $V_H = R1 \times (V_{SAT+} - V_{SAT-}) / R2$. If we can arrange for the output saturation levels to be equal and opposite, i.e., if $V_{SAT+} = -V_{SAT-}$, the expression can be written $V_H = (2 \times R1 \times V_{SAT+}) / R2$.

Now, the mid-point of the hysteresis band is simply the lower threshold plus half of the hysteresis voltage, or $V_{TL} + (V_H/2)$. So, for the case when $V_{SAT+} = -V_{SAT-}$ (and assuming $R_{SOURCE} = 0$), we find that:

$$\text{Mid-point of Hysteresis Voltage} = V_{TL} + (V_H/2) = \frac{V_{REF} \times (R1 + R2) - (R1 \times V_{SAT+})}{R2} + \frac{(R1 \times V_{SAT+})}{R2} \quad (\text{volts})$$

which simplifies nicely to:

$$V_{TL} + (V_H/2) = V_{REF} \times \frac{(R1 + R2)}{R2} \quad (\text{volts})$$

If we apply a d.c. bias voltage, V_{BIAS} , to the inverting input using the R3-R4 potential divider as shown in Fig.7a, we see that $V_{REF} = (V_{BIAS} \times R4) / (R3+R4)$, and so:

$$V_{TL} + (V_H/2) = V_{BIAS} \times \frac{R4}{(R3 + R4)} \times \frac{(R1 + R2)}{R2} \quad (\text{volts})$$

Therefore, if we make the ratio of $R2 / R1 = R4 / R3$, we get:

$$V_{TL} + (V_H/2) = V_{BIAS}$$

In other words, the mid-point of the hysteresis band will equal the bias voltage V_{BIAS} , as shown in Fig.2.7b for a positive value of V_{BIAS} .

This technique can be useful where the average level of the a.c. input signal changes unpredictably, a problem that can make it difficult or impossible to set appropriate thresholds using the simple Schmitt trigger of Fig.2.6.

By using the circuit of Fig.2.7a, and by arranging for V_{BIAS} to track the average level of the input signal, the thresholds will shift automatically such that the hysteresis band will always be centred

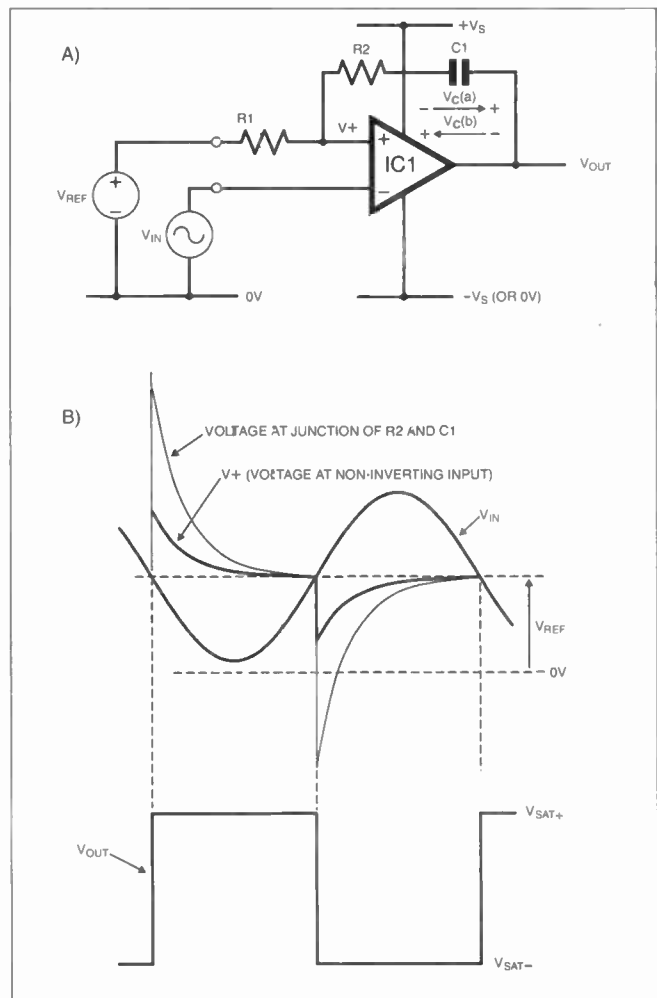


Fig.2.8. Using capacitive feedback provides temporal hysteresis.

on the a.c. signal. Remember, however, that this technique can only be used when the output saturation levels are equal and opposite.

SINGLE THRESHOLD VOLTAGE

For applications that demand only a single threshold voltage and yet still require noise rejection, we must find a way of introducing hysteresis without having two separate thresholds. This apparent paradox is achieved using "temporal" hysteresis, usually implemented with capacitive positive feedback as shown in the inverting Schmitt trigger of Fig.2.8a. The circuit works as follows:

Assume that V_{IN} is lower than $V+$, the voltage at the non-inverting terminal, such that V_{OUT} is in positive saturation. Capacitor C1 charges via R1 and R2 until its voltage, $V_C(a)$ equals $(V_{SAT+} - V_{REF})$.

When C1 is fully charged, no current flows through R1 and so $V+ = V_{REF}$. If V_{IN} now rises above $V+$, V_{OUT} abruptly changes state from V_{SAT+} to V_{SAT-} , causing the voltage at the R2-C1 junction to go to $V_{SAT-} - V_C(a) = V_{SAT-} - V_{SAT+} + V_{REF}$.

Thus, $V+$ is suddenly pulled down to a voltage lower than V_{REF} , resulting in the regenerative action needed for proper Schmitt trigger operation. (The actual voltage that $V+$ goes to depends on the ratio of R1 and R2).

However, $V+$ does not stay at this low level because C1 starts to charge via R1 and R2 until its voltage, $V_C(b)$ equals $(V_{REF} - V_{SAT-})$. Once C1 is fully charged, $V+$ again settles back to equal V_{REF} .

If V_{IN} now falls below $V+$, V_{OUT} snaps into positive saturation, and $V+$ is rapidly pulled to a voltage greater than V_{REF} . Once again, positive feedback causes the required regenerative action. C1 now charges until its voltage, $V_C(a)$ equals $(V_{SAT+} - V_{REF})$, at which point $V+$ again falls back to equal V_{REF} .

The waveforms in Fig.2.8b are those typically occurring in response to a sinusoidal input voltage, where $V_{REF} = V_{SAT+} / 2$ and $V_{SAT-} = 0$ (i.e., a single rail application).

Notice that when V_{IN} crosses the V_{REF} threshold, $V+$ jumps above or below V_{REF} and then decays back to a level equal to V_{REF} . Provided the $(R1 + R2) \times C1$ time constant is less than one-tenth the period of V_{IN} , $V+$ will always return to V_{REF} before V_{IN} next crosses the V_{REF} threshold.

Knowing the maximum input signal frequency, the appropriate $(R1 + R2) \times C1$ time constant may be determined. Then, having chosen $C1$, the ratio of $R1$ and $R2$ must be selected to maximise the voltage swing at the non-inverting input (thereby maximising the circuit's noise rejection properties) whilst ensuring that $V+$ remains within the common-mode input limits for the op.amp or comparator used.

Temporal hysteresis can be demonstrated using the circuit of Fig.2.9, a single rail circuit which again uses one half of an LMC6482 op.amp (although other op.amps or comparators with rail-to-rail input and output capability could be used).

Making $R1a$ and $R1b$ both equal to $100k\Omega$ sets $V_{REF} = 2.5V$ and provides an effective (Thévenin) value of $R1 = 50k\Omega$. With $R2 = 100k\Omega$ and $C1 = 6.8nF$, the feedback network's time constant is $1ms$, allowing the circuit to accommodate input signal frequencies as high as $100Hz$.

The maximum voltage swing at the non-inverting input is $V_{REF} \pm 1.7V$, i.e., $0.8V$ to $4.2V$, well within the op.amp's common-mode input limits.

The circuit's response to a noisy input signal is illustrated in Fig.2.10. The top trace shows the input signal, a sine wave containing over 30 per cent of "triangular" noise.

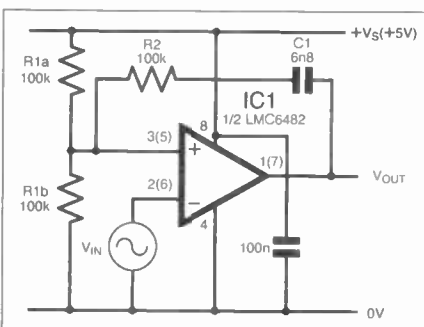


Fig.2.9. Single rail Schmitt trigger with temporal hysteresis.

The middle trace is the output of the circuit in Fig.2.9. Notice how there is only one transition each time the sinusoid crosses the $2.5V$ reference threshold.

The bottom trace shows the circuit's output with $R2$ and $C1$ removed (i.e., no positive feedback at all). The circuit now

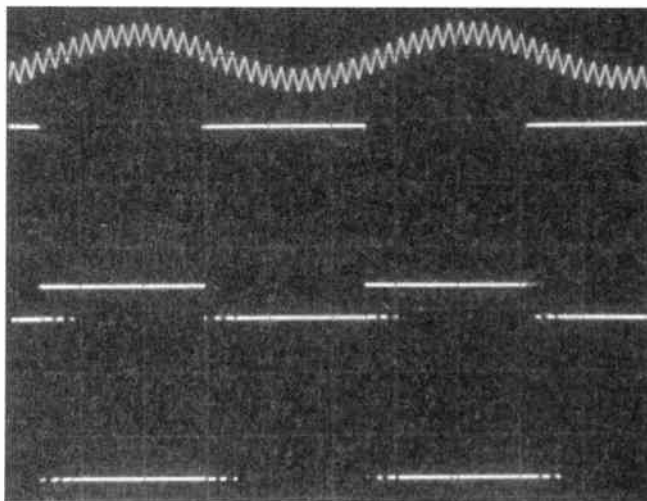


Fig.2.10. Waveforms showing that temporal hysteresis provides noise rejection. Top trace: V_{IN} ($5V/div.$). Middle trace: Output waveform of circuit with temporal hysteresis ($2V/div.$). Bottom trace: Output waveform with no positive feedback ($2V/div.$). Timebase: $2ms/div.$

behaves as a simple comparator, such that its input is triggered each time the noise crosses the $2.5V$ reference: the multiple transitions caused by the noise can clearly be seen on the output wave-form.

PRECISION AND VERSATILITY

In Part Three of this series, we'll examine methods for improving the Schmitt trigger's precision and flexibility. We'll also see how this versatile circuit element is used as the basis for other circuit functions, such as oscillators and pulse generators.

SHOP TALK

with David Barrington

PIC-Monitored Dual PSU

We have a minor problem concerning the metal case for the *PIC-Monitored PSU*. The original one used in the author's model is no longer stocked. However, although the dimensions are not exactly the same, our investigations have thrown up two possibilities and they are both RS types.

The first one measures $305mm \times 178mm \times 177mm$, is coded 223-972 and listed at $\pounds 19.55$. The other one is coded 671-242, measures $254mm \times 197mm \times 159mm$ and is listed at $\pounds 40.21$. Readers should be able to order these through any *bona-fide* RS stockists in their area. Alternatively, they can be ordered through **Electromail** (☎ **01536 304555** or <http://rswww.com>), their mail order outlet. No doubt, readers will have their own ideas regarding the case.

The $50VA$ mains transformer, code 805-142, and the L272 dual power op.amp, code 635-167, also came from the above source.

Regarding the monitor section. The alphanumeric 2-line 16-character per line liquid display module used in the prototype has an integral cable and connector. It was purchased from **Magenta Electronics** (☎ **01283 565435** or www.magenta2000.co.uk). Other advertisers will no doubt be able to offer something similar, without cable.

For those readers unable to program their own PICs, a ready-programmed PIC16F877-4P can be purchased from Magenta (see above) for the inclusive price of $\pounds 10$ (overseas readers add $\pounds 1$ for postage). Software for the *PIC-Monitored Dual PSU* is available on a 3.5-in. PC-compatible disk from the *EPE* Editorial Office – see *PCB Service* page 946. It is also available free via the *EPE* web site: <ftp://epemag.wimborne.co.uk/pubs/PICS/PICmonpsu>.

The two printed circuit boards are available from the *EPE PCB Service*, codes 280 (Power Supply, of which two are needed for the full PSU) and 281 (Monitor).

Static Field Detector

This month's Starter Project is a low-cost *Static Field Detector* and we do not expect any "sticky" component problems.

The specified TL061CP op.amp has an output stage that will drive both i.e.d.s from fully switched off to fully on, whereas many other op.amps will fail to do this. Therefore, the use of alternative devices is not really recommended. The TL061CP should be readily available from component advertisers.

The printed circuit board for the "Detector" is the Multi-project board available from our *PCB Service*, code 932.

Motorists' Buzz-Box

Prices for panel meters tend to vary quite considerably and it may pay you to shop around when collecting together parts for the *Motorists' Buzz-Box* project.

The LM334N adjustable current source chip came from Maplin (☎ **0870 264 6000** or www.maplin.co.uk), code WQ32K. They also have a "large" $50\mu A$ panel

meter, code RX54J, but you will need a larger plastic box for this one. The printed circuit board is available from the *EPE PCB Service*, code 278.

Festive Fader

The $3VA$ mains transformer, with twin primary and secondary windings, and the MOC3020 opto-isolated triac for the *Festive Fader* were purchased from **Farnell** (☎ **0113 263 6311** or www.farnell.com), codes 159-438 and 280-320. They also supplied the $1\mu F$ multilayer ceramic capacitor, but you will probably have to buy in multiples of 5. It is also listed by **Electromail** (☎ **01536 304555** or <http://rswww.com>), code 264-4977 (packs of 5).

The printed circuit board is available from the *EPE PCB Service*, code 277 (see page 946). The Euro mains connector, with fuseholder, should be widely stocked.

PICtogram

All of the components called up for the *PICtogram* project appear to be "off-the-shelf" items except, of course, a ready-programmed PIC16F84 microcontroller. The $2mA$ i.e.d.s certainly seem to be in abundant supply, in various colours.

For those readers unable to program their own PICs, the author is able to supply ready-programmed PIC16F84 microcontrollers for the sum of $\pounds 6$ each, inclusive of postage (overseas add $\pounds 1$ per order). Orders should be sent to: **Andy Flind, 22 Holway Hill, Taunton, Somerset, TA1 2HB**. Payments should be made out to *A. Flind*. For those who wish to program their own PICs, the software is available from the Editorial offices on a 3.5-in. PC-compatible disk, see *PCB Service* page 946. It is also available free via the *EPE* web site: <ftp://epemag.wimborne.co.uk/pubs/PICS/PICtogram>.

Finally, the printed circuit board is available from the *EPE PCB Service*, code 271 (see page 946).

Christmas Bubble and Twinkling Star

Regarding the *Christmas Bubble* and *Twinkling Star* projects, both sets of components appear to be "run of the mill" items and should not cause any sourcing problems.

Jumbo i.e.d.s ($10mm$) should cost you just under $\pounds 1$ on average. You may also be able to buy the standard i.e.d.s at quantity discounts from some advertisers, it's worth trying.

If you must run these two projects using mains adaptors, most of our components advertisers seem to stock good quality, multi-voltage types. The small printed circuit board for the *Twinkling Star* is available from the *EPE PCB Service*, code 276 (see page 946).

PLEASE TAKE NOTE

Anti-Tamper Alarm (*Ingenuity Unlimited*)

Page 766. The i.c.s should be types 4001 and not as shown on the circuit. Also, note capacitor $C1$ should be $10n$ (nano) and not as shown. Oct '00

Versatile Mic/Audio Preamp

It would appear that supplies of the SSM2166P mic. preamp chip have now "dried-up". If any readers know of a source please let us know so that we can pass it on. May '00

CONTROL & ROBOTICS

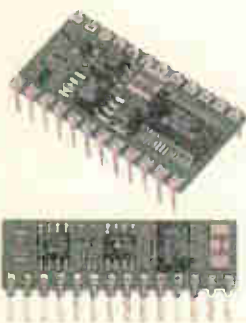
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New range of robotic arms for educational and hobbyist use with super powerful servos. Controlled from PC (Windows freeware provided) or from optional keypad. Stands about 450mm high when fully extended. Kit includes all pre-cut body parts, servo controller board, servos and software. Requires 9v Dc. Kits start of **£189**



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Stamp2 based controller with voice record-playback capability, PIR input and/or random playback. 4-servo actions are recorded/edited one track at a time. May also be controlled from PC. **Head kits start at £29. Controllers from £29**

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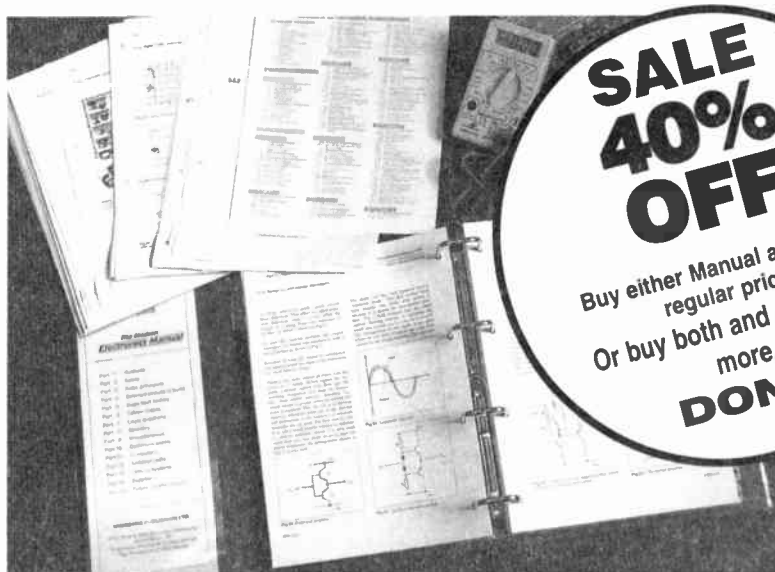
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New Technology Update

Inkjet and optical technologies combine to provide greater comms bandwidth. Ian Poole reports.

THE telecommunications industry is one of the major growth areas in today's business arena. Increasing amounts of information are required and they are needed faster than ever before. Much of this has been fuelled by the phenomenal growth of the Internet, with applications like e-commerce and the transmission of audio and video providing ever-increasing levels of traffic.

Such is the growth that it has been predicted that the capacity required will have risen by a factor of thirty-six in the eight years from 1995.

Optical Data Rates

Many of the transmission paths use optical technologies. New techniques like Dense Wavelength Division Multiplexing (DWDM) are being used more widely. In this, a single fibre is used to carry several channels, each having a different wavelength.

Optical fibre data rates are also increasing, with transmission speeds set to quadruple in the next two years. This will enable network builders to move from the existing backbones running at 10 Gigabits per second to 40 Gigabits.

To ensure that the required speeds can be met, many organisations are moving to all-optical networks. This alleviates a number of the problems found in mixed technology systems. It also gives additional levels of flexibility, for example allowing operators to lease a wavelength, whereby the entire wavelength channel is leased out to a user.

This gives the potential of desk-top to desk-top optical communications, which can be very attractive to the system provider as there could be many thousands of optical channels available within a single fibre.

Switched Solution

To achieve these goals, optical devices need to be developed further. At the moment many are very expensive, but there are a number of developments that are under way that are likely to resolve many of the problems being encountered.

One of these areas is in optical switching, where Agilent (formerly the non-computer related areas of Hewlett Packard) have developed an optical switch. This uses a combination of inkjet activators and optical planar waveguides to give a simple and scalable optical switch with no moving parts.

Agilent's new switch is the N3565A, which provides a 32 x 32 photonic switching platform. It innovatively uses inkjet printer technology, combined with planar

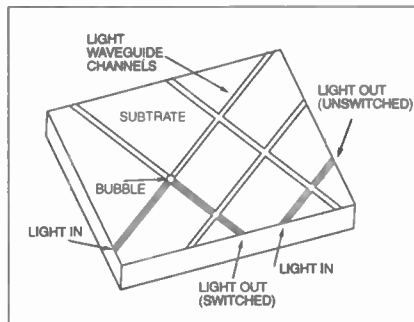


Fig.1. The light waveguide switching platform.

lightwave circuit technologies.

The switch consists of intersecting silica waveguides as shown in Fig.1. At each intersection a trench is etched into the waveguide. This is filled with a fluid that has a refractive index matching that of the light path, and accordingly it allows unimpeded transmission of the light across the intersection.

When a command to switch is issued, a bubble is created at the intersection and this causes the light to be reflected down the intersecting light path by total internal reflection (Fig.2). It is this bubble that is formed using inkjet printer technology.

Switching Technique

Switching is performed using the piezo-electric actuators that are based on those found in inkjet printers. These are solid state devices that are comprised of a pump chamber, inlet mechanism and a bubble nozzle. When a voltage is applied to the piezo-electric actuator, it contracts and

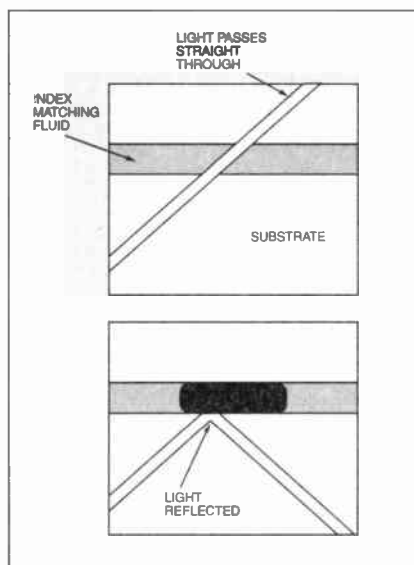


Fig.2. Principle of switching.

then relaxes when the voltage is removed. This action increases and then reduces the pump chamber volume, drawing in liquid and ejecting droplets under pulsed control.

Using this system, switching can occur in less than ten milliseconds, and this is sufficiently fast for the systems on which it is anticipated the switch will be used.

The absence of moving parts is the key to the reliability of the system. The inkjet elements have been switched many millions of times in tests and have been shown to be exceedingly reliable. Additionally, the fluid that is used is non-corrosive and stable, which are key elements in the reliability of the whole system.

System Aspects

The basic principle can be used to create very large switching matrices that enable a considerable amount of flexibility to be introduced into optical data systems. Whilst there is about 5dB of loss from one fibre, through the switch, into the output fibre, for what is termed a wavelength selective cross-connect, this is quite acceptable, especially when it is compared to other technologies.

Crosstalk is surprisingly low at -50dB, demonstrating the very high level of isolation that is achieved. This is particularly important where large numbers of optical paths are switched, because if the levels were higher then it would also lead to high levels of interfering noise that would result in data errors.

Future

This development is likely to achieve widespread use. It is flexible, cheap and effective. It shows that optical technology for data transmission can now be used even in small installations.

The development is also indicative of the growing use of optical technology. It has several advantages, even for the small user. Not only are much higher data rates possible than for an electrical wire system, but it also has greater immunity to electrical noise. For those interested in security, the optical fibres do not radiate the signals in the same way that wired systems do, thereby making eavesdropping much more difficult.

In view of all these advantages, many commentators anticipate that optical technology will grow considerably in importance in the coming years.

Further information about these optical switches can be found on the Agilent website at www.agilent.com.

Information about radio and electronics in general can be found at www.radio-

*All in all, it can be
thoroughly recommended.*

Everyday Practical Electronics - 10/2000

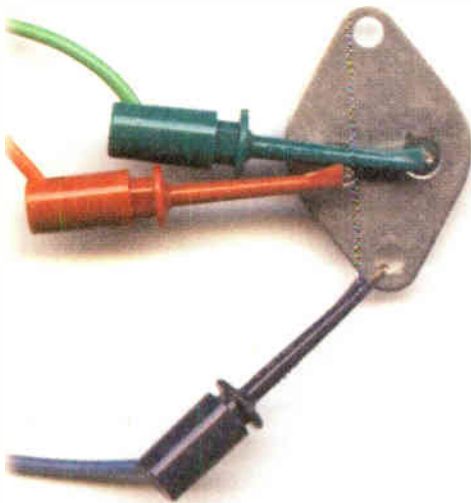
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INTERFACE

Robert Penfold



EXTENDED TEMPERATURE PC INTERFACE SOFTWARE

As pointed out in the past, this series is primarily concerned with the hardware side of add-on projects for PCs. However, without software the projects are of no use, and the software topics have to be considered from time to time.

Interestingly, it is the software that tends to bring the most feedback from readers. Over the last year software matters seem to have generated three or four times as many letters and E-mails as hardware related topics.

Some of the letters contain suggestions for better ways of doing things. Thanks to those who have made suggestions, some of which have been incorporated into the software featured in recent months.

Others are interested in using improved software to extend the capabilities of the projects featured in this series. Arrays and data logging is a topic that turns up from time to time, and is one that has not been covered significantly in this series.

Arrays

Usually the software has to do nothing more than take a reading from a port, do some simple arithmetic on the returned value, and then display the value on the screen. This is achieved by storing the reading in a variable, doing any necessary mathematics with the result being stored back in the variable, and then writing the contents of the variable to the screen via a label or text box.

Data logging is more complex in that it requires what could be hundreds or even thousands of readings to be taken and stored in the computer's memory. The results can then be read via a text box, printed out, or presented on the screen in some graphic form.

Arrays are used to store blocks of data, and each element of an array is just a special form of variable. The exact way in which arrays are handled varies slightly from one programming language to another, but here we will consider the Visual BASIC 6 version, which is fairly typical.

Each element in an array has the same name, but a number in parentheses (brackets) follows this name. This number gives each element in the array a unique identity. In the normal scheme of things the numbering starts at 0 and goes up to the number that is specified when the array is dimensioned. In Visual BASIC you can declare variables or simply make them up as you go along.

This same flexibility is not available when using arrays, and they must be declared and dimensioned before they are used. By telling the programming

language the type of variable used in the array and the number of elements, it is then able to reserve a suitably sized block of memory to store the data.

Going Public

When declaring variables and arrays in Visual BASIC it has to be borne in mind that there are public and private variables. If the declaration is made within a subroutine, the variable can only be used within that routine.

This can be very useful, but with interfacing software it is often the public version that is of more use. By declaring a variable outside a subroutine it becomes a public type that can be accessed by any part of the program. The following line, for example, would declare an array containing 100 elements, with each element an integer:

```
Dim Reading(99) As Integer
```

Note that there are 100 and not 99 cells in this array, because the numbering starts at 0 and not 1. In order to read a set of data into an array a loop is used, together with a variable that acts as a counter. For example, the followings routine would read the printer port data lines at address 888 one hundred times, placing the readings in the elements of the array called Reading:

```
Dim Reading(99) As Integer
```

```
Out 890,32
```

```
For Counter = 0 To 99
```

```
Reading(Counter) = Inp(888)
```

```
Next Counter
```

The first line dimensions the array, and the second one sets the printer port data

Listing 1: Extended Temperature Interface Program

```
Dim Port1 As Integer
Dim Port2 As Integer
Dim Port3 As Integer
Dim Reading As Integer
Dim Counter As Integer
Dim Readings(99) As Integer

Private Sub Command1_Click()
Port1 = 632
Port2 = 633
Port3 = 634
Timer1.Enabled = True
End Sub

Private Sub Command2_Click()
Port1 = 888
Port2 = 889
Port3 = 890
Timer1.Enabled = True
End Sub

Private Sub Command3_Click()
Label1.Caption = Readings(Text1.Text)
End Sub

Private Sub Form_Load()
Counter = 0
End Sub

Private Sub Timer1_Timer()
Out Port3, 1
Out Port3, 3
Out Port3, 2
For D = 1 To 2000
Next D
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = 128
Else Reading = 0
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 64
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 32
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 16
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 8
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 4
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 2
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 1
Out Port3, 3
Out Port3, 2
Dta = Inp(Port2) And 8
If Dta = 8 Then Reading = Reading + 1
Out Port3, 3
Label1.Caption = Reading / 2
Readings(Counter) = Reading / 2
Counter = Counter + 1
If Counter = 100 Then Label1.Caption = "STOPPED"
If Counter = 100 Then Timer1.Enabled = False
End Sub
```


lines as inputs. The port must obviously be a bidirectional type for this to work. Note that Visual BASIC does not have built-in Inp and Out commands, and that these must be added using Inpout32.dll, as described in previous *Interface* articles. The rest of the routine is a For...Next loop that executes 100 times, incrementing the variable called Counter from 0 to 99 in the process. Counter is used as the element number in the program line that reads the printer port and the result into the array.

Therefore, on the first loop the returned value is read into Reading(0), on the next it is placed into Reading(1), and so on until the value read from the port is placed in Reading(99) on the one hundredth loop.

Perfect Timing

In practical applications the readings will usually have to be taken at regular intervals, and it may be necessary to have a substantial gap from one reading to the next. This could be achieved by adding a delay routine in the For...Next loop, but

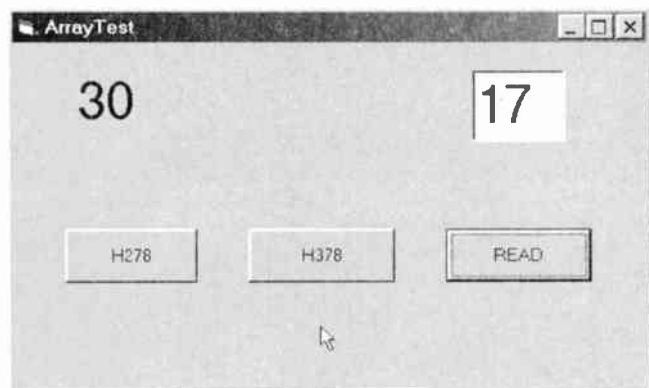


Fig.1. Screen shot showing display text box window and READ "button".

with Visual BASIC the obvious way of handling things is to assign the routine that reads the port to a timer component.

Readings are then taken at whatever interval is used for the timer. The method used to obtain readings might be more complex than simply reading a port, but the basic method outlined here can still be applied.

Program Listing 1 is an extension of the thermometer program featured in the previous *Interface* article. It takes 100 temperature readings at one-second intervals and places them in an array. See the October 2000 issue for details of the Temperature Interface.

As in the original program, operating either the button marked H278 or the one captioned H378 selects the required base address and starts the timer. The routine that reads the analogue-to-digital converter is relatively long because the data is read one bit at a time and then reconstituted into an 8-bit value. However, once the final value has been obtained it is displayed on Label1 and placed in the array.

Numbers Count

A variable called Counter is used to provide the element number, and this

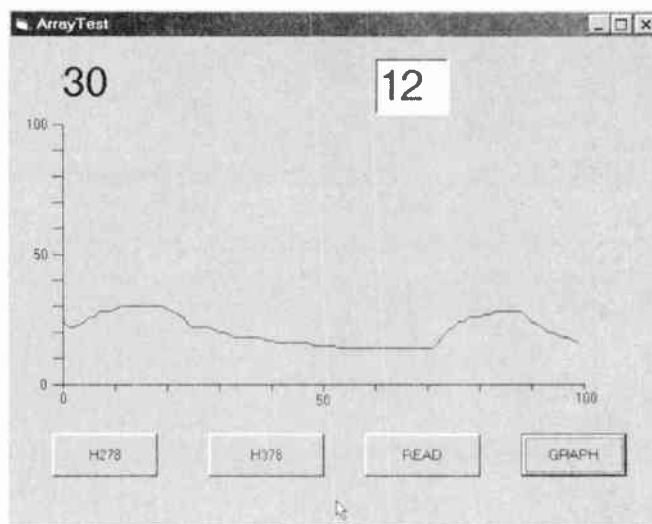


Fig.2. Extended program in action. Some simple calibration marks have been added to the graph to make it easier to interpret results.

variable is incremented by one each time the routine is performed. Eventually the value of Counter reaches 100, and the last line in the routine then switches off the timer so that no further readings are taken and stored. The penultimate line prints STOPPED on Label1 so that you know that things have come to a halt.

Once the data has been safely stored in an array the PC can manipulate it in a variety of ways. This program simply has a third button and a text box that enable individual samples to be displayed on the screen. Just type a number from 0 to 99 into the text box and then press the READ button. The relevant reading will then be displayed on Label1, as in the screen dump

that is shown in Fig.1.

There are plenty of other possibilities. The PC can be used to find and display the maximum and minimum readings, calculate and display various types of mean reading, and so on.

Graphics

A modern PC is also well equipped to display various types of graph and chart. The following routine can be applied to a fourth command button, and it draws a simple graph on the screen once a set of readings have been taken. The form must be large enough to accommodate the graph, and the middle section that the graph occupies must be left free of other components.

```
Private Sub Command4_Click()
Counter = 0
T1 = 600
T2 = 660
For Loops = 0 To 98
Lft = Readings(Counter)
Counter2 = Counter + 1
Rght = Readings(Counter2)
Lft = Lft * 30
```

```
Rght = Rght * 30
Lft = Lft + 1000
Rght = Rght + 1000
Lft = 5000 - Lft
Rght = 5000 - Rght
Line (T1, Lft) - (T2, Rght)
Counter = Counter + 1
T1 = T1 + 60
T2 = T2 + 60
Next Loops
End Sub
```

An enlarged version of the program in action is depicted in the screen dump of Fig.2. Some simple calibration marks have been added to make it easier to interpret results.

The routine starts by setting three variables at their initial values. T1 and T2 are variables used to provide the X1 and X2 co-ordinates for each section of the graph. Counter is used to select

the required element of the array, and is initially set at 0. The routine then goes into a For...Next loop that actually draws the graph.

The first and second readings are used to provide the Y1 and Y2 co-ordinates for the first section of the graph. Both require some mathematical manipulation in order to match up with the Visual BASIC co-ordinate system. Incidentally, the graphics area extends from 600,4000 at the bottom left corner to 6600,10000 at the top right hand corner.

A Line command is then used to actually draw the line, and this operates in much the same way as the QBASIC Line command. Counter is then incremented by 1, and T1 plus T2 are incremented by 60 (one second's worth of co-ordinates). The loop causes this process to be repeated a further 98 times until all 99 sections of the graph have been completed.

The routines provided here are quite basic, and do not contain any error trapping for example. However, they do demonstrate that reading data into an array is very straightforward. Processing the captured data and displaying it on the screen in various ways is then just a matter of using conventional programming techniques.

On Disk

Should you wish to experiment with them, the source files for the graph program are available on the EPE web site, as is the compiled version of the program. It is also available on the EPE *Interface* Disk 1, see EPE PCB Service page elsewhere in this issue for details.



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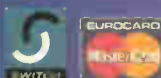
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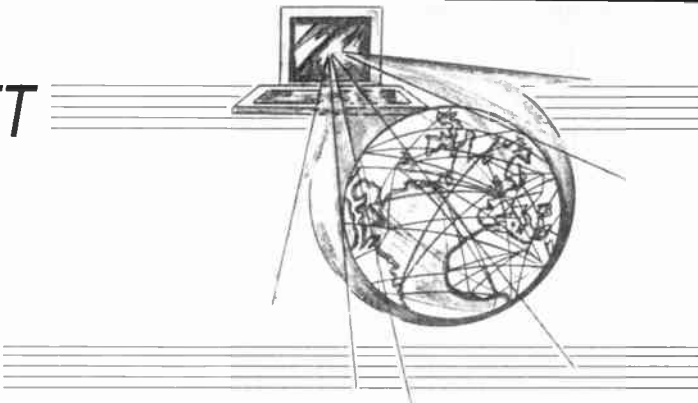
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SURFING THE INTERNET

NET WORK

ALAN WINSTANLEY



ONE of the downsides of writing a column dated several months ahead of reality is that it doesn't half make time fly. Here I am in early October already writing for the December 2000 issue, with the year 2001 arriving "next month"! This year started on an optimistic note, with promises of unmetered Internet access and broadband services wetting our appetites. Spring 2000 saw the attempt by Alta Vista UK to pre-empt the market by promising an unmetered package. Other unmetered tariffs have come and gone since then, all of them proving a financial drain for their operators, or in the case of Ezesurf, ruining them altogether.

Freeserve (www.freeserve.net) was amongst the very first to offer BT Surftime unmetered tariffs in Summer 2000, but has gone underground with its advertising, apparently having been heavily subscribed, even more so when other users, finding themselves turfed off other failed unmetered packages, migrated to Freeserve instead. Users were soon complaining of slow connections, engaged tones and poor bandwidth as the networks creaked under the strain.

On the Hog

Predictably, a minority of Freeserve incumbents decided to hog some lines to themselves, therefore spoiling the show for everyone else. More than 750 customers have since been served with a month's written notice because of their disproportionate drain – said to be up to 10% – on Freeserve's bandwidth.

The UK Consumer's Association said in October: "ISPs offering an unmetered service have seemed more interested in increasing their customer numbers than in delivering the services that consumers were originally promised when they signed up."

"Recent press reports suggesting that Freeserve would withdraw its unmetered access service from heavy users seems like another example of an unmetered offer that can't live up to the hype. If Freeserve has found itself caught out by the heavy usage of its customers it should accept its share of the blame."

"The ISPs have got themselves into a mess" says the CA. "Rather than luring consumers in and then kicking them off schemes, what is needed on their part is better planning, realistic projections of customer usage and clearer advertising for new schemes."

However, no ISP can reasonably cater for, say, a quarter of a million users all suddenly commandeering a cheap leased line 24x7, because the capacity just isn't there and probably never will be. Expectations are still running unrealistically high on both sides: consumers demand "excessive" levels of bandwidth on the cheap, and ISPs hope their customers will show restraint when using it.

The times are a'changing

I decided the time was ripe to review my Internet provision, if only to see what could be done to bring the cost down from its interstellar trajectory. I soon saw that confusion marketing reigned supreme. I started by checking my regular ISP, Demon Internet, who pioneered the flat-rate "TAM" (tenner-a-month) account in the early 1990's. Recently Demon felt sufficiently moved to incorporate a BT Surftime package, the idea being that the BT portion of the cost would be charged directly to your phone bill to provide for unmetered tariffs.

Under Surftime the standard TAM account will then benefit from reduced call costs – 2p/minute daytimes, 0-6p evenings, 0-5p weekends. For its proposed evenings and weekends package, Demon offers the standard Surftime £5.99 monthly rate paid direct to BT. This provides 100 per cent discount on all evening and weekend Internet calls, remembering Demon's £11.75 monthly subscription is extra.

Demon also proposes an enhanced package called *Premier Connect Plus* which costs nearly twice the standard account rate.

For a monthly sum of £19.99, the Surftime numbers can be used throughout the working day as well as during evenings and weekends. Under this package, call costs drop to 1p/min. weekday daytimes, 0-6p evenings and 0-5p weekends.

Demon continues: "Again, if you make a fixed extra payment to BT (a further £19.99 per month) you can get a 100 per cent discount on some or all of your Surftime calls so that they become 'free'... or you can pay £5.99 per month to cover evening and weekend calls only." Demon puts a price of £119.94 (\$167) per quarter on 100 per cent unmetered access. This service was due to roll out on 9 October.

This typifies the sort of stuff most users have to grapple with when comparing the best deals. Your cable operator may have packages comparable with any BT service (*ntl* hasn't replied to my query about cable modems). Personally, I sought a credible Surftime ISP offering a reasonable compromise to help slash daytime access costs, and maybe provide free calls in the evening and weekends.

As mentioned in previous columns, the choice of Surftime-enabled ISPs listed on BT's web site is meagre. A glitch with my Demon dial-in access – Demon changed their access software which rendered my modem obsolete – finally caused me to start shopping around.

Enter an ISP which bowed out from offering unmetered access earlier this year: LineOne (www.lineone.net). Their new Surftime tariff is simple and to the point: for a fee of £9.99 added to your phone bill – £5.99 is BT's Surftime evening and weekend portion, £4 is LineOne's ISP subscription – I could enjoy 1p a minute during the business daytime and completely free access during the evenings and weekends.

LineOne's on-line sign-up was soon completed and three days later an E-mail confirmed that my BT account had been updated for LineOne Surftime. This will cost £29.97 per quarter including VAT. Quickly dialling in via the new 0844 number, I was soon in business at 1p/minute or completely free altogether, and have high hopes of dramatically cutting costs. Note that LineOne telephone support costs 50p/minute, whilst Demon's is free, but this won't worry proficient users.

On your bike, ET

I was feeling quite pleased at this point. However, there's just enough room left this month to describe a perverse coincidence which rained on my parade. On the very same day I started to celebrate new lower prices, I spied a BT engineer shinning up the telegraph pole outside. In giving a neighbour a second line, the engineer did something to my own Internet access line which has resulted in my line speed being crippled to 33-6Kbps maximum, and it now takes several noisy attempts of my new modem to access any ISP at all, and connection speeds are suddenly 40 per cent slower.

This has all the makings of having a phone line "DACSeD" (Digital Access Carrier Service), multiplexing two signals down a copper wire where no new circuits are available, to channel two phone lines down one wire. It's a common BT trick.

A maximum line speed of 33-6K every time is a dead give-away that something is wrong, but my problem makes no sense as two separate properties are involved. I have already had the "we don't guarantee any modem speeds down a voice line" argument with several unsympathetic BT reps.

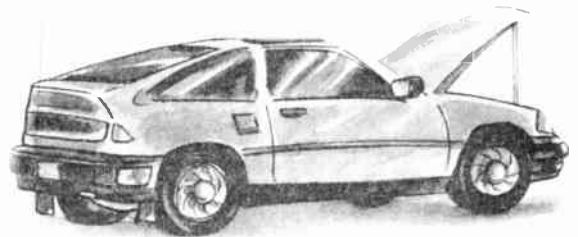
I am therefore, at a single stroke, back to the sort of line speed I endured half a decade ago. British Telecom uses E.T. the Extra Terrestrial as their TV advertising mascot and I can tell you that at the time of writing, I am more incandescent than E.T.'s finger-end.

You can E-mail me at alan@epemag.demon.co.uk. See you next month.

Constructional Project

MOTORISTS'

BUZZ-BOX



TERRY de VAUX-BALBIRNIE

A multi-purpose test instrument for the intrepid car owner.

It also provides a "crank test". This gives a battery "goodness" check by measuring the voltage under the heavy load over the starter motor.

THIS easy-build Buzz-Box is a test instrument having six useful functions. It would be ideal for anyone involved with fitting car accessories and for checking bulbs, fuses, switches, ignition leads and "earth" points. Since the unit receives power from the car electrical system, it does not need any internal batteries so will always be ready to use.

One particular advantage of this circuit is that most of the tests are provided by audible signals. This means that the user can concentrate on the task in hand without having to look at a display!

NEGATIVE ONLY

The Buzz-Box is suitable only for vehicles having a 12V *negative earth* system. That is, the negative terminal of the car battery is connected directly to the vehicle's metal structure ("earth" or "ground"). It is usual for the car body to provide the return path for the various circuits and this saves a lot of wiring.

Practically all cars in use today use the negative earth system although certain old models are "positive earth" (where the positive terminal of the battery is connected to the chassis). It is a simple matter to check this point if in doubt. **Damage will be caused to the unit if it is connected with incorrect polarity.**

OVERVIEW

The instrument is built in a small plastic box. On top there is a meter, a rotary control with scale, a pair of terminals, pair of sockets and two metal contact "rails" (see photograph). On the side, there is a further socket which accepts a test meter type probe. A long piece of twin wire is used to connect the unit to the car cigar lighter socket for powering it.

The Buzz-Box provides the following functions:

1. Earth Test. When the probe is applied to some point which has a small resistance with respect to the car chassis, an internal buzzer will emit a short bleep. This will be found useful for finding a good "earth" point when wiring an accessory or for checking the quality of an existing connection. Rust at a securing screw is a common problem and will result in increased resistance.



2. 12V Test. When the probe is touched on to some point which is within approximately 300mV of supply voltage (nominally 11.7V), the buzzer will emit a long bleep.

3. Low Resistance Test (20Ω). When the terminals of a low-resistance component bridge the test rails, the buzzer will sound continuously providing its resistance lies between zero and 20 ohms approximately. Several items associated with the car electrical system have near-zero resistance. Examples include fuses, pieces of wire and "closed" switch contacts.

However, the "cold" resistance of a low-power bulb may exceed ten ohms. A facility for giving a bleep with a resistance less than 20 ohms or thereabouts is therefore useful. This may be used as a quick "continuity" check on any low-resistance item.

4. Ignition Lead Test (Hi-R). The lead is connected to the Hi-R (high resistance) test position. The knob on the rotary control is turned until the buzzer just sounds and the resistance read off on a scale from ten kilohms (10kΩ) to 50 kilohms (50kΩ).

5. Battery Voltmeter. While the unit is connected to the car system, a narrow-scale analogue meter gives a read-out of the battery voltage from 10V to 14V. This may be used to check the charge state of the battery.

6. Loudspeaker Test. When loudspeaker leads are connected to the terminals, the loudspeaker will emit an audible tone. This is useful when it is not known which set of loudspeaker leads is which. It will also identify faulty units and connections. *Note that this test does not determine how well the loudspeaker is working.*

In order to set up the voltmeter section at the end of construction, you will need brief access to a good-quality test meter.

Since the circuit receives current from the car system, the 0V line will be automatically connected to the car chassis through the low resistance of the feed wire. The positive line will be at whatever voltage exists across the car battery terminals. This will be approximately 12V but will vary to some extent depending on the state of charge of the battery.

HOW IT WORKS

The full circuit diagram for the Motorists' Buzz-Box is shown in Fig.1. In the descriptions which follow, the supply voltage is assumed to be 12V. However, it turns out that the exact value of the voltage (within operating limits) does not matter and this point will be explained later.

Note that there is *no* reverse-polarity protection provided. This would introduce

a voltage drop which would interfere with correct operation of the circuit.

However, providing the unit is correctly wired to the cigar lighter plug, the circuit cannot be connected incorrectly. Fuse FS1 provides some protection against overheating if a short-circuit were to occur. However, it does not provide any protection against reverse-polarity.

DOWN TO EARTH

The "earth test" centres around IC1a which is one section of quad op.amp (operational amplifier), IC1. This contains four identical units – the other three are associated with other tests.

The non-inverting input (pin 3) of IC1a is connected to a potential divider having fixed resistor R1 as the top arm. Resistor R2 appears in series with the resistance between the probe and the 0V line. This is labelled "R" (the "earth resistance") in Fig.1. Resistor R2 and R form the lower arm of the potential divider.

It will be noted that resistors R7 and R8 connected in series, appear in parallel with

R. When the probe is connected to an earth point there will be only a very small resistance between itself and the 0V line so the effect of resistors R7 and R8 (having a combined resistance much higher than R) is negligible.

When the probe is left unconnected, the non-inverting input (pin 3) will be at 9.7V approximately. This is due to the potential divider which now consists of resistor R1 in the top arm and R2 in series with R7 and R8 in the lower one.

When the probe is connected to an "earth" point, R will have a very low value. Assume for the moment that this is zero. The upper and lower arms of the potential divider connected to IC1a non-inverting input will now be equal. The voltage here will then be one-half that of the supply – that is, 6V approximately.

However, if the earth resistance was, say, 0.5 ohm the lower arm would have a greater resistance than the upper one. In this case, calculation shows that the voltage at IC1a non-inverting input would be 6.03V, 30mV more than before.

POTENTIALLY MORE

The inverting input of IC1a (pin 2) is also connected to a potential divider. This comprises resistor R3 (the top arm) and the network of resistors R4, R5 and preset potentiometer VR1 connected in series (the bottom one). When preset VR1 is set to minimum, the voltage at the inverting input will be 5.8V and when at maximum, 6.1V approximately.

By adjusting preset VR1 at the end of construction, the inverting input voltage can be made to exceed that at the non-inverting one when R is between zero and some chosen value. The op.amp will then have its output (pin 1) low.

Some adjustment is needed to provide the required "low" point taking account of component tolerances and the resistance already existing in the connecting wires. In the prototype unit, the low point was set at 0.3 ohm approximately.

With the probe unconnected, the voltage at IC1a pin 3 (9.7V) exceeds that at pin 2 (6V approx.) so the op.amp output is high. This has no further effect.

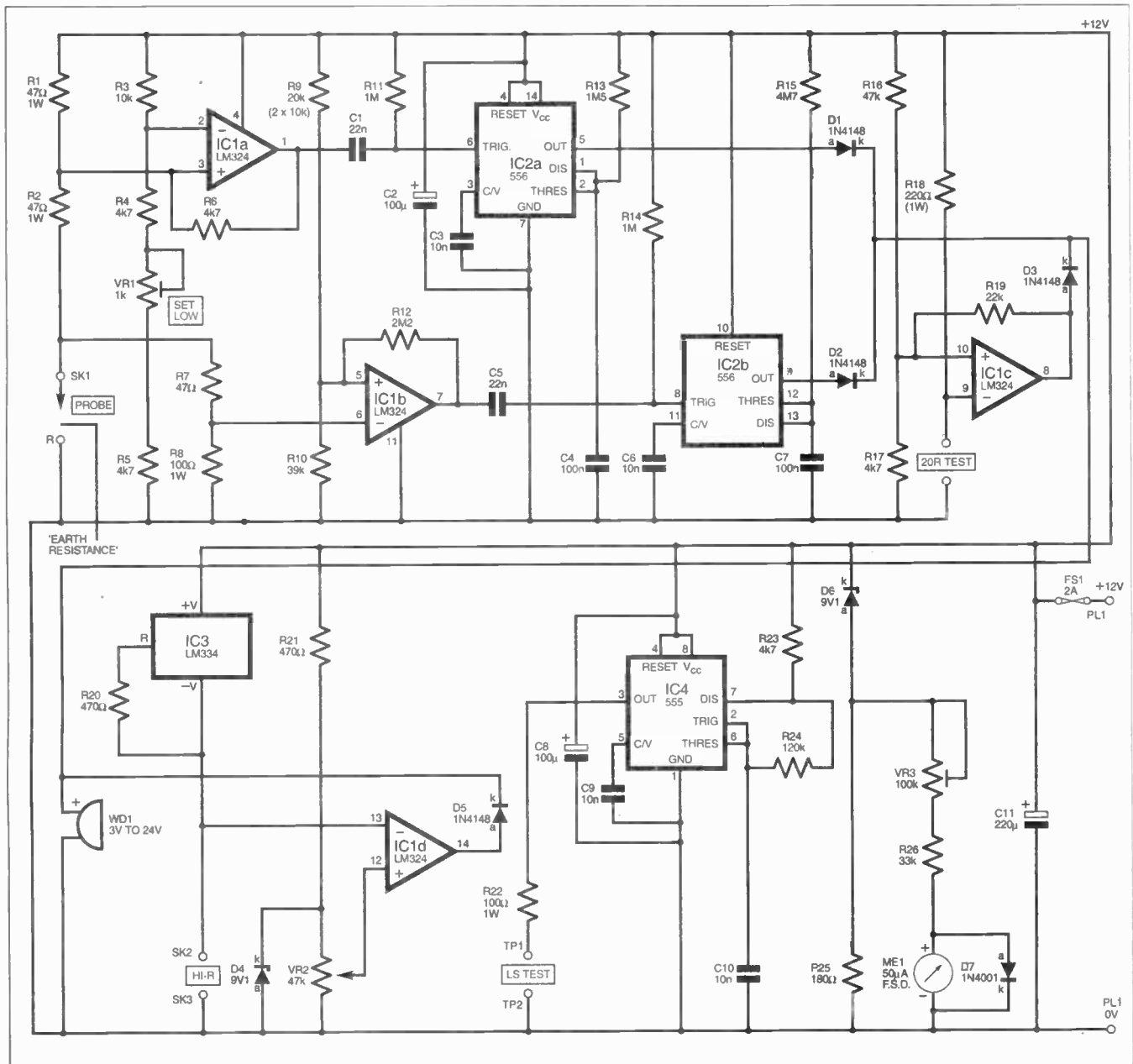


Fig.1. Complete circuit diagram for the Motorists' Buzz-Box. Note that some resistors must be rated at 1W.

GOOD ENOUGH!

When the probe detects a sufficiently "good" (that is, low resistance) earth point, the low logic state of IC1a output (pin 1) applies a short duration low pulse to IC2a pin 6 (the trigger input) via capacitor C1. IC2 is a dual timer with both sections, IC2a and IC2b configured as monostables.

In the case of IC2a, the time period is set by the value of resistor R13 and capacitor C4 and with those used here, it will be rather less than 0.2 second. During this time, the output (pin 5) goes high then reverts to low. While high, current flows through diode D1 to buzzer WD1, which gives a *short* bleep.

Resistor R11 maintains IC2a pin 6 in a high state in the absence of a trigger pulse and this prevents false operation. Resistor R6 applies a little positive feedback to the op.amp (IC1a) system and this sharpens the switching action.

12 VOLT TEST

For the 12V Test, op.amp IC1b (another section of quad op.amp, IC1) and IC2b (the other section of dual timer IC2) are used. It will be noted that the same probe is used for both the "Earth" and "12V" tests and this is particularly convenient when making checks.

The action of the 12V test is best described by considering what voltage exists at IC1b inverting input (pin 6) when the probe is (a) connected to a point at +12V, (b) unconnected and (c) when connected to 0V (that is, while performing an earth test).

In the case of (a), IC1b pin 6 may be considered to be connected to a potential divider having resistors R7 in the upper arm and R8 in the lower one (remembering that the top end of R7 is now connected to +12V). This gives a voltage of 8.16V.

In the case of (b) pin 6 is connected to the potential divider comprising resistors R1, R2 and R7 in series in the upper arm and R8 in the lower one. This provides a voltage of almost 5V. In the case of (c) the top end of R7 and the bottom end of R8 are both connected to 0V so the voltage at pin 6 is zero.

MORE POTENTIAL

The non-inverting input of IC1b is connected to another potential divider comprising resistor R9 in the upper arm and R10 in the lower one. With the values specified, the voltage applied to this input will be 7.93V.

If the probe is touched on a point within about 0.3V of the positive supply voltage, the inverting input voltage will exceed the non-inverting one. The output at pin 7 will then go low. This low state is applied, via capacitor C5, to the trigger input (pin 8) of the monostable based on IC2b.

The time period of this section is related to the values of resistor R15 and capacitor C7, and with those specified it will be 0.5 second approximately. During this time, the output at pin 9 goes high and current passes via diode D2 to the buzzer. This then emits a *long* bleep.

The trigger input at pin 8 of IC2b is maintained in a normally-high state using resistor R14. Resistor R12 provides a little positive feedback to op.amp IC1b and this sharpens the switching action.

COMPONENTS

Approx. Cost
Guidance Only
excluding case & meter

£15

Resistors

R1, R2	47Ω 1W (2 off)
R3	10k
R4 to R6,	
R17, R23	4k7 (5 off)
R7	47Ω
R8, R22	100Ω 1W (2 off)
R9	20k (2 off 10k units – see text).
R10	39k
R11, R14	1M (2 off)
R12	2M2
R13	1M5
R15	4M7
R16	47k
R18	220Ω 1 watt
R19	22k
R20, R21	470Ω (2 off)
R24	120k
R25	180Ω
R26	33k

Plus 0.22W test resistor – see test. Also 10kΩ and 47kΩ test resistors.

All resistors, apart from the 0.22W test resistor, are of the 1% metal film type. Unless otherwise indicated, they should be rated at 0.6W. The 0.22W test resistor may be of any type.

Potentiometers

VR1	1k min. preset, vert.
VR2	47k min. rotary carbon, lin.
VR3	100k min. preset, vert.

Capacitors

C1, C5	22n polyester, 5mm pin spacing (2 off)
C2, C8	100μ radial elect. 25V (2 off)

See
SHOP
TALK
page

C3, C6	
C9, C10	10n polyester, 5mm pin spacing (4 off)
C4, C7	100n polyester, 5mm pin spacing (2 off)
C11	220μ radial elect. 25V

Semiconductors

D1 to D3,	1N4148 signal diode (4 off)
D4, D6	9V1 Zener diode (2 off)
D7	1N4001 50V 1A rect. diode
IC1	LM324N dual op.amp
IC2	556N dual timer
IC3	LM334N adjustable current source
IC4	555 timer

Miscellaneous

ME1	50μA full-scale deflection (f.s.d.), moving coil panel meter – see text
SK1, SK2,	4mm chassis sockets
SK3	(3 off) matching plugs (2 off) – see text.
TP1, TP2	small terminal posts (2 off)

Printed circuit board available from the *EPE PCB Service*, code 278; plastic box, size 150mm x 100mm x 60mm external; 8-pin d.i.l. socket; 14-pin d.i.l. socket (2 off); test meter probe to fit SK1; screw terminals (2 off); 5A terminal block (2 sections); 5A flexible twin wire (or ready-made cigar lighter extension lead (PL1)) – see text; materials for test rails; strain relief bush; control knob for VR2; self-adhesive p.c.b. stand-off pillar (2 off); solder etc.

TWENTY OHM TEST

The 20 Ohm Test or "low resistance test" is centred on IC1c, the third section of quad op.amp IC1. The non-inverting input (pin 10) is held at a potential of just over 1V due to the potential divider R16/R17. The inverting input (pin 9) is held at +12V due to resistor R18.

The metal rails on top of the unit form the "20R test" position. When a low-resistance item bridges the rails, this becomes the lower arm of a potential divider with resistor R18 as the upper one.

If the component on test has a resistance less than 22 ohms approximately, the inverting input voltage will fall below that at the non-inverting one. The output at IC1c pin 8 will then go high. The high state will pass, via diode D3, to the buzzer, which will sound.

When the test position is not occupied, the inverting input voltage exceeds the non-inverting one and the output will be low. This state is blocked by diode D3 and has no effect.

Timer IC2 is a robust bipolar device. It needs small-value capacitors connected between the control voltage pins (pin 3 and pin 11) and the 0V line (C3 and C6 respectively). Also, because momentary large current "spikes" occur on the supply rails, capacitor C2 is included to provide a charge reservoir.

In the Earth Test, 12V Test and 20 Ohm Test, both inputs of the op.amp involved

have applied voltages which are derived from potential dividers. These are connected to the same supply lines. Thus, as the supply voltage rises or falls, the voltages at both op.amp inputs will rise or fall in sympathy. It, therefore, does not matter what battery voltage actually exists within operating limits.

TAKING THE LEAD

Ignition leads have a relatively high resistance and this is built into the design to suppress RFI (radio-frequency interference). This would otherwise cause severe noise in the loudspeaker connected to audio equipment and it would even affect radios in nearby cars.

The voltage used in the ignition system is very high (tens of kilovolts) so the relatively high resistance of the leads still enables sufficient current to flow to provide an effective spark at each plug gap.

However, if the resistance rises too much mis-firing occurs. This usually varies with factors such as engine speed and load. If the lead becomes open-circuit, the corresponding cylinder will not fire at all. Any such faults will play havoc with a catalytic converter.

Unfortunately, problems with ignition leads are fairly common so some means of quickly measuring their resistance is useful. This enables the user to check how the resistance of the various leads compare and to determine whether or not they fall within

manufactures' tolerances if this data is available. By "wiggling" the leads as the tests are made, it is possible to check for intermittent faults.

The High Resistance test is centred on IC1d, the fourth section of the quad op.amp. The lead is connected between the inverting input, pin 13, and the 0V line. A fixed current is now passed through it from the adjustable current source device IC3. This is programmed using resistor R20 and with the specified value, will be some 140µA.

With a constant current flowing through the lead, the voltage across its ends will be proportional to its resistance. It turns out that with a resistance of 64kΩ, the voltage across it will be nearly 9V and, of course, with zero ohms it is 0V. With no lead connected, virtually no current flows so IC3 obviously cannot maintain its regulation. However, this is of no consequence.

RESISTANCE TRACKING

Operational amplifier IC1d non-inverting input (pin 12) is connected to the sliding contact of panel-mounted potentiometer VR2. The track is connected in series with fixed resistor R21 across the supply.

Zener diode D4 operates in conjunction with R21 to provide a stable 9.1V (regarded as 9V) across VR2 track despite changes in supply voltage (down to around 9.5V). The difference between these two voltages appears across resistor R21. Since VR2 is a linear device, its angle of rotation will be approximately proportional to the voltage at the sliding contact rising from zero to 9V.

With the ignition lead in "Hi-R" position, VR2 control knob is slowly rotated. At some point, the voltage at the non-inverting input will exceed that at the inverting one. The output at pin 14 of IC1d will then go high and provide a feed to the buzzer WD1 through diode D5.

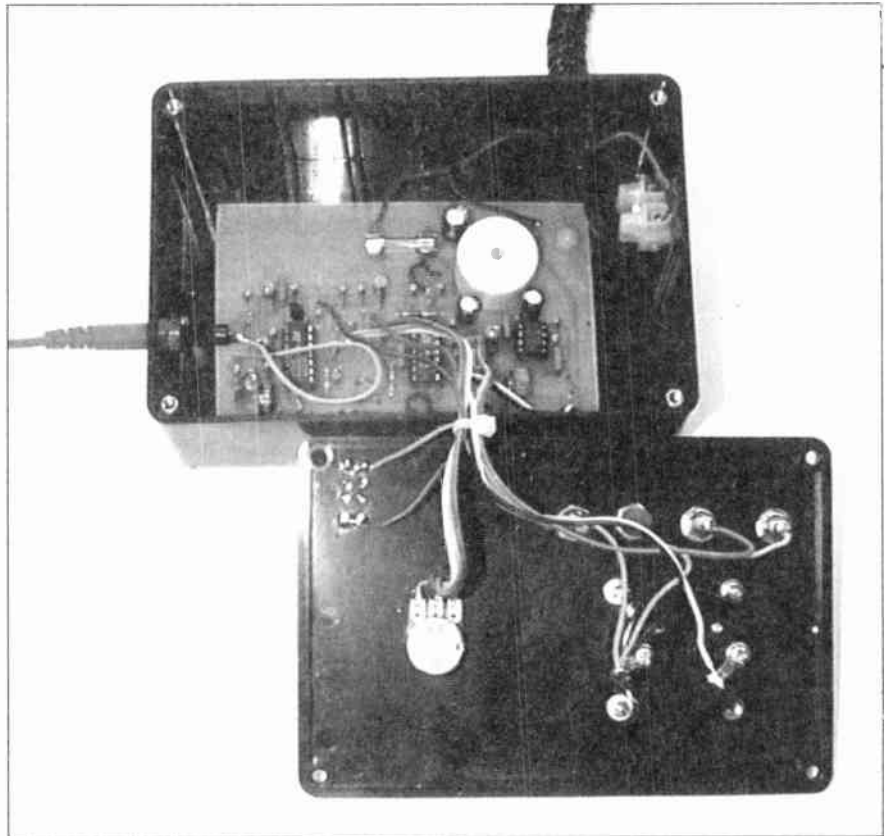
By adjusting the control knob, the position can be found where the buzzer just sounds. The resistance of the lead may then be read off a scale. Marking the scale 0 to 50kΩ (50 kilohms) is a simple matter and will be carried out at the end.

LOUDSPEAKER TEST

For the Loudspeaker Test a single 555 timer, IC4, is used. This is of the same type as the dual unit used for IC2. However, here it is configured as an astable. Thus, as long as a supply exists, the output at pin 3 of IC4 will provide a continuous train of on-off pulses.

The output from IC4 pin 3 is connected to one of the loudspeaker terminals (TP1), via resistor R22, while the other one (TP2) is connected to 0V. Providing the pulse frequency lies within the audible range, a loudspeaker connected to the terminals will produce a sound. Remembering the description of IC2, capacitor C9 is the control voltage capacitor and C8 provides a charge reservoir.

The components which determine the pulse frequency are resistors R23, R24 and capacitor C10. Using the specified values, this frequency will be 600Hz approximately. The ears are sensitive to this frequency and the loudspeaker will reproduce the sound well.



Layout of components inside the plastic box and wiring to components mounted on the lid.

Because the signal is a simple square wave, the power has been kept low to prevent possible damage. This is the reason for including resistor R22 in series with the output. This limits the peak current to 120mA or thereabouts.

This is not a precision signal designed to assess the performance of the loudspeaker. It is used simply to find which pair of leads is which and to identify non-working units, loose connections and so on.

VOLTMETER

The read-out of the supply voltage is provided by panel meter ME1. This is scaled 0 to 50µA but it is modified to show a d.c. range voltage from 10V to 14V.

The supply is connected to a 9.1V (regarded as 9V) Zener diode, D6, connected in series with fixed resistor R25. As long as the supply is a little more than the Zener breakdown voltage, the diode will conduct and this voltage will appear across it. The difference between the supply voltage and 9V will then appear across R25.

If the supply voltage is less than the Zener breakdown voltage, the diode will not conduct and therefore the voltage across resistor R25 will be zero. With a supply voltage of 14V (the maximum value in practice), the voltage across R25 will be 5V.

Meter ME1 operates in conjunction with preset potentiometer VR3 and fixed resistor R26 to provide a voltmeter having a full-scale reading of 14V. With an applied voltage less than about 9.5V, it will read zero.

The region between 9V and 10V must be regarded as a "grey area". This depends on exactly when the Zener diode begins to conduct. Also, at the beginning it does not do this sharply.

Values below 10V are therefore not known with any accuracy. At 10V the Zener diode will be behaving as it should and the scale after that will be more-or-less linear (equal changes in voltage producing equal steps on the scale). This is why there is space between the rest position of the pointer and 10V (see photograph).

METER CHOICE

The values of components have been chosen for a meter having a full-scale deflection of 50µA (although a 100µA unit would also work). Preset VR3 will be adjusted to give the correct full-scale reading at the end of construction. The internal resistance of the meter itself will be a few kilohms. However, the exact value does not matter because it is taken into account when VR3 is adjusted.

Diode D7 is connected in parallel with the meter movement as a protection device. Thus, if due to a fault an excessive current would otherwise flow through the meter, the voltage across it would be limited to 0.7V approximately (the forward voltage drop).

Normally, a smaller voltage than this exists across the meter (with the specified device carrying 50µA it is about 0.2V). Under normal conditions, therefore, the diode will have no effect. Under fault conditions, the current will be around 200µA but the meter will probably not be damaged.

CONSTRUCTION

Construction is based on a single-sided printed circuit board (p.c.b.). The topside component layout and full size underside copper track foil master are shown in Fig. 2. This board is available from the EPE PCB Service, code 278.

Begin construction by drilling the two fixing holes then solder the sockets for IC1, IC2 and IC4 in position (but do not insert the i.c.s themselves yet). Solder the fuse clips in place. If these are not available, you could use a small fuse block instead. If necessary, this could be mounted off board and hard-wired to the FS1 points on the p.c.b. later. Solder in position the single link wire, just above IC2 socket.

Add all resistors and the preset potentiometers. Note that some of the fixed resistors *must* have a power rating of one watt minimum. This is because they can become quite warm in prolonged tests.

Although five per cent tolerance would be sufficient for some of the resistors, some must have a tolerance of one per cent. To avoid confusion, one per cent tolerance resistors have therefore been specified throughout.

Resistor R9 must have a value of 20kΩ. It will probably be easier to use two 10kΩ units connected in series. Space has been left for two such resistors on the p.c.b. Note that they are *both* labelled R9 on the component layout, Fig.2.

Solder the capacitors in place. It is essential to place the electrolytic

capacitors – C2, C8 and C11 – with the correct polarity. Solder all diodes in position taking care over their polarity, noting particularly the orientation of Zener diodes (D4 and D6). Add the audible warning device (WD1), taking note of its polarity (which is marked on top).

Next, solder 15cm pieces of light-duty stranded connecting wire to the following points on the p.c.b.: +12V; 0V; ME1 (2 off); VR2 (3 off); Probe; TP1; TP2; HI-R and 20Ω. By using different coloured wires (pieces of "rainbow" ribbon cable), problems will be avoided later.

Solder IC3 in position (the flat face is towards the left-hand side of the p.c.b.) keeping its end leads at least 5mm in length. Solder it quickly to avoid damage. If necessary, use a simple heat shunt – this may be nothing more than a pair of fine-nose pliers. These are used to grip each lead between the body of the device and the p.c.b. as it is soldered.

BOXING-UP

Begin the boxing-up procedure by making the holes for the meter. Mark out the large one and the small fixing ones using

the template supplied with it. The large hole can be made by drilling a series of small holes around the outline. These are then joined together using a small hacksaw blade. The holes will be covered by the meter face so there is no point in trying to make a perfect job.

Place the p.c.b. in position on the base of the box. Mark through the fixing holes. Remove the p.c.b. and drill these through.

Decide on positions and drill holes for VR2 bush, also for terminals TP1/TP2 (for the loudspeaker test) and sockets SK2/SK3 (for the HI-R test). Place the control knob on the potentiometer spindle and measure how much needs to be cut off. Mark this, remove the knob and cut off the excess using a small hacksaw.

While doing this, hold the spindle (not the potentiometer body) in the vice. *Gripping the body of the device is likely to damage it.* File the cut edge smooth.

Place the potentiometer bush through its hole and secure it loosely. Mark a suitable position for the anti-rotation lug on the inside. Remove the potentiometer again and drill a small hole to be a tight fit with the lug.

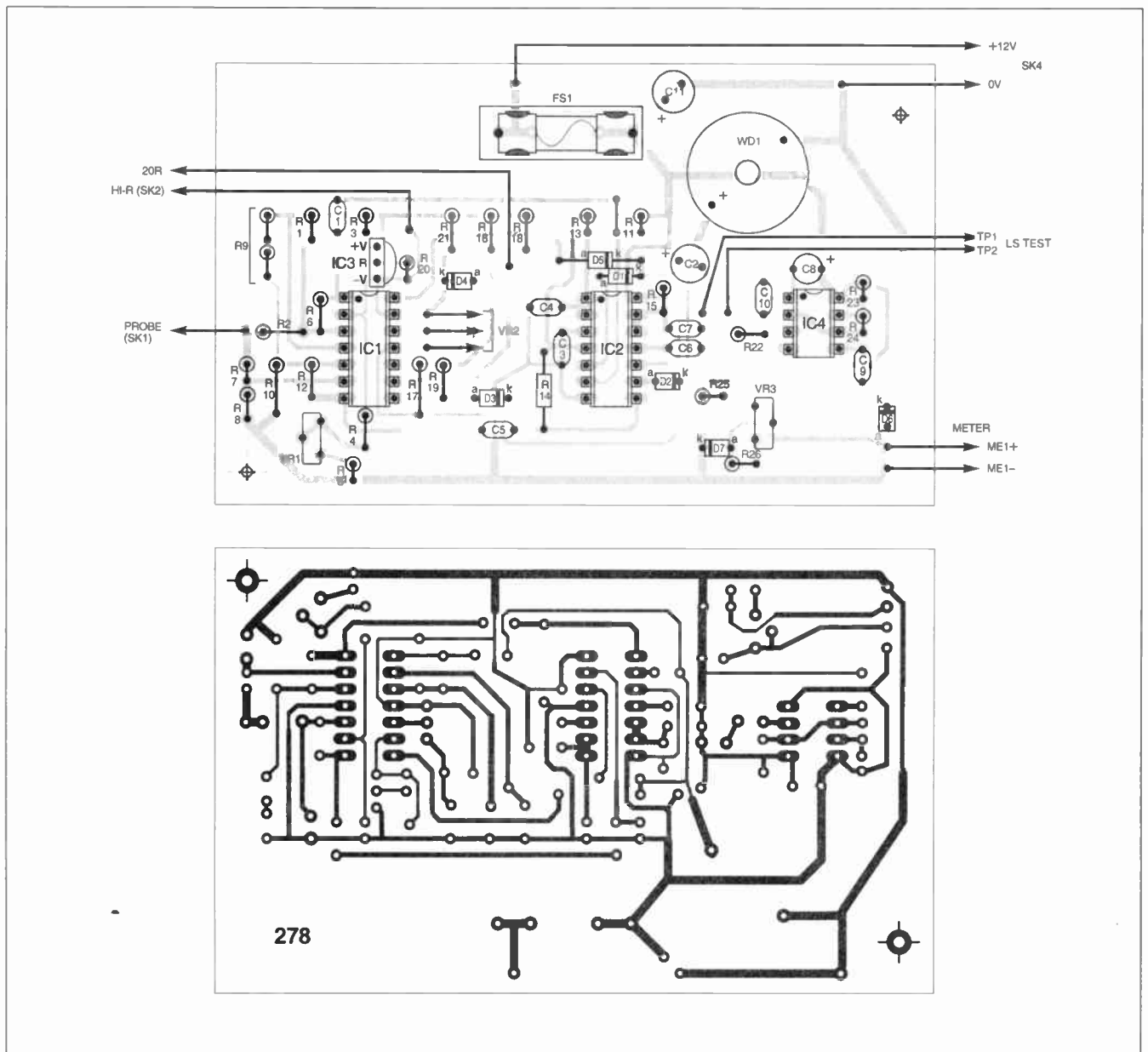
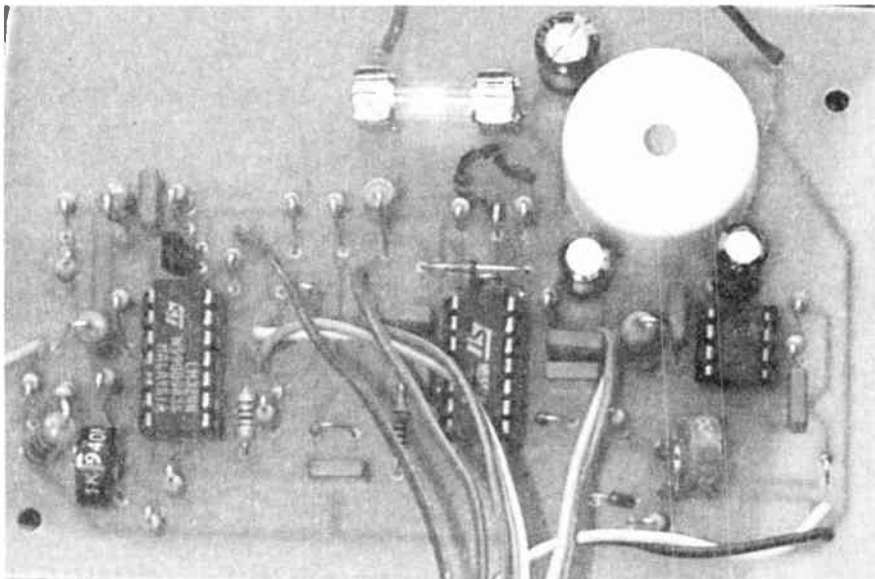


Fig.2. Printed circuit board topside component layout and full-size copper foil master for the Motorists' Buzz-Box.



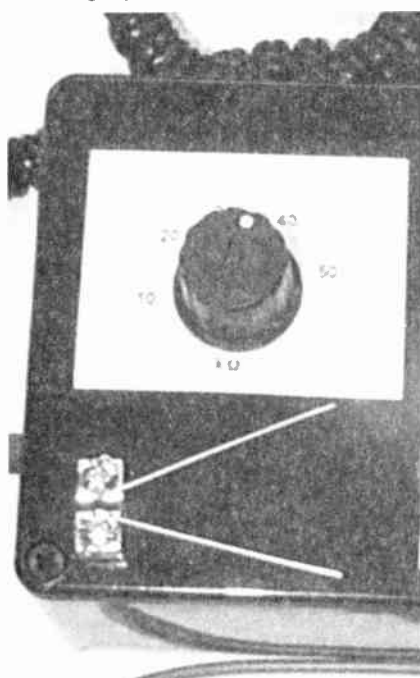
Completed prototype circuit board. Use different coloured wires (rainbow ribbon cable) to ease identification.

Drill the hole for probe the socket, SK1, in one side panel of the box and attach it. Drill a hole in the rear of the box for entry of the input wire. This must be large enough to accommodate the strain relief grommet.

ON THE RAILS

Refer to the photographs and make the test "Rails". In the prototype unit, these were constructed using paper clips which were secured in place using screw terminals of the type shown. This method gives a neat finish and also allows the rail wires to be easily replaced if they become damaged in use.

The screw terminals used in the prototype were of the p.c.b. mounting type. These had four lugs which were made to be pushed through holes in a panel and soldered into position. However, if tight holes are drilled in the box, the lugs may be pushed through then secured by bending them slightly.



The "test" rails made from paper clips and the cardboard "resistance" scale.

The narrow end of the rails should be only a few millimetres apart (to allow for the testing of small bulbs) and about 35mm apart at the other end to enable testing of 1/4in fuses. The suggested method raises the rails above the top face of the case and this allows for the easy testing of small bulbs.

Attach potentiometer VR2 securely with the anti-rotation lug engaged in its hole. This lug prevents the body from possibly rotating in service (due to harsh use or loosening of the fixing nut). This would

result in incorrect readings and could even snap off the connecting wires. Mount the p.c.b. on short plastic spacers and all remaining components.

Refer to Fig.3 and complete all the interwiring between the p.c.b. and off-board components. This should be done slowly to avoid errors in view of the fact that there are several components involved. Note particularly which wire from the p.c.b. connects to which VR2 tag (the diagram gives a rear view). Only if they are correct will the high resistance (Hi-R) section work properly with clockwise rotation corresponding with increasing resistance.

SUPPLY LEAD

The cable used for the supply voltage input lead **must be rated at 5A minimum**. This will avoid excessive voltage drops due to resistance. In the prototype, a ready-made "curly" extension lead was used with the line socket end cut off.

Fit the 2-core cable wire through the strain relief bush. *Make sure it is secure.* Leaving a little slack, connect the ends to a 2-section piece of screw terminal block mounted inside the case. Connect the p.c.b. wires to this making certain that the polarity is correct.

Adjust preset VR1 fully clockwise (with respect to the left-hand side of the p.c.b.) and preset VR3 to approximately mid-track position.

TESTING

Double-check that the polarity to the circuit is correct before plugging in.

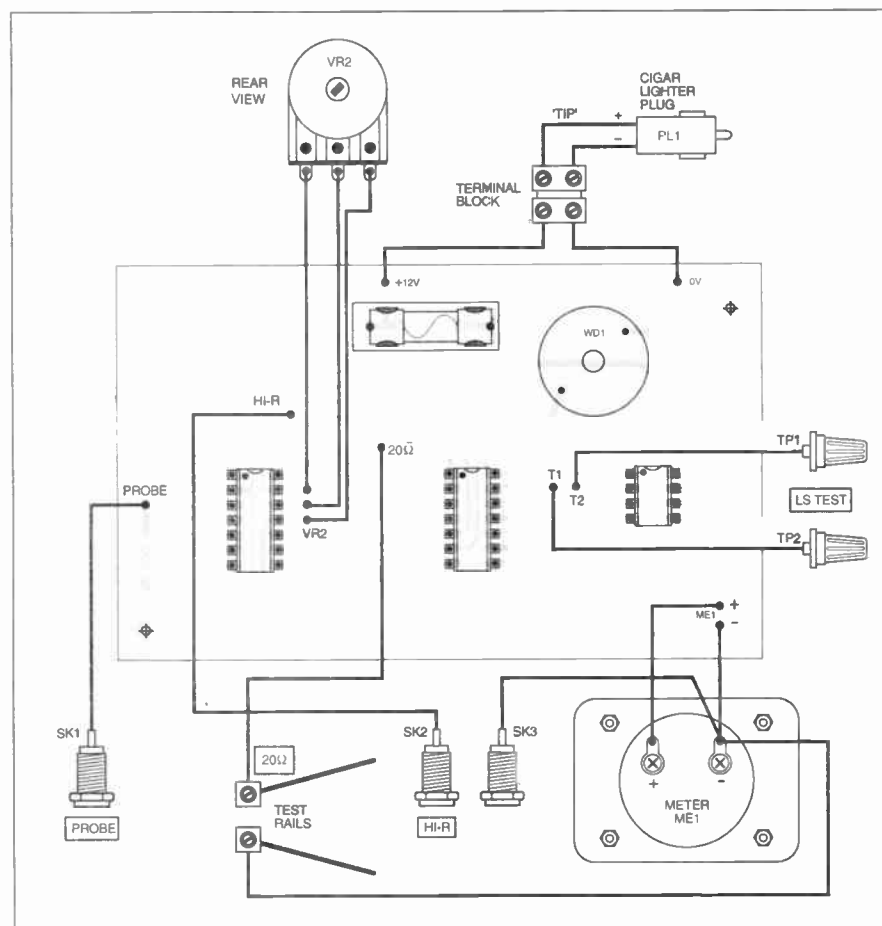


Fig.3. Interwiring from the printed circuit board to off-board components. The general layout within the case can be seen in the photographs.

Connect the unit to the lighter socket. In some cars the ignition must be switched on for this to operate. The monostables will probably self-trigger and the buzzer give a bleep. The meter should read somewhere on the scale. Adjust preset VR3 slightly if necessary.

Plug the probe into socket SK1. Touch this on an "earth" point. The buzzer should give a short bleep. If not, adjust preset VR1 so that it does. Touch the probe on a point at positive supply voltage. The buzzer should give a longer bleep.

Check that the 20Ω test works by bridging the rails with a piece of wire. The buzzer should sound continuously.

METER SCALE

Remove the front cover from the meter by gently pulling or careful levering using a thin knife blade. Exercising great care, remove the scale fixing screws using a small screwdriver. Slide out the scale taking care not to touch the pointer.

Cut out a paper scale to glue on top of the existing one. By pressing them in contact, you will see through sufficiently to mark with a pencil the positions of 10μA, 20μA, 30μA, 40μA and 50μA. Mark these 10V, 11V, 12V, 13V and 14V respectively using dry print lettering. Put a light pencil dot at the zero position.

You can, if you wish, "Tippex" or white-out the old scale (so that it does not show through the paper). Glue the new scale over it and re-assemble the meter. Attach the front taking care that the adjustment peg engages with the fork in the movement.

Check that the pointer rests at the zero dot. If not, adjust the screw on the top face until it does.

MAKING ADJUSTMENTS

Start the adjustment procedure with the Voltmeter. If you have access to a variable voltage power supply unit, you could use that to set VR3 to give a full-scale reading when 14V is applied. You will then find that the other markings correspond fairly well. Adjust preset VR3 to give the best compromise on these figures.

However, if you do not have access to a suitable power supply, plug the unit into the cigar lighter, measure the battery terminal voltage and adjust preset VR3 to correspond. The whole of the scale will then be reasonably accurate.

GOOD OLE' EARTH

To adjust the Earth Test low point, take the 0.22 ohm test resistor (or some other chosen value) and connect the probe to one end of this. Connect the other end to the negative terminal of the battery or a good earth point. Adjust preset VR1 until the buzzer just sounds.

R-SCALE CALIBRATION

Now for potentiometer VR2's front panel "resistance" scale and calibration. Make a thin cardboard scale and secure it temporarily behind the control knob. Pencil in the zero position (knob turned full anti-clockwise).

There is no point in marking the scale with great accuracy. It may be assumed that this is linear – that is, equal increases in resistance correspond with equal steps.



Testing an ignition (plug) lead. The new meter scaling can also be seen here.

Take the 10kΩ test resistor and connect it to the Hi-R test position. Rotate VR2 control knob to the point where the buzzer just sounds. Make a pencil mark. Repeat using the 47kΩ (regarded as 50kΩ) test resistor, again, making a mark.

Remove the scale and, by measurement, make marks for each 10kΩ step from 10 to 50. Mark these permanently. Label the scale "kΩ" then attach the scale in its original position. Check that the "zero" point is still correct.

USING THE BUZZ-BOX

There are several points to observe when using the test probe. *This must be applied with care and with reference to the car wiring diagram. It may be used on items which carry supply current direct to some accessory (e.g. at a fuse, switch or connector) or an earth point. Do not probe around indiscriminately.*

On no account use it inside pieces of electronic equipment. If, for example, it was used inside an electronic control unit severe damage could result to the control unit.

Do not apply it to any connector associated with an engine management system, ABS unit or any other electronic system or sensor. Do not apply it to any wires inter-connecting such circuits.

Do not apply it to points on any diagnostic socket. Do not use it in the engine compartment with the engine running.

When using the loudspeaker test, disconnect both of the wires involved before connecting them to the unit.

CRANK TEST

The unit is not really designed to be used with the engine running except for a "crank test". To do this, watch the voltmeter as the starter motor is operated. If the needle drops immediately below 10V and the battery is known to be well charged, it is likely to be at the end of its useful life.

It would be worth checking the battery connectors (for tightness and lack of corrosion) and the connection of the earth strap to the car chassis since trouble here could produce a similar result. A good battery should be capable of maintaining a voltage of 10V or more for a few seconds until the engine fires.

STATE OF CHARGE

The charge state of the battery is found by measuring the voltage but this needs some interpretation. It will only be meaningful if the battery has not been charged for a few hours before the test is made.

A terminal voltage less than 11.5V indicates a battery which is "flat" (possibly irreversibly so). A voltage of 12.5V or more indicates a good state of charge and near 13V indicates full charge.

Take great care to avoid touching the probe on a +12V point and the car chassis or other earth point at the same time. *If you did, there would be a short circuit and damage could be caused.* In the prototype unit, the end of the probe was insulated using heat-shrinkable sleeving so that only a little bare tip remained. This reduced the likelihood of causing a short circuit.

UP TO YOU

Ignition leads could be connected to the test position in various ways. The method used in the prototype was to solder short pieces of stiff wire to the 4mm plugs. The other ends of the wire were bent into a loop to make contact with the connectors when inserted in the ends of the leads (see photograph).

Several ignition leads were tested and these had a resistance between 5kΩ and 20kΩ. Without specific data, compare the resistances of the leads. If one has a markedly higher value than the rest, it should be replaced and, preferably, the whole set renewed. □

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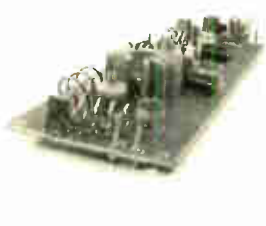
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- **PC DATA ACQUISITION/CONTROL UNIT** Use your PC to monitor physical variables (e.g. pressure, temperature, light, weight, switch state, movement, relays, etc.), process the information & use results to control physical devices like motors, sirens, relays, servo & stepper motors. Inputs: 16 digital & 11 analogue. Outputs: 8 digital & 1 analogue. Plastic case with printed front/rear panels, software utilities, programming examples & all components (except sensors & cable) provided. 12VDC 3093KT £99.95
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- **PC SERIAL PORT ISOLATED I/O BOARD** Provides eight 240VAC/10A relay outputs & 4 optically isolated inputs. Designed for use in various control & sensing applications. e.g. load switching, external switch input sensing, contact closure & external voltage sensing. Controlled via serial port & a terminal emulator program (built into Windows). Can be used with ANY computer/operating system. Plastic case with printed front/rear panels & all components (except cable) provided. 3108KT £54.95
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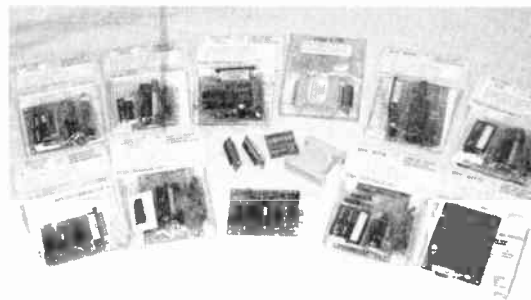
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QUASAR KITS REVIEW



ROBERT PENFOLD

Examining the merits of a dozen electronic kits from Quasar.

WHILE I NORMALLY receive things for review one or two at a time, but no less than a dozen units are under consideration in this review. This is perhaps a slight exaggeration since there are only six different units, but each one has been supplied in kit form and ready-made. All marketed by Quasar Electronics.

It is not practical to consider every device in detail, so we will take a detailed look at one kit and then consider the other units in more general terms. It is kit number 3113, the PC-based dual stepper motor driver that will receive the in-depth coverage.

STEP-BY-STEP

A stepper motor has two centre-tapped coils, effectively giving four solenoids for the driver circuit to control. By pulsing the solenoids in the correct fashion the motor can be made to rotate in either direction in small steps of typically about 15 degrees.

Stepper motors produce little torque, but are used in applications that require precise positioning rather than high power. One way of driving a stepper motor is to use a special integrated circuit to simplify control. With this method there are two control inputs, one of which controls the direction of rotation. The other input is pulsed each time the motor must be moved on by one step.

The more simple method, and the one adopted in this case, is to control the solenoids from four output lines of the computer. Software is then used to generate the appropriate control pulses for whatever actions are required. This slightly complicates the software side of things, but direct control of a stepper motor is not that difficult.

This dual stepper motor interface is basically just eight open collector driver transistors controlled by the data outputs of a PC printer port, plus an MS-DOS program to make the unit operate as a dual stepper motor driver.

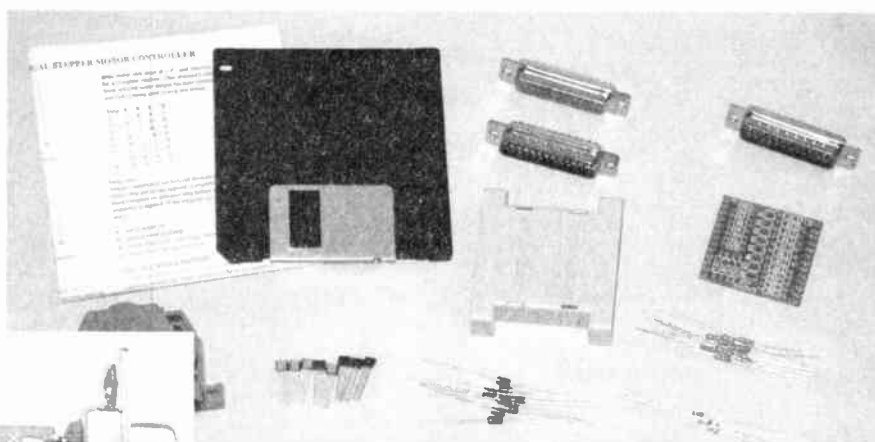
The interface takes the form of a small box having a 25-way D-type connector at each end. The male connector plugs into the PC's printer port, or it can be connected via a "straight" 25-way D connector cable (not supplied). Connections to the motors and power source are by way of the female connector at the other end of the case, and the supplied male connector. You have to supply your own connecting wires. The electronics fits on a tiny printed circuit board that fits between the two D connectors.

GETTING IT TOGETHER

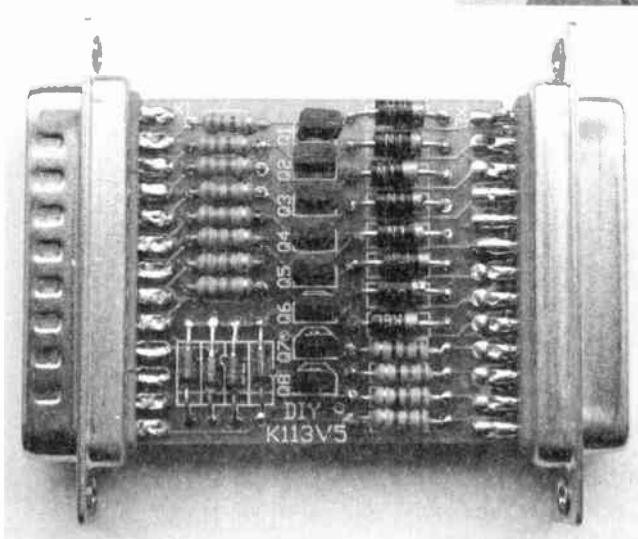
Two A4 size sheets contain the building instructions, notes on use, the circuit diagram, etc. Quite a lot of information is crammed onto these two sheets, and it is definitely advisable to read through them once or twice before starting construction.

The fibreglass printed circuit board is a good quality double-sided type that is printed with a component overlay. Construction starts by fitting the two D connectors, and then the 32 small components are added.

There is no problem in identifying the components, and it is fairly obvious where everything fits. One slight problem is that the board is designed to take eight resistors in the form of a 16-pin



The contents of the stepper motor driver kit.



Completed twin stepper motor driver unit.

d.i.l. package, but the kit is supplied with eight individual resistors. However, the instruction sheets do point out this discrepancy, and make it clear where the resistors are fitted. (The p.c.b. has now been redesigned to overcome this - Ed.)

Having some 32 components squeezed into about nine square centimetres makes construction fiddly rather than difficult. The situation is eased somewhat by the solder resist on the board which helps to avoid accidental short circuits. Also, the board is through-plated so there is no need for any pins to carry connections from one layer to the other.

It is still necessary to take reasonable care to avoid short circuits, and a magnifying glass is as essential as a soldering iron when building this type of board.

The instruction sheets give advice about using the interface with various types of stepper motor. It acknowledges the fact that many

of the motors used by electronics hobbyists are surplus components that are supplied with little or no technical data.

Having sorted out some basic information about the motors it is often a matter of using some trial and error to get everything working properly. I tried the interface with an old Maplin stepper motor for which I did still have the connection data, and I am pleased to say that the unit worked first time.

SOFTWARE

The only supplied software is a DOS program on a 3.5-inch floppy disk. This did not work properly when run in a DOS window under Windows 98, but it worked fine when the computer was rebooted in DOS mode, or when the computer was booted into DOS from a floppy disk.

The program provides a command line interpreter that can be used to issue various commands to the motors, such as spin, stop, dir (direction) and wait. The commands seem to work well enough, and the program is easy to use.

It is possible to have the software process a series of commands contained in a text file, rather like running a DOS batch file. This enables what is effectively a simple program to be written and executed, but for many purposes something more sophisticated than this will be needed. It should not be too difficult to control the motor using a Windows programming language such as Delphi or Visual BASIC.

The output port of the interface also provides access to four handshake inputs of the printer port, which makes it possible to have control of the motor to some extent dependent on feedback from sensors. However, you are completely on your own with this type of thing.

CONCLUSION

Although this is a fairly simple kit, it is not really suitable for beginners, and is not aimed at those of limited experience. Constructing the kit is actually quite easy, but a fair amount of technical expertise is needed to get the finished unit do anything worthwhile.

Considering the simplicity of the unit, at a VAT inclusive cost of £17.95 it is not particularly cheap, but the price is reasonable considering the quality of the components. The printed circuit board is as good as any I have seen, and better than most. As the ready-made interface costs some £29.95 including VAT, it seems to be well worthwhile spending half an hour or so building the kit version.

PIC/ATMEL PROGRAMMERS

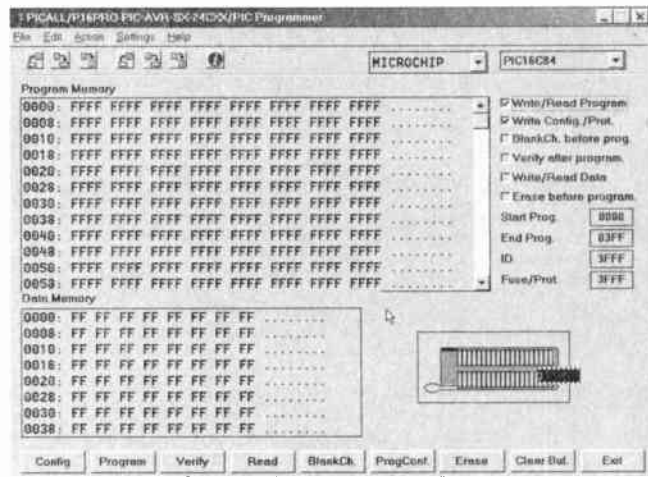
The other kits and ready-made boards received for review are for programming PIC or ATMEL microcontrollers. The Quasar kits seem to be based on designs that are available on the Internet. The original instruction leaflets were quite brief at just one A4 sheet with printing on one side, but these have now been updated and improved. Further assistance is often available from one or more web sites, as are more recent versions of the software. The latter is shareware, although in some cases the full registered version is supplied in the kit price. In general, the kits seem to be quite easy to put together.

The printed circuit boards are good quality fibreglass boards, but are mostly single-sided types that require some link-wires. The boards have a solder resist layer that helps to avoid short circuits due to excess solder, and this makes it much easier to get things working first time.

Some of the kits have attractively low prices, but bear in mind that all you get is a kit of parts to build the board, together with a floppy disk containing the software. The cable to connect the board to the computer and the mains adapter are optional extras at £4.95 and £5.95 each.

The kit versions of the programmers are supplied with an ordinary 40-pin d.i.l. socket, and a universal ZIF socket costs an additional £15.95. The ZIF socket has to be regarded as an essential buy. Apart from other considerations, many PIC chips will simply not fit an ordinary 40-pin holder with its 0.6-inch row spacing.

As already pointed out, there is also a software registration fee with some kits if the full version is required, and this adds a further £14.95 to the cost. These extras can



Windows version of the PICALL software.

substantially boost the basic kit price, although the overall cost still seems to be reasonable.

The programmers are mostly quite easy to use. The P16PRO serial PIC programmer supports a range of PIC microcontrollers and has MS-DOS software, but it is very simple and straightforward to use. The PICALL programmer supports a wide range of PIC microcontrollers, plus a limited range of non-PIC devices, and has the option of MS-DOS or Windows software. The Windows version of the program is easier to use, but the diagram showing how to connect the selected device to the ZIF socket makes things "as clear as mud". It is best to resort to the MS-DOS version for connection details.

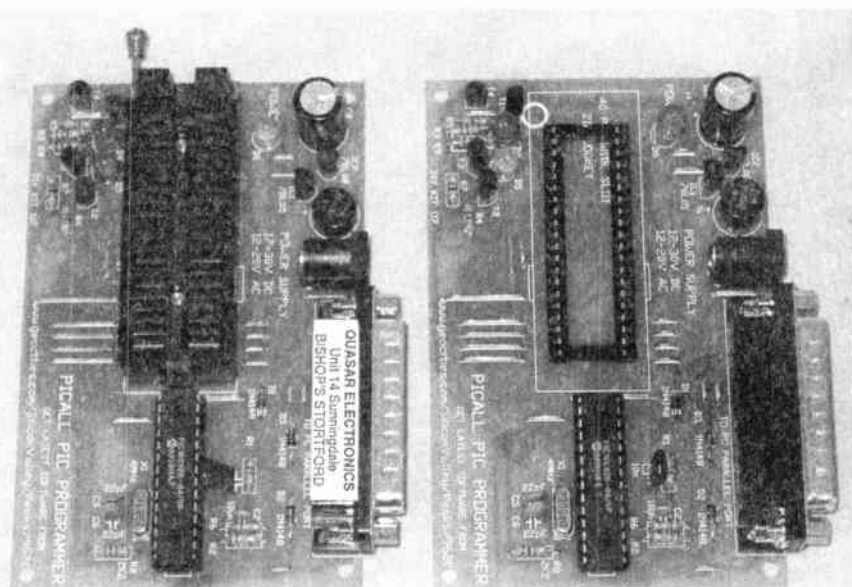
FINAL CONCLUSION

Building any of these kits should not present any major problems for someone who has a small amount of experience at electronic project building. However, none of the kits can really be recommended for beginners, since a fair amount of technical know-how is needed in order to utilize the finished units. For the same reason, the ready-made boards are only suitable for those who know what they are doing.

It is a pity that neither the kits nor the ready-made units are supplied with more documentation as this would substantially broaden their appeal. (This point has now been addressed with new documents plus an electronic manual that is provided with the software - Ed.) As things stand, the kits and ready-made units are of excellent quality, represent reasonably good value for money, and represent a worthwhile proposition for someone having the requisite technical expertise and an Internet connection.

For more information contact Quasar Electronics, Unit 14 Sunningdale, Bishop's Stortford, Herts CM23 2PA. Tel: 01279 306504. Fax: 08707 064222.

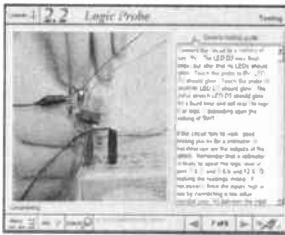
Email: epesales@QuasarElectronics.com.
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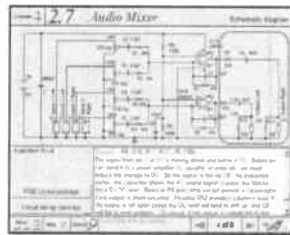
The ready-made (left) and kit version of the PICALL programmer.

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NEW ELECTRONICS PROJECTS



Logic Probe testing

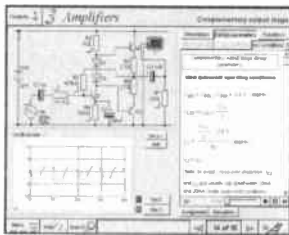


Audio Mixer circuit description

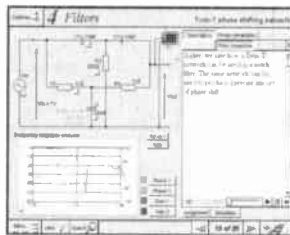
Electronic Projects is split into two main sections: **Building Electronic Projects** contains comprehensive information about the components, tools and techniques used in developing projects from initial concept through to final circuit board production. Extensive use is made of video presentations showing soldering and construction techniques. The second section contains a set of ten projects for students to build, ranging from simple sensor circuits through to power amplifiers. A shareware version of Matrix's CADPACK schematic capture, circuit simulation and p.c.b. design software is included.

The projects on the CD-ROM are: Logic Probe; Light, Heat and Moisture Sensor; NE555 Timer; Egg Timer; Dice Machine; Bike Alarm; Stereo Mixer; Power Amplifier; Sound Activated Switch; Reaction Tester. Full parts lists, schematics and p.c.b. layouts are included on the CD-ROM.

ANALOGUE ELECTRONICS



Complimentary output stage

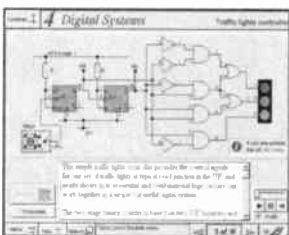


Twin-T phase shifting network

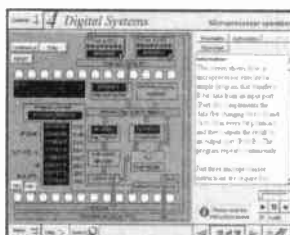
Analogue Electronics is a complete learning resource for this most difficult branch of electronics. The CD-ROM includes a host of virtual laboratories, animations, diagrams, photographs and text as well as a SPICE electronic circuit simulator with over 50 pre-designed circuits.

Sections on the CD-ROM include: **Fundamentals** – Analogue Signals (5 sections), Transistors (4 sections), Waveshaping Circuits (6 sections). **Op.Amps** – 17 sections covering everything from Symbols and Signal Connections to Differentiators. **Amplifiers** – Single Stage Amplifiers (8 sections), Multi-stage Amplifiers (3 sections). **Filters** – Passive Filters (10 sections), Phase Shifting Networks (4 sections), Active Filters (6 sections). **Oscillators** – 6 sections from Positive Feedback to Crystal Oscillators. **Systems** – 12 sections from Audio Pre-Amplifiers to 8-Bit ADC plus a gallery showing representative p.c.b. photos.

DIGITAL ELECTRONICS



Virtual laboratory – Traffic Lights

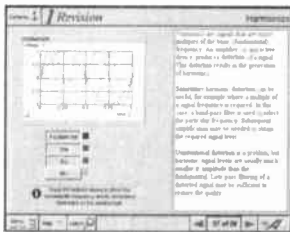


Microprocessor

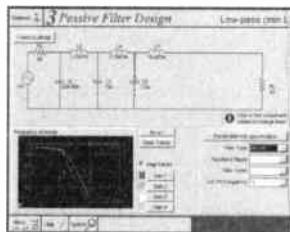
Digital Electronics builds on the knowledge of logic gates covered in *Electronic Circuits & Components* (opposite), and takes users through the subject of digital electronics up to the operation and architecture of microprocessors. The virtual laboratories allow users to operate many circuits on screen.

Covers binary and hexadecimal numbering systems, ASCII, basic logic gates and their operation, monostable action and circuits, and bistables – including JK and D-type flip-flops. Multiple gate circuits, equivalent logic functions and specialised logic functions. Introduces sequential logic including clocks and clock circuitry, counters, binary coded decimal and shift registers. A/D and D/A converters and their parameters, traffic light controllers, memories and microprocessors – architecture, bus systems and their arithmetic logic units.

FILTERS



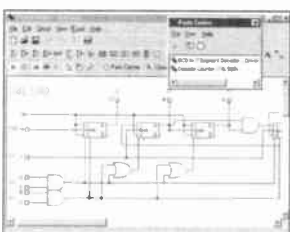
Filter Theory



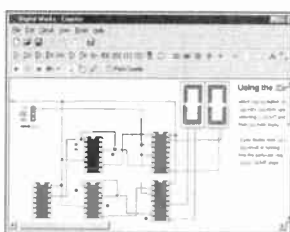
Active filter synthesis

Filters is a complete course in designing active and passive filters that makes use of highly interactive virtual laboratories and simulations to explain how filters are designed. It is split into five chapters: **Revision** which provides underpinning knowledge required for those who need to design filters. **Filter Basics** which is a course in terminology and filter characterization, important classes of filter, filter order, filter impedance and impedance matching, and effects of different filter types. **Advanced Theory** which covers the use of filter tables, mathematics behind filter design, and an explanation of the design of active filters. **Passive Filter Design** which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, band-pass, and band-stop Bessel, Butterworth and Chebyshev ladder filters. **Active Filter Design** which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, band-pass, and band-stop Bessel, Butterworth and Chebyshev op.amp filters.

DIGITAL WORKS 3.0



Macro screen



Counter project

Digital Works Version 3.0 is a graphical design tool that enables you to construct digital logic circuits and analyze their behaviour. It is so simple to use that it will take you less than 10 minutes to make your first digital design. It is so powerful that you will never outgrow its capability.

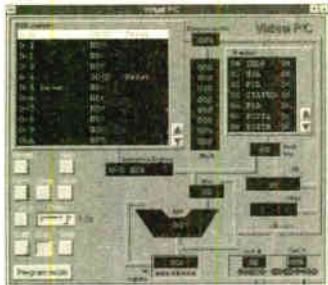
- Software for simulating digital logic circuits
- Create your own macros – highly scalable
- Create your own circuits, components, and i.c.s
- Easy-to-use digital interface
- Animation brings circuits to life
- Vast library of logic macros and 74 series i.c.s with data sheets
- Powerful tool for designing and learning

PRICES
Prices for each of the CD-ROMs above are:

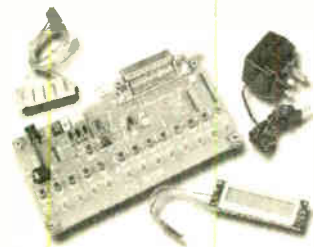
Hobbyist/Student£45 inc VAT
Institutional (Schools/HE/FE/Industry).....£99 plus VAT
Institutional 10 user (Network Licence)£199 plus VAT

(UK and EU customers add VAT at 17.5% to "plus VAT" prices)

Interested in programming PIC microcontrollers? Learn with **PICtutor** by John Becker



The Virtual PIC



Deluxe PICtutor Hardware

This highly acclaimed CD-ROM, together with the PICtutor experimental and development board, will teach you how to use PIC microcontrollers with special emphasis on the PIC16x84 devices. The board will also act as a development test bed and programmer for future projects as your programming skills develop. This interactive presentation uses the specially developed **Virtual PIC Simulator** to show exactly what is happening as you run, or step through, a program. In this way the CD provides the easiest and best ever introduction to the subject.

Nearly 40 Tutorials cover virtually every aspect of PIC programming in an easy to follow logical sequence.

HARDWARE

Whilst the CD-ROM can be used on its own, the physical demonstration provided by the **PICtutor Development Kit**, plus the ability to program and test your own PIC16x84s, really reinforces the lessons learned. The hardware will also be an invaluable development and programming tool for future work. Two levels of PICtutor hardware are available – Standard and Deluxe. The **Standard** unit comes with a battery holder, a reduced number of switches and no displays. This version will allow users to complete 25 of the 39 Tutorials. The **Deluxe Development Kit** is supplied with a plug-top power supply (the **Export** Version has a battery holder), all switches for both PIC ports plus I.C.D. and 4-digit 7-segment I.E.D. displays. It allows users to program and control all functions and both ports of the PIC. All hardware is supplied **fully built and tested** and includes a PIC16F84.

PICtutor CD-ROM

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ELECTRONIC COMPONENTS PHOTOS

A high quality selection of over 200 JPG images of electronic components. This selection of high resolution photos can be used to enhance projects and presentations or to help with training and educational material. They are royalty free for use in commercial or personal printed projects, and can also be used royalty free in books, catalogues, magazine articles as well as worldwide web pages (subject to restrictions – see licence for full details). Also contains a FREE 30-day evaluation of Paint Shop Pro 6 – Paint Shop Pro image editing tips and on-line help included!

Price **£19.95 inc. VAT**

ELECTRONIC CIRCUITS & COMPONENTS + THE PARTS GALLERY

Provides an introduction to the principles and application of the most common types of electronic components and shows how they are used to form complete circuits. The virtual laboratories, worked examples and pre-designed circuits allow students to learn, experiment and check their understanding. Sections include: **Fundamentals:** units & multiples, electricity, electric circuits, alternating circuits. **Passive Components:** resistors, capacitors, inductors, transformers. **Semiconductors:** diodes, transistors, op.amps, logic gates. **Passive Circuits . Active Circuits**

The Parts Gallery will help students to recognise common electronic components and their corresponding symbols in circuit diagrams. Selections include: **Components, Components Quiz, Symbols, Symbols Quiz, Circuit Technology**

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Essential information for anyone undertaking GCSE or "A" level electronics or technology and for hobbyists who want to get to grips with project design. Over seventy different Input, Processor and Output modules are illustrated and fully described, together with detailed information on construction, fault finding and components, including circuit symbols, pinouts, power supplies, decoupling etc.

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Minimum system requirements for these CD-ROMs: PC with 486/166MHz, VGA+256 colours, CD-ROM drive, 32MB RAM, 10MB hard disk space. Windows 95/98, mouse, sound card, web browser.

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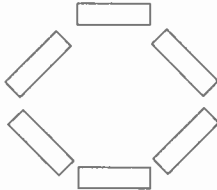
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DO YOU NEED ALL ROUND AIR MOVEMENT?

A simple fan will move air in one direction only. Even an oscillating fan will only blow just over halfway round, but if you need to blow all round, then our boxer fans, Order Ref: 4P114, could give you just this. Six of these placed as the sketch could be operated from our transformer, Order Ref: 4P24. The fans have their own mounting frames and the transformer has a frame so they could all be assembled on a table-top or plywood base, and if you wanted to blow hot air then a heater could also be placed in the centre. We are making a special offer of six fans and the transformer for £20 the lot, including VAT. Order Ref: 20P41.



ANOTHER USEFUL FAN

This is a 12V d.c. brushless fan. It is very small, in fact only just under 60mm square including fixings. Being brushless this fan consumes very little current, in fact it is only 150mA. It is very quiet running and we can supply a transformer and full wave rectifier which would operate it for just £1. Price of the fan £5. Order Ref: 5P291.

TWO MORE POST OFFICE INSTRUMENTS

Both instruments contain lots of useful parts, including sub-min toggle switch sold by many at £1 each. They are both in extremely nice cases, with battery compartment and flexible carrying handles so if you don't need the instruments themselves, the case may be just right for a project you have in mind.

The first is Oscillator 87F. This has an output, continuous or interrupted, of 1KHz. It is in a plastic box size 115mm wide, 145mm high and 50mm deep. Price only £1. Order Ref: 7R1.

The other is Amplifier Ref. No. 109G. This is in a case size 80mm wide, 130mm high and 35mm deep. Price £1. Order Ref: 7R2.

HEAVY DUTY POT

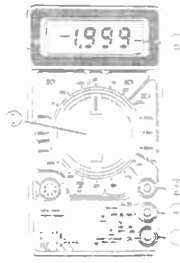
Rated at 25W, this is 20 ohm resistance so it could be just right for speed controlling a d.c. motor or device or to control the output of a high current amplifier. Price £1. Order Ref: 1/33L1.

STEPPER MOTOR

Made by Philips as specified for the wind-up torch in the Oct 100 Practical Electronics is still available, price £2. Order Ref: 2P457. Sorry, but the other item which Practical Electronics Shop Talk suggests we might supply is the IF Memory Back-up Capacitor, sorry we have no stocks of this item.

THIS MONTH'S SPECIAL

IT IS A DIGITAL MULTI-TESTER, complete with backrest to stand it and hands-free test prod holder. This tester measures d.c. volts up to 1,000 and a.c. volts up to 750; d.c. current up to 10A and resistance up to 2 megs. Also tests transistors and diodes and has an internal buzzer for continuity tests. Comes complete with test prods, battery and instructions. Price £6.99. Order Ref: 7P29.



YOUR CHANCE TO BUY SOME POPULAR LINES AT BARGAIN PRICES

250W WOOFER. Made by Challenger, this is 10in. 4 ohm, very high quality make. Our normal price £29, we are reducing to £20, which is almost a third off. Order Ref: 29P7L.

200W WOOFER. Again by Challenger, this is 8in. 4 ohm, our normal price £18 but it is reduced to £14 making it a terrific bargain. Order Ref: 18P81.

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100A TIME SWITCH. Ex-electricity board, this is extra useful because it has a mechanism to keep it going should there be a power failure, and although 100A it will operate quite happily on 5A. Regular price £10, now reduced to £8. Order Ref: 10P14L.

MOTORISED DISPLAY. This could control up to 120A of lighting or other equipment. The mains operated motor drives 12 x 10A microswitches, each of which can be set to come on at a different time, so giving running lights or other interesting displays. Regular price £10, reduced to £8. Order Ref: 1P191L.

BRUSH TYPE MAINS MOTOR. Probably ¼hp but being brush type it is easily speed controllable. Normal price £5, special offer price £4. Order Ref: 5P275L.

SOLAR KITS. To make an old fashioned gramophone which will operate in sunlight or under a light bulb. Normal price £7.50, reduced to £6. Order Ref: 7P16L.

A SIMILAR SOLAR KIT. This one makes a monoplane, again £6. Order Ref: 7P18L.

MOST USEFUL MAINS TRANSFORMER. This is a 12V-0-12V 35W rated, has mounting legs so can stand directly on base panel, price £2.50. Order Ref: 2.5P15.

PROJECT BOX BARGAIN. Colour beige and size approximately 250mm x 130mm wide and 50mm deep. Divides into 2 halves, held together by screws. It has ventilators in the top and bottom corners, but these are quite a decoration and give the box a pleasing look. Price £1. Order Ref: D201.

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ENGINEERS BENCH PANEL. This has 2 x 13A mains sockets which are switched and illuminated, thus saving you having to keep pulling out the plugs. Nicely cased, only £2. Order Ref: 2P461.

OVEN THERMOSTAT with knob calibrated so you can set it to cut out at any temperature up to 600 degrees F. Price £3. Order Ref: 3P229.

BUY ONE GET ONE FREE

ULTRASONIC MOVEMENT DETECTOR. Nicely cased, free standing, has internal alarm which can be silenced. Also has connections for external speaker or light. Price £10. Order Ref: 10P154.

CASED POWER SUPPLIES which, with a few small extra components and a bit of modifying, would give 12V at 10A. Originally £9.50 each, now 2 for £9.50. Order Ref: 9.5P4.

3-OCTAVE KEYBOARDS with piano size keys, brand new, previous price £9.50, now 2 for the price of one. Order Ref: 9.5P5.

RECHARGEABLE 12V JELLY ACID BATTERIES.

Yuasa 12V 2.3AH. These are 7in. long, 3in. high and 1½in. wide with robust terminals protruding through the top. Price £3.50. Order Ref: 3.5P11.

DITTO, but 12V 18AH. This is 7in. long, 7in. high and 3in. wide. Brand new with 12 months guarantee, price £12.50 or pack of 4 for £48, including VAT and carriage. Order Ref: 12.5P3.

Note - This battery will start a car and is ideal for golf trolleys, etc.

CHARGER for these batteries and other sealed lead acid batteries, £5. Order Ref: 5P269.

RECHARGEABLE NICAD BATTERIES. AA size, 25p each, which is a real bargain considering many firms charge as much as £2 each. These are in packs of 10, coupled together with an output lead so are a 12V unit but easily divideable into 2 x 6V or 10 x 1.2V. £2.50 per pack, 10 packs for £25 including carriage. Order Ref: 2.5P34.

FOR QUICK HOOK-UPS.

You can't beat leads with a croc clip each end. You can have a set of 10 leads, 2 each of 5 assorted colours with insulated crocodile clips on each end. Lead length 36cm, £2 per set. Order Ref: 2P459.



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Approximately 80mm x 55mm, front engraved 0-100. Price £1.50 each. Order Ref: 1/16R2.

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EVEN THINNER DRILLS. 12 that vary between 0.1 and 0.5mm. Price £1. Order Ref: 129.

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D.C. MOTOR WITH GEARBOX. Size 60mm long, 30mm diameter. Very powerful, operates off any voltage between 6 and 24 D.C. Speed at 6V is 200 rpm, speed controller available. Special price £3 each. Order Ref: 3P108.

MOST USEFUL POWER SUPPLY. Rated at 9V 1A, this plugs into a 13A socket, is really nicely boxed. £2. Order Ref: 2P733.

BT TELEPHONE EXTENSION WIRE. This is proper heavy duty cable for running around the skirting board when you want to make a permanent extension, 4 cores properly colour coded, 25m length. Only £1. Order Ref: 1067.

12V 8A DC POWER SUPPLY. Totally enclosed with its own cooling fan. Normal mains operation. Price £11. Order Ref: 11P6.

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BIG 12V TRANSFORMER. It is 55VA so that is over 4A which is normal working, intermittently it would be a much higher amperage. Beautiful transformer, well made and very well insulated, terminals are in a plastic frame so can't be accidentally touched. Price £3.50. Order Ref: 3.5P20.

RELAYS

We have thousands of relays of various sorts in stock, so if you need anything special give us a ring. A few new ones that have just arrived are special in that they are plug-in and come complete with a special base which enables you to check voltages of connections of it without having to go underneath. We have 6 different types with varying coil voltages and contact arrangements. All contacts are rated at 10A 250V AC.

Coil Voltage	Contacts	Price	Order Ref:
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24V DC	2-pole changeover	£1.50	FR12
24V DC	4-pole changeover	£2.00	FR13
240V AC	1-pole changeover	£1.50	FR14
240V AC	4-pole changeover	£2.00	FR15

Prices include base
NOT MUCH BIGGER THAN AN OXO CUBE. Another relay just arrived is extra small with a 12V coil and 6A changeover contacts. It is sealed so it can be mounted in any position or on a p.c.b. Price 75p each, 10 for £6 or 100 for £50. Order Ref: FR16.

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Circuits and Design



ELECTRONICS TEACH-IN No. 7 ANALOGUE AND DIGITAL ELECTRONICS COURSE

(published by *Everyday Practical Electronics*)
Alan Winstanley and Keith Dye B.Eng(Tech)AMIEE

This highly acclaimed *EPE Teach-In* series, which included the construction and use of the *Mini Lab* and *Micro Lab* test and development units, has been put together in book form.

An interesting and thorough tutorial series aimed specifically at the novice or complete beginner in electronics. The series is designed to support those undertaking either GCSE Electronics or GCE Advanced Levels, and starts with fundamental principles.

If you are taking electronics or technology at school or college, this book is for you. If you just want to learn the basics of electronics or technology you must make sure you see it. *Teach-In No. 7* will be invaluable if you are considering a career in electronics or even if you are already training in one. The *Mini Lab* and software enable the construction and testing of both demonstration and development circuits. These learning aids bring electronics to life in an enjoyable and interesting way; you will both see and hear the electron in action! The *Micro Lab* microprocessor add-on system will appeal to higher level students and those developing microprocessor projects.

160 pages

Order code T17

£3.95

ELECTRONICS PROJECTS USING ELECTRONICS WORKBENCH plus FREE CD-ROM

M. P. Horsey

This book offers a wide range of tested circuit modules which can be used as electronics projects, part of an electronics course, or as a hands-on way of getting better acquainted with Electronics Workbench. With circuits ranging from 'bulbs and batteries' to complex systems using integrated circuits, the projects will appeal to novices, students and practitioners alike.

Electronics Workbench is a highly versatile computer simulation package which enables the user to design, test and modify their circuits before building them, and to plan PCB layouts on-screen. All the circuits in the book are provided as runnable Electronic Workbench files on the enclosed CD-ROM, and a selection of 15 representative circuits can be explored using the free demo version of the application.

Contents: Some basic concepts; Projects with switches, LEDs, relays and diodes; Transistors; Power supplies; Op.amp projects; Further op.amp circuits; Logic gates; Real logic circuits; Logic gate multivibrators; The 555 timer; Flip-flops, counters and shift registers; Adders, comparators and multiplexers; Field effect transistors; Thyristors, triacs and diacs; Constructing your circuit; Index.

227 pages

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A BEGINNER'S GUIDE TO MODERN ELECTRONIC COMPONENTS R. A. Penfold

The purpose of this book is to provide practical information to help the reader sort out the bewildering array of components currently on offer. An advanced knowledge of the theory of electronics is not needed, and this book is not intended to be a course in electronic theory. The main aim is to explain the differences between components of the same basic type (e.g. carbon, carbon film, metal film, and wire-wound resistors) so that the right component for a given application can be selected. A wide range of components are included, with the emphasis firmly on those components that are used a great deal in projects for the home constructor.

170 pages

Order code BP285

£4.99

The books listed have been selected by *Everyday Practical Electronics* editorial staff as being of special interest to everyone involved in electronics and computing. They are supplied by mail order to your door. Full ordering details are given on the last book page.

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Computing & Robotics

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The book was written with the non-expert, busy person in mind. It explains the hardware that you need in order to run Windows 95 successfully, and how to install and optimize your system's resources. It presents an overview of the Windows 95 environment.

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170 pages

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Detailed building and programming instructions provided, including numerous step-by-step photographs.

288 pages - large format

Order code BP901

£12.99

INTRODUCTION TO MICROPROCESSORS

John Crisp

If you are, or soon will be, involved in the use of microprocessors, this practical introduction is essential reading. This book provides a thoroughly readable introduction to microprocessors, assuming no previous knowledge of the subject, nor a technical or mathematical background. It is suitable for students, technicians, engineers and hobbyists, and covers the full range of modern microprocessors.

After a thorough introduction to the subject, ideas are developed progressively in a well-structured format. All technical terms are carefully introduced and subjects which have proved difficult, for example 2's complement, are clearly explained. John Crisp covers the complete range of microprocessors from the popular 4-bit and 8-bit designs to today's super-fast 32-bit and 64-bit versions that power PCs and engine management systems etc.

Contents: The world changed in 1971; Microprocessors don't have ten fingers; More counting; Mathematical micros; It's all a matter of logic; Registers and memories; A microprocessor based system; A typical 8-bit microprocessor; Programming, High level languages; Micros are getting bigger and faster; The Pentium; The PowerPC; The Alpha 21164 microprocessor; Interfacing; Test equipment and fault finding.

222 pages

Order code NE31

£16.99

FREE
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PRACTICAL REMOTE CONTROL PROJECTS

Owen Bishop

Provides a wealth of circuits and circuit modules for use in remote control systems of all kinds; ultrasonic, infra-red, optical fibre, cable and radio. There are instructions for building fourteen novel and practical remote control projects. But this is not all, as each of these projects provides a model for building dozens of other related circuits by simply modifying parts of the design slightly to suit your own requirements. This book tells you how.

Also included are techniques for connecting a PC to a remote control system, the use of a microcontroller in remote control, as exemplified by the BASIC Stamp, and the application of ready-made type-approved 418MHz radio transmitter and receiver modules to remote control systems.

160 pages

Order code BP413

£5.99

DISCOVERING ELECTRONIC CLOCKS

W. D. Phillips

This is a whole book about designing and making electronic clocks. You start by connecting HIGH and LOW logic signals to logic gates. You find out about and then build and test bistables, crystal-controlled astables, counters, decoders and displays. All of these subsystems are carefully explained, with practical work supported by easy to follow prototype board layouts.

Full constructional details, including circuit diagrams and a printed circuit board pattern, are given for a digital electronic clock. The circuit for the First Clock is modified and developed to produce additional designs which include a Big Digit Clock, Binary Clock, Linear Clock, Andrew's Clock (with a semi-analogue display), and a Circles Clock. All of these designs are unusual and distinctive.

This is an ideal resource for project work in GCSE Design and Technology; Electronics Product, and for project work in AS-Level and A-Level Electronics and Technology.

194 pages, A4 spiral bound

Order code DEP1

£16.50

DOMESTIC SECURITY SYSTEMS

A. L. Brown

This book shows you how, with common sense and basic do-it-yourself skills, you can protect your home. It also gives tips and ideas which will help you to maintain and improve your home security, even if you already have an alarm. Every circuit in this book is clearly described and illustrated, and contains components that are easy to source. Advice and guidance are based on the real experience of the author who is an alarm installer, and the designs themselves have been rigorously put to use on some of the most crime-ridden streets in the world.

The designs include all elements, including sensors, -detectors, alarms, controls, lights, video and door entry systems. Chapters cover installation, testing, maintenance and upgrading.

192 pages

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F. A. Wilson, C.G.I.A., C.Eng., F.I.E.E., F.I.E.R.E.,
F.B.I.M.

Bridges the gap between complicated technical theory, and "cut-and-try" methods which may bring success in design but leave the experimenter unfulfilled. A strong practical bias - tedious and higher mathematics have been avoided where possible and many tables have been included.

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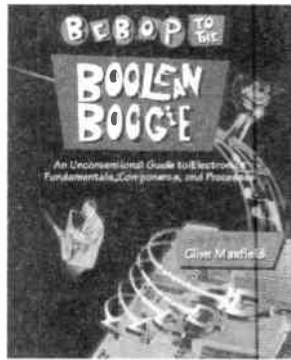
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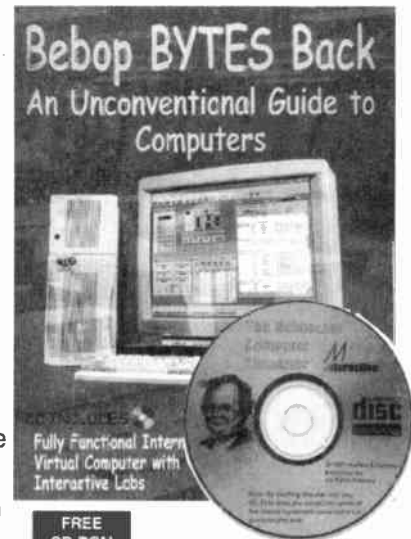
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There is a 'blow-by-blow' guide to the use of EASY-PC Professional XM (a schematic drawing and printed circuit board design computer package). The guide also conducts the reader through logic circuit simulation using Pulsar software. Chapters on p.c.b. physics and p.c.b. production techniques make the book unique, and with its host of project ideas make it an ideal companion for the integrative assignment and common skills components required by BTEC and the key skills demanded by GNVQ. The principal aim of the book is to provide a straightforward approach to the understanding of digital electronics.

Those who prefer the 'Teach-In' approach or would rather experiment with some simple circuits should find the book's final chapters on printed circuit board production and project ideas especially useful.

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Ian R. Sinclair

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R. A. Penfold

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Since *Foundations of Wireless* was first published over 60 years ago, it has helped many thousands of readers to become familiar with the principles of radio and electronics. The original author Sowerby was succeeded by Scroggie in the 1940s, whose name became synonymous with this classic primer for practitioners and students alike. Stan Amos, one of the fathers of modern electronics and the author of many well-known books in the area, took over the revision of this book in the 1980s and it is he, with his son, who have produced this latest version.

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Ian Sinclair

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R. A. Penfold

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Geoff Phillips

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The book covers: Basics – Voltage, current and resistance; Capacitance, inductance and impedance; Diodes and transistors; Op-amps and negative feedback; Fault finding – Analogue fault finding, Digital fault finding; Memory; Binary and hexadecimal; Addressing; Discrete logic; Microprocessor action; I/O control; CRT control; Dynamic RAM; Fault finding digital systems; Dual trace oscilloscope; IC replacement.

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R. A. Penfold

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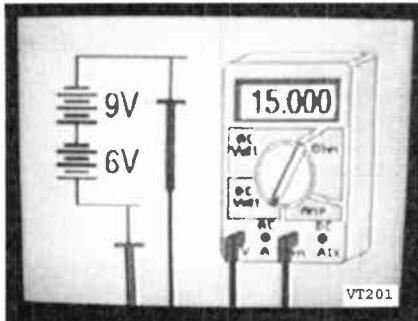
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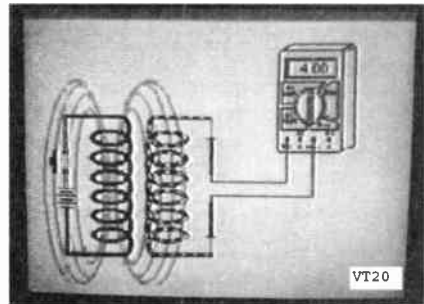
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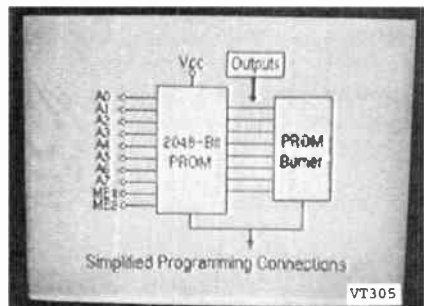
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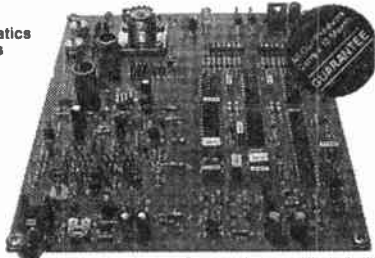
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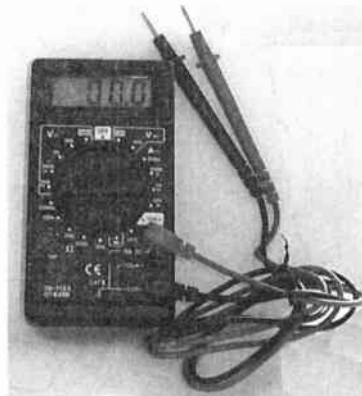
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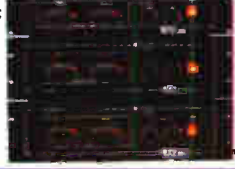
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